# CS101 Computer Programming and Utilization

Milind Sohoni

May 4, 2006

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#### 1 Memory in CAL-Programs

- The Quadratic polynomial
- Computing Slope

#### The TEST instruction

- Motivation: The Fibonacci Problem
- Solution: The Fibonacci Problem
- The log function

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# In Summary

#### The Programmer

- Writes a CAL-program by assuming a typical input.
- Writes where typical inputs are to be replaced by user inputs.
- Stores/writes and transmits.

# 10 % substitute input here \* 8 DIV 5 + 32 = % see output in display

#### The Bum

- Receives the program.
- Substitutes his inputs.
- Runs the program on his calculator line-by-line in that order.



#### Another Problem

#### Quadratic

Write a CAL-program to solve the quadratic  $Ax^2 + Bx + C = 0$ .

We solve a particular case and annotate the program in the right places. We consider  $x^2 + 3x + 2$ , thus A = 1, B = 3, C = 2.

- Compute  $\Delta = \sqrt{B^2 4AC}$ .
- Now calculate the two roots using the expressionss

$$roots = rac{-B+\Delta}{2A}$$
 and  $rac{B+\Delta}{2A}$ 

3 % STO	substitute B here	anno placo thus	otate es. V A =	the /e 1,
RCL		٩	Com	pu
* R.CI.		۲	Now	ca
-			RCL	
4			-	
*			3	%
1 %	substitute A here		=	
*			DIV	
2 %	substitute C here		2	
=			DIV	
SQRT			1	%
STO	% discriminant stored		=	%

We solve a particular case and annotate the program in the right places. We consider  $x^2 + 3x + 2$ , thus A = 1, B = 3, C = 2.

- Compute  $\sqrt{B^2 4AC}$ .
- Now calculate the two roots:

- 3 % substitute B here

- % substitute A here
- % read root 1 here

# Clunky

- We see that there are repeated insertions of *A*, *B*, *C*. This is clumsy.
- We allow our calculator to have more than 1 memory register.
- STO 5 will mean store contents of visible register in memory location 5.
- RCL 5 will mean move contents of memory location 5 to the visible register.

Thus the set of instructions will now be:

- +,-,=,\*,DIV
- AC
- numbers
- STO 1, STO 2 ,...,STO 10
- RCL 1, RCL 2 ,...,RCL 10

Programs in this language will be called MCAL-programs.

#### **Final Code**

1		%	subs. A here	RCL	4			
STO	1	%	A is in M1	-				
3		%	subs B here	RCL	2			
STO	2	%	B in M2	=				
2		%	С	DIV				
STO	3	%	in M3	2				
RCL	2			DIV				
*				RCL	1			
RCL	2			=				
-				STO	5	% fi	irst roo	ot
4								
*					г	<b>k</b>	(EY	1
RCL	1					INIT	A	
*						M2	В	
RCL	3					M3	С	
=						M5	root 1	
SQRT						M6	root 2	
STO	4	%	disc in M4		-			

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#### Another example: Computing Slope

We are given a point (x, y). We wish to compute sin  $\alpha$  where  $\alpha$  is the angle between the X-axis and the line joining the origin.

$$\sin \alpha = \frac{y}{\sqrt{x^2 + y^2}}$$

• first compute  $\sqrt{x^2 + y^2}$ .

• next compute  $\frac{y}{\sqrt{x^2+y^2}}$ .



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We pick typical x, y say (3, 4).

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3 % this is x STO 1 % 4 % this is y STO 2 % \* RCL 2 + RCL 1 \* RCL 1 = SQRT STO 4 % the denominator RCL 2 DIV RCL 4 = STO 3 % answer stored in M3

Here:

M1	х	input
M2	У	input
M3	sin alpha	output

We pick typical x, y say (3, 4).

3		%	this	is	х	
STO	1	%				
4		%	this	is	У	
STO	2	%				
*						
RCL	2					
+						
RCL	1					
*						
RCL	1					
=						
SQRI	Γ					
STO	4	%	the d	len	ominato	or
RCL	2					
DIV						
RCL	4					
=						
STO	3	%	answe	er s	stored	in

Here:

M1	х	input
M2	У	input
M3	sin alpha	output

-						
5	11	m	m	2	r1	1
)	u			а	1 \	I

- Enhance calculator with more memory.
- Use STO 5, RCL 5 as additional instructions.
- Groups registers as Input, Output and temporary.

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Let  $a_0 = 1$  and  $a_1 = 1$ , and  $a_n$  be given by the following recursive definition:

 $a_n = a_{n-1} + a_{n-2}$ 

Write an MCAL-program to compute  $a_n$ .

Thus:

<i>a</i> 0	1
$a_1$	1
<i>a</i> <sub>2</sub>	2
<b>a</b> 3	3
a <sub>4</sub>	5
<i>a</i> 5	8
•••	••••

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Write an MCAL-program to compute  $a_n$ .

```
%value of n
4
1
STO 1 % will store a(n-1)
STO 2 % will store a(n-2)
RCL 1 % beginning of loop
+
RCL 2
=
STO 3 % temporary
RCL 1
STO 2
RCL 3
STO 1 % end of loop
```

#### Our variable storage is as follows:

M1	$a_{n-1}$
M2	<i>a</i> <sub><i>n</i>-2</sub>

(a) < ((a) <

Let  $a_0 = 1$  and  $a_1 = 1$ , and  $a_n$  be given by the following recursive definition:

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Write an MCAL-program to compute  $a_n$ .

```
%value of n
4
1
                               Our variable storage is as follows:
STO 1 % will store a(n-1)
STO 2 % will store a(n-2)
                                             M1
                                                  a_{n-1}
                                             M2
                                                  a_{n-2}
RCL 1 % beginning of loop
+
RCL 2
=
                               Next.
STO 3 % temporary
                                 • There is an initialization part.
RCL 1
                                 • and then a loop.
STO 2
RCL 3
STO 1 % end of loop
                                            (a)
```

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```
%value of n
4
1
STO 1 % will store a(n-1)
STO 2 % will store a(n-2)
RCL 1 % beginning of loop
+
RCL 2
=
STO 3 % temporary
RCL 1
STO 2
RCL 3
STO 1 % end of loop
```

Let us analyse the loop. Assume that M1 has stored  $a_{n-1}$  and M2 has stored  $a_{n-2}$ . We plot M1,M2,M3,D, the display register.

```
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RCL 1
STO 2
RCL 3
STO 1 % end of loop
```

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	M1	M2	M3	D
RCL 1	$a_{n-1}$	<i>a</i> <sub>n-2</sub>	х	$a_{n-1}$
+	$a_{n-1}$	<i>a</i> <sub>n-2</sub>	х	$a_{n-1}$
RCL 2	$a_{n-1}$	<i>a</i> <sub>n-2</sub>	х	<i>a</i> <sub>n-2</sub>
=	$a_{n-1}$	<i>a</i> <sub>n-2</sub>	x	an
STO 3	$a_{n-1}$	<i>a</i> <sub><i>n</i>-2</sub>	an	an
RCL 1	$a_{n-1}$	<i>a</i> <sub>n-2</sub>	an	$a_{n-1}$
STO 2	$a_{n-1}$	$a_{n-1}$	an	$a_{n-1}$
RCL 3	$a_{n-1}$	$a_{n-1}$	an	an
STO 1	an	$a_{n-1}$	an	an

# The Solution and ...

We see that the code consists of a preamble and a loop. The preamble sets up the recurrence relation and the loop executes it. The *loop* may be repeated as many times as required. Thus if n = 5, then  $a_0, a_1$  are inputed raw, and the first loop calculates  $a_2$ . Thus the loop needs to be executed n - 1 times to compute  $a_n$ .



# The Solution and ...

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#### The Problem

But this NOT what we want. The code changes with the input *n*, while we want it to be fixed!

# What do we need?

Let us recall the BUM model of running programs. which goes through the following steps:

- We write a program and give it to BUM.
- BUM executes the first each instruction on the page sequentially.

Thus, in other words, the BUM may as well eat up each line since he will never use it again. From our earlier loop program, we see that there are some set of instructions which need to be executed again and again. This is the key observation.

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- Let us devise a slightly cleverer **DUMMY** who retains the paper on which the program is written.
- Let us add an instruction which alerts the DUMMY.

#### The new format

Our program will now have a line number. Thus each line has a number and a CALC-instruction. For example:

1	10	% substitute input here
2	*	
3	8	
4	DIV	
5	5	
6	+	
7	32	
8	=	% see output in display

## The new format

Our program will now have a line number. Thus each line has a number and a CALC-instruction. For example:

1	10	% substitute input here
2	*	
3	8	
4	DIV	
5	5	
6	+	
7	32	
8	=	% see output in display

We add a new instruction:

#### TEST nos

This is executed by the DUMMY as follows.

- On encountering the TEST instruction, the DUMMY scans the DISPLAY.
- If the Display holds a negative or zero value, the DUMMY moves to the next instruction.
- If, however, the Display value is positive, the DUMMY moves to line number nos.

		initialization
1	5	this is <i>n</i>
	STO5	
	_	
	1	
	=	
	STO4	the counter
	1	$a_{n-1}$ and $a_{n-2}$
	STO1	
9	STO2	

This completes the initialization. At the end of this phase, we have:

M1	1
M2	1
M4	4 = (n - 1)
M5	5=(n)

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		initialization
1	5	this is <i>n</i>
	<i>STO</i> 5	
	_	
	1	
	=	
	STO4	the counter
	1	$a_{n-1}$ and $a_{n-2}$
	STO1	
9	STO2	

This completes the initialization. At the end of this phase, we have:

M1	1
M2	1
M4	4 = (n - 1)
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10	RCL1	the loop
	+	
	RCL2	
	=	
	STO3	temporary
	RCL1	
	STO2	
	RCL3	
18	STO1	
19	RCL4	the termination
	—	
	1	
	=	
	STO4	counter -1
24	TEST 10	
25	STOP	

10	RCL1	the loop
	+	
	RCL2	
	=	
	STO3	temporary
	RCL1	
	STO2	
	RCL3	
18	STO1	
19	RCL4	the termination
	_	
	1	
	=	
	STO4	counter -1
24	TEST 10	
25	STOP	

We observe the values of each register at various times:

line	10	18	24
M1	1	2	2
M2	1	1	1
M4	4	4	3

10	DCI1	the lear
10	RCLI	the loop
	+	
	RCL2	
	=	
	STO3	temporary
	RCL1	
	STO2	
	RCL3	
18	STO1	
19	RCL4	the termination
	_	
	1	
	=	
	STO4	counter -1
24	TEST 10	
25	STOP	

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display is positive, so next instruction is 10.

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10	D CL 1	
10	RCL1	the loop
	+	
	RCL2	
	=	
	STO3	temporary
	RCL1	
	STO2	
	RCL3	
18	STO1	
19	RCL4	the termination
	_	
	1	
	=	
	STO4	counter -1
24	TEST 10	
25	STOP	

We observe the values of each register at various times:

line	10	18	24
M1	1	2	2
M2	1	1	1
M4	4	4	3

display is positive, so next instruction is 10.

line	10	18	24
M1	2	3	3
M2	1	2	2
M4	3	3	2

10	RCL1	the loop
	+	
	RCL2	
	=	
	STO3	temporary
	RCL1	
	STO2	
	RCL3	
18	STO1	
19	RCL4	the termination
	—	
	1	
	=	
	STO4	counter -1
24	TEST 10	
25	STOP	

We observe the values of each register at various times:

line	10	18	24
M1	1	2	2
M2	1	1	1
M4	4	4	3

display is positive, so next instruction is 10.

line	10	18	24
M1	2	3	3
M2	1	2	2
M4	3	3	2

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10	RCL1	the loop
	+	
	RCL2	
	=	
	STO3	temporary
	RCL1	
	STO2	
	RCL3	
18	STO1	
19	RCL4	the termination
	—	
	1	
	=	
	STO4	counter -1
24	TEST 10	
25	STOP	

line	10	18	24
M1	3	5	5
M2	2	3	3
M4	2	2	1

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10	RCL1	the loop
	+	
	RCL2	
	=	
	STO3	temporary
	RCL1	
	STO2	
	RCL3	
18	STO1	
19	RCL4	the termination
	_	
	1	
	=	
	STO4	counter -1
24	TEST 10	
25	STOP	

line	10	18	24
M1	3	5	5
M2	2	3	3
M4	2	2	1

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display is positive, so next instruction is 10.

10	RCL1	the loop
	+	
	RCL2	
	=	
	STO3	temporary
	RCL1	
	STO2	
	RCL3	
18	STO1	
19	RCL4	the termination
	_	
	1	
	=	
	STO4	counter -1
24	TEST 10	
25	STOP	

line	10	18	24
M1	3	5	5
M2	2	3	3
M4	2	2	1

display is positive, so next instruction is 10.

line	10	18	24
M1	5	8	8
M2	3	5	5
M4	1	1	0

10	RCL1	the loop
	+	
	RCL2	
	=	
	STO3	temporary
	RCL1	
	STO2	
	RCL3	
18	STO1	
19	RCL4	the termination
	_	
	1	
	=	
	STO4	counter -1
24	TEST 10	
25	STOP	

line	10	18	24
M1	3	5	5
M2	2	3	3
M4	2	2	1

display is positive, so next instruction is 10.

line	10	18	24
M1	5	8	8
M2	3	5	5
M4	1	1	0

display is zero, so next instruction is 26.

10	RCL1	the loop
	+	
	RCL2	
	=	
	STO3	temporary
	RCL1	
	STO2	
	RCL3	
18	STO1	
19	RCL4	the termination
	_	
	1	
	=	
	STO4	counter -1
24	TEST 10	
25	STOP	

line	10	18	24
M1	3	5	5
M2	2	3	3
M4	2	2	1

display is positive, so next instruction is 10.

line	10	18	24
M1	5	8	8
M2	3	5	5
M4	1	1	0

display is zero, so next instruction is 26. computation stops.

#### Another Problem

#### The Log problem

Given a positive integer *n*, the largest *k* so that  $10^k \le n$ .

What is the strategy?

- Maintain the input *n* in one register.
- Maintain k and  $2^k$  in separate registers.
- Compute  $n + 1 2^k$ . If positive, loop back.

M1	n+1		
M2	k		
M3	2 <sup>k</sup>		

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### Another Problem

The Log problem			nos	RCL2			
Give arg	en a est i	positive k so that	integer $n$ , the $10^k \leq n$ .			+ 1	
_	_					= <i>STO</i> 2	incremented $k$
	1	155	<i>n</i> here			RCL3	
	-	+	in here			* 10	
		1				=	· · · · · ·
		STO1	stored 156!			RCL1	incremented 2*
		0 <i>STO</i> 2	stores k			_ RCI3	
		1 <i>STO</i> 3	stores 2 <sup>k</sup>			= TEST nos	This is $n+1-2^k$
		r.				STOP	