

Understanding GCC: The Essential Abstractions in GCC

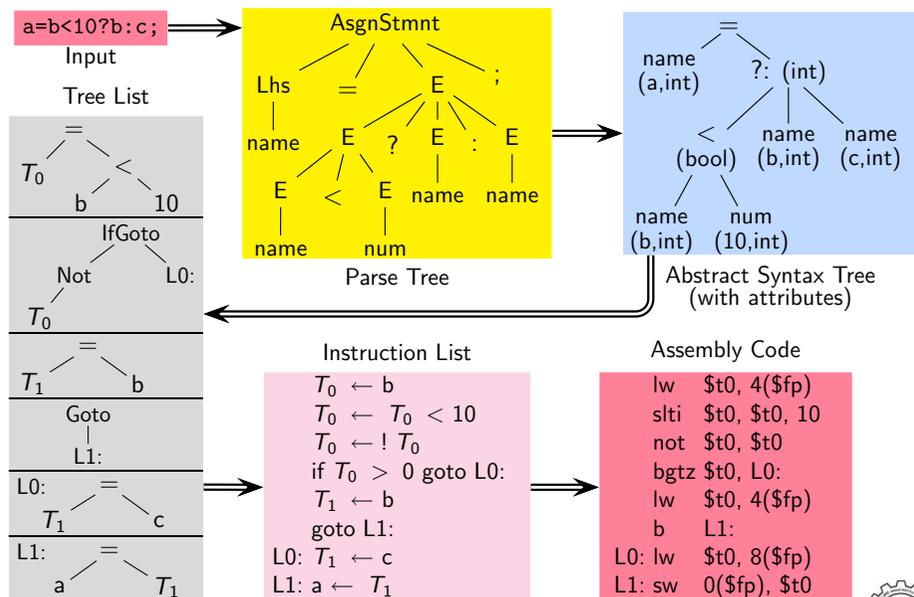
GCC Resource Center
(www.cse.iitb.ac.in/grc)

Department of Computer Science and Engineering,
Indian Institute of Technology, Bombay



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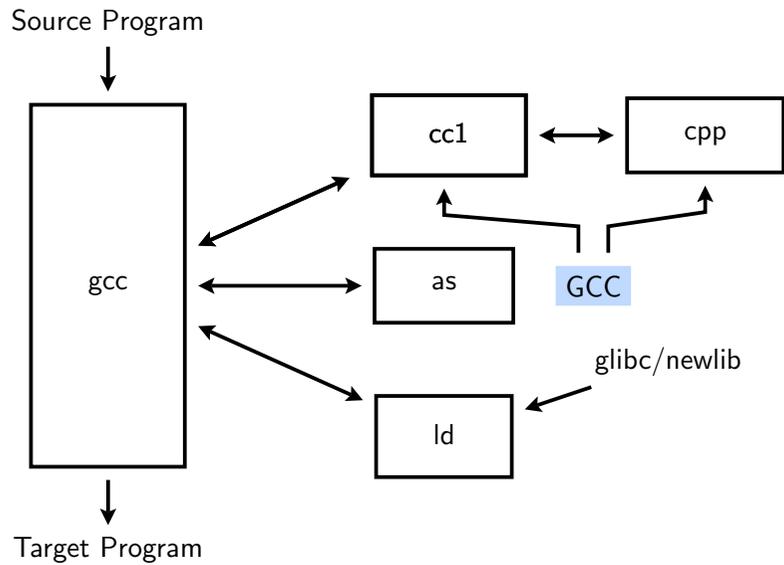
Translation Sequence in Our Toy Compiler



Translation Sequence in Our Toy Compiler

Notes

The GNU Tool Chain

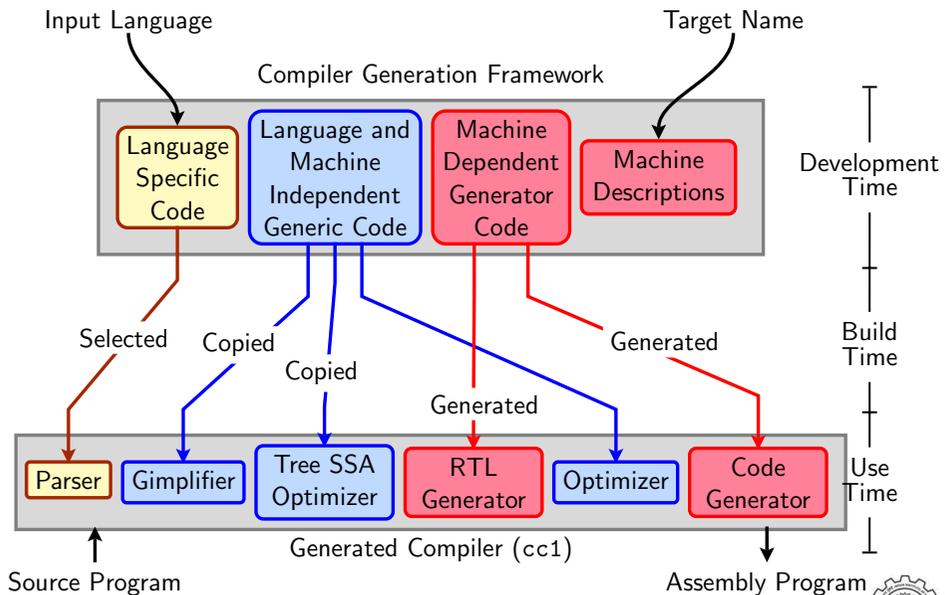


The GNU Tool Chain

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The Architecture of GCC

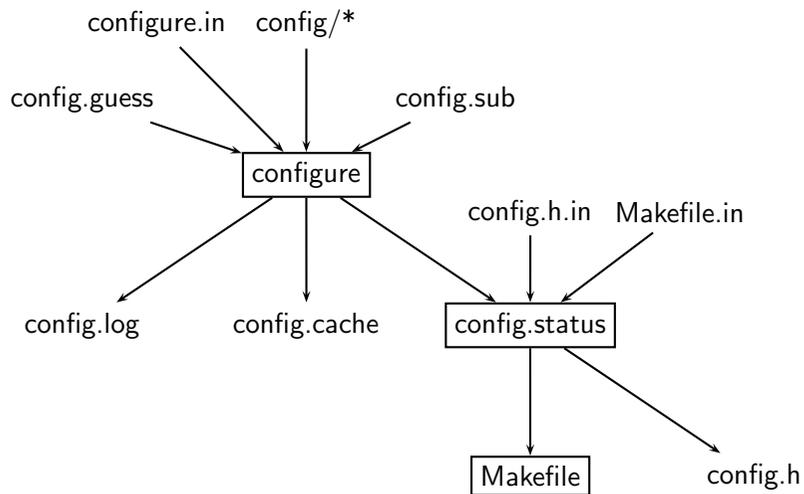


The Architecture of GCC

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Configuring GCC

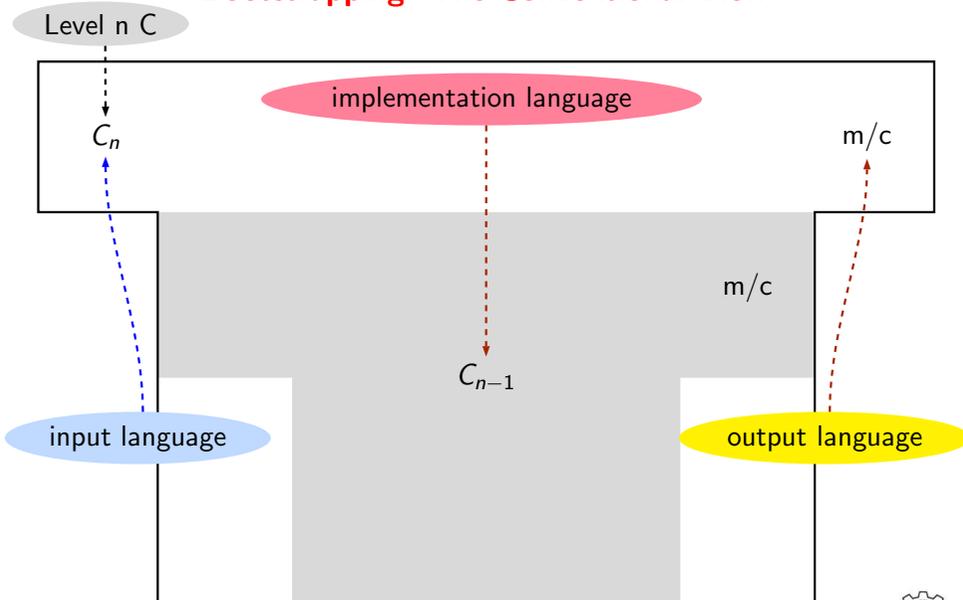


Configuring GCC

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Bootstrapping: The Conventional View

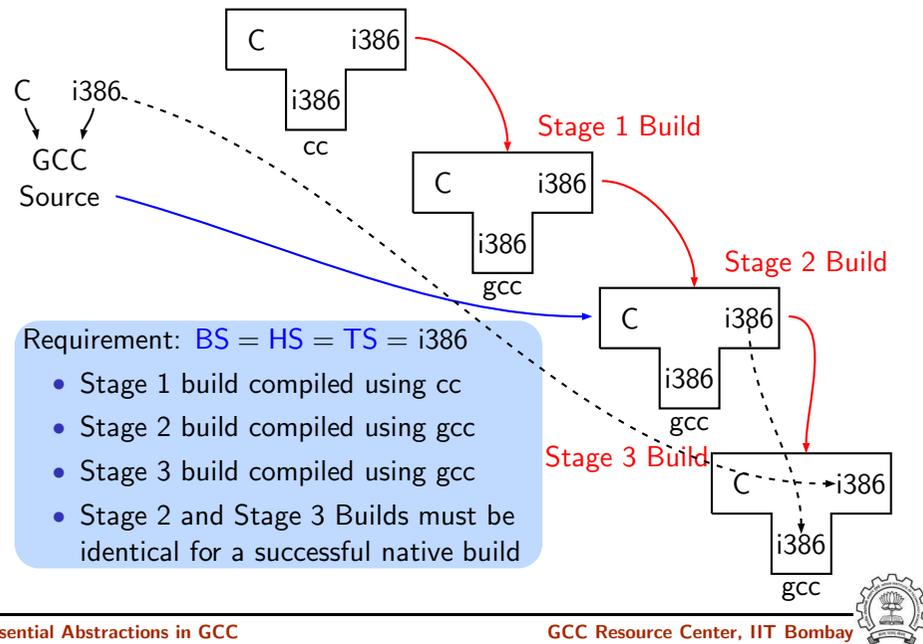


Bootstrapping: The Conventional View

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A Native Build on i386



A Native Build on i386

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Build for a Given Machine

This is what actually happens!

- Generation
 - ▶ Generator sources ($\$(SOURCE_D)/gcc/gen*.c$) are read and generator executables are created in $\$(BUILD)/gcc/build$
 - ▶ MD files are read by the generator executables and back end source code is generated in $\$(BUILD)/gcc$
- Compilation

Other source files are read from $\$(SOURCE_D)$ and executables created in corresponding subdirectories of $\$(BUILD)$
- Installation

Created executables and libraries are copied in $\$(INSTALL)$

genattr
gencheck
genconditions
genconstants
genflags
genopinit
genpreds
genattrtab
genchecksum
gencondmd
genemit
gengenrtl
genmddeps
genoutput
genrecog
genautomata
gencodes
genconfig
genextract
gengtype
genmodes
genpeep

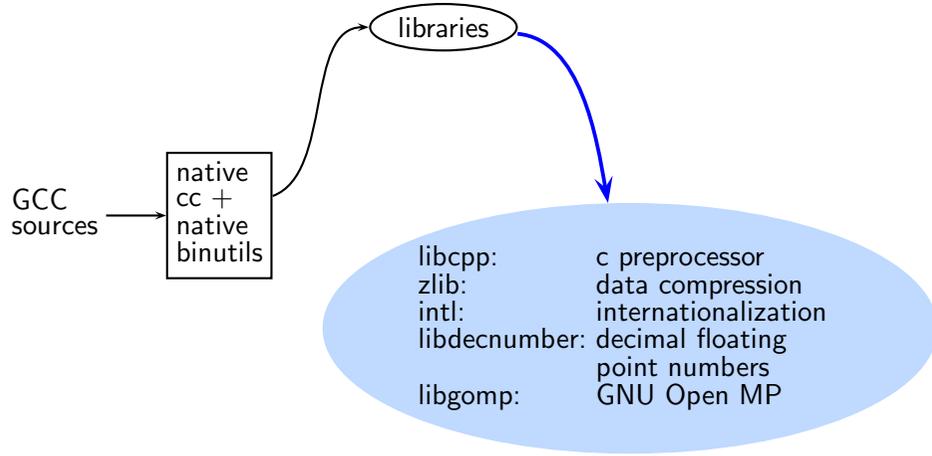


Build for a Given Machine

Notes



More Details of an Actual Stage 1 Build for C

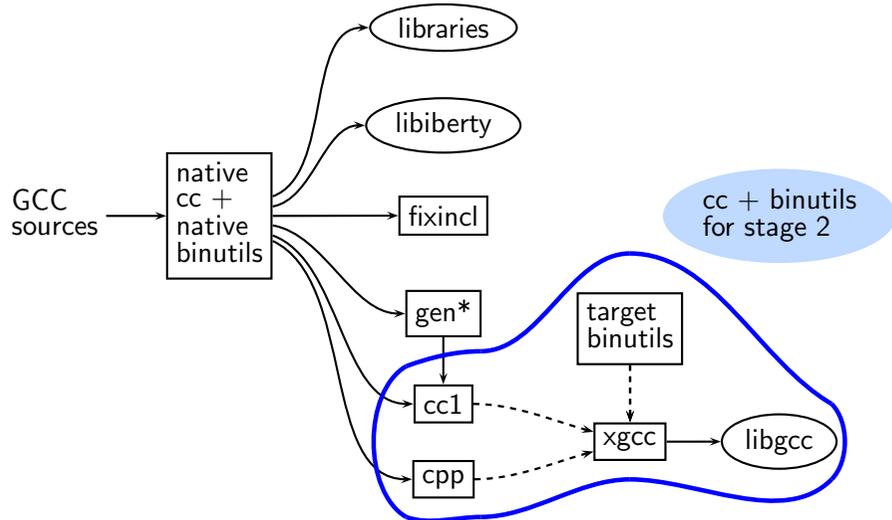


More Details of an Actual Stage 1 Build for C

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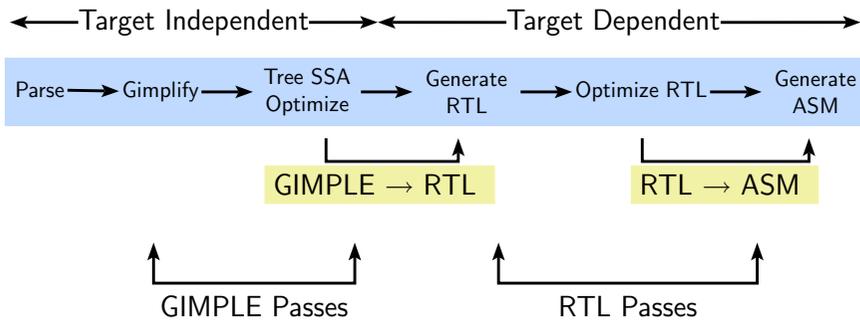
More Details of an Actual Stage 1 Build for C

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Basic Transformations in GCC

Transformation from a language to a *different* language



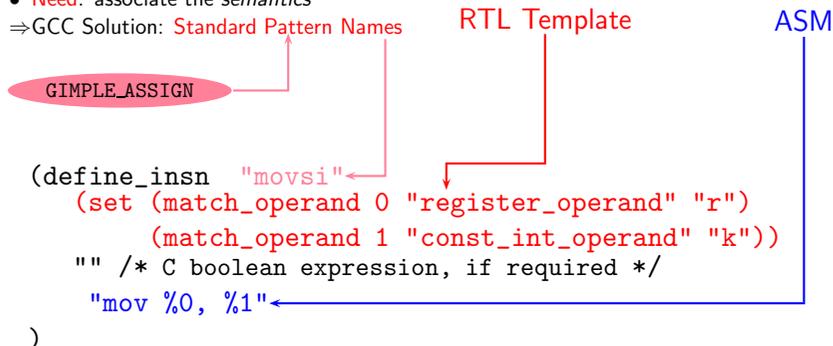
Instruction Specification and Translation: A Recap



- GIMPLE: target independent
- RTL: target dependent

• **Need:** associate the *semantics*

⇒GCC Solution: **Standard Pattern Names**



Basic Transformations in GCC

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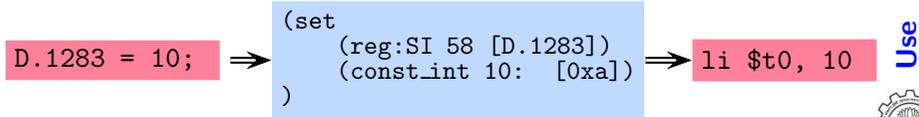
Instruction Specification and Translation: A Recap

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Translation Sequence in GCC

```
(define_insn
  "movsi"
  (set
    (match_operand 0 "register_operand" "r")
    (match_operand 1 "const_int_operand" "k")
  )
  "" /* C boolean expression, if required */
  "li %0, %1"
)
```

Development



Use

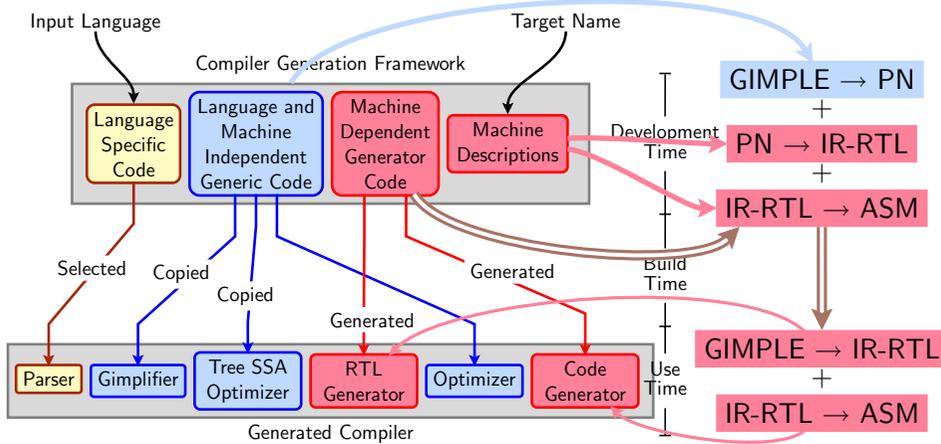


Translation Sequence in GCC

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Retargetability Mechanism of GCC

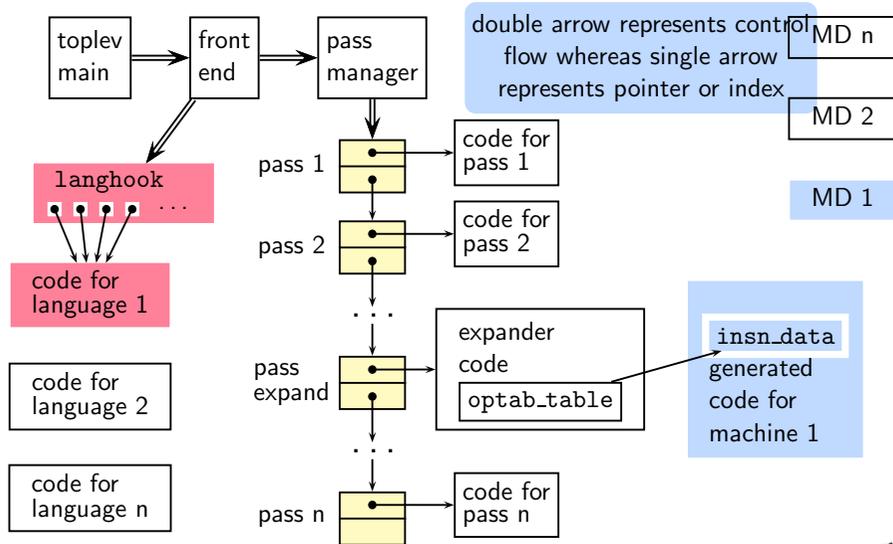


Retargetability Mechanism of GCC

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Plugin Structure in cc1

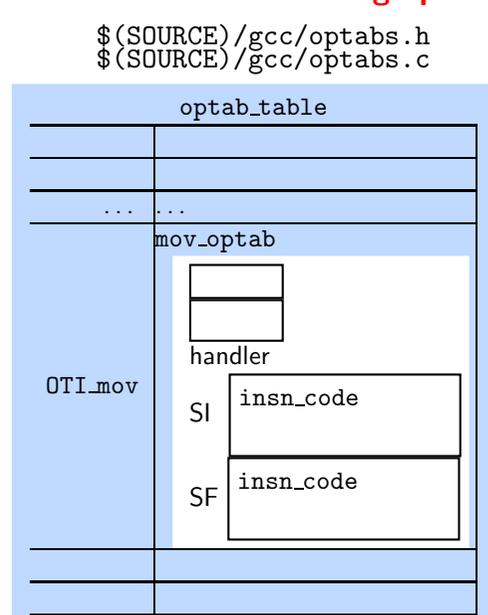


Plugin Structure in cc1

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Hooking up Back End Details



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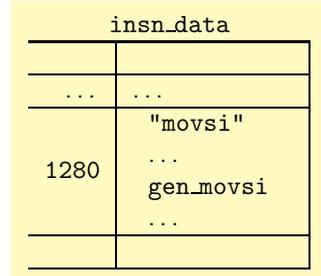
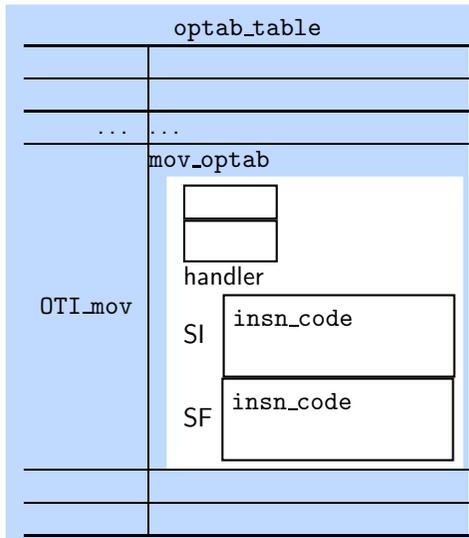
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Hooking up Back End Details

\$(SOURCE)/gcc/optabs.h
\$(SOURCE)/gcc/optabs.c

\$(BUILD)/gcc/insn-output.c



\$(BUILD)/gcc/insn-codes.h
CODE_FOR_movsi=1280
CODE_FOR_movsf=CODE_FOR_nothing



Hooking up Back End Details

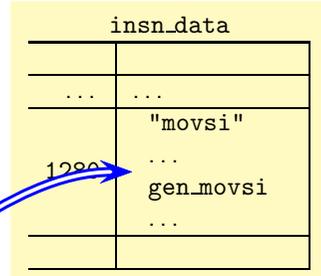
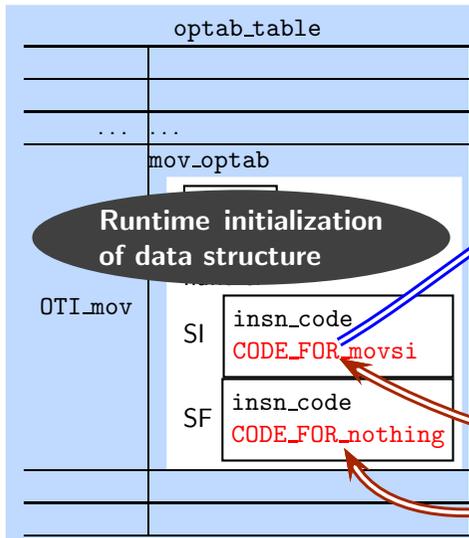
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\$(BUILD)/gcc/insn-codes.h
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\$(BUILD)/gcc/insn-opinit.c



Hooking up Back End Details

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