## Workshop on Essential Abstractions in GCC

## Introduction and Opening Remarks

GCC Resource Center (www.cse.iitb.ac.in/grc)

Department of Computer Science and Engineering, Indian Institute of Technology, Bombay



30 June 2011

Part 1

About GCC Resource Center

Outline

- About GCC Resource Center
- Workshop Plan

Essential Abstractions in GCC GCC Resource Center, IIT Bombay

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 Introduction: About GCC Resource Center
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 Objectives of GCC Resource Center
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## $1. \ \mbox{To support the open source movement}$

Providing training and technical know-how of the GCC framework to academia and industry.

2. To include better technologies in GCC

Whole program optimization, Optimizer generation, Tree tiling based instruction selection.

3. To facilitate easier and better quality deployments/enhancements of GCC

Restructuring GCC and devising methodologies for systematic construction of machine descriptions in GCC.

4. To bridge the gap between academic research and practical implementation

Designing suitable abstractions of GCC architecture



Introduction: About GCC Resource Center

# Broad Research Goals of GCC Resource Center

- Program Analysis and Optimization
- Translation Validation
- Retargetable compilation
- Parallelization and Vectorization for SIMD and MIMD Architectures

General explorations applied in the context of GCC

## • Using GCC as a means

- Adding new optimizations to GCC
- Adding flow and context sensitive analyses to GCC (In particular, pointer analysis)
- Translation validation of GCC
- Using GCC as an end in itself
  - Changing the retargetability mechanism of GCC
  - Cleaning up the machine descriptions of GCC
  - Systematic construction of machine descriptions
  - Facilitating optimizer generation in GCC



Essential Abstractions in GCC

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	GRC Training Programs	

Title	Target	Objectives	Mode	Duration
Workshop on Essential Abstractions in GCC	People interested in deploying or enhancing GCC	Explaining the essential abstractions in GCC to ensure a quick ramp up into GCC Internals	Lectures, demonstrations, and practicals (experiements and assignments)	Three days
Tutorial on Essential Abstractions in GCC	People interested in knowing about issues in deploying or enhancing GCC	Explaining the essential abstractions in GCC to ensure a quick ramp up into GCC Internals	Lectures and demonstrations	One day
Workshop on Compiler Construction with Introduction to GCC	College teachers	Explaining the theory and practice of compiler contruction and illustrating them with the help of GCC	Lectures, demonstrations, and practicals (experiements and assignments)	Seven days
Tutorial on Demystifying GCC Compilation	Students	Explaining the translation sequence of GCC through gray box probing (i.e. by examining the dumps produced by GCC)	Lectures and demonstrations	Half day



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**Essential Abstractions in GCC** 

#### Introduction: About GCC Resource Center GRC Training Programs

Title	Target	Objectives	Mode	Duration
Workshop on Essential Abst 3, 4 GCC IIT E	People interested 4, and 5 Jul '09 3ombay, Mumbai	Explaining the essential obstractions in CO e a qui GCC Inte. IIT Bo	Lectures. 5-8 Jul '10 ombay, Mumbai	Three days
Tutorial on	People interested	Explaining the essential	Lectures and	One day
Essential (mod Abst. AC GCC	ified version) 9 Ja M PPoPP, Banglo enhancing GCC	n '10 stions in GC ick i CGO	2 Apr '11 , Chamonix, Fran	ce
Workshop on	College teachers	Explaining the theory and	Lectures,	Seven
Compiler Construction with Introduction T Bombay, Mumbai ating them with the Source of				
-20 Jan '10	Students	Explaining the translation	25 Apr '10 ad	Half day
I Cummins	20 Feb '10 06 M	ar '10 uc 27 Mar '10 ugh	Punjabi tic 13 S	ep '10
College	IIITDM, SGGS	SIET, © RSCoE, by	University II	ТМ
Pune	Jabalpur Nar	nded <sup>ni</sup> Pune	Patiala Ch	ennai
				5/1000

**Essential Abstractions in GCC** 

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Introduction: About GCC Resource Center

## **GRC Training Programs**

CS 715: The Design and Implementation of GNU Compiler Generation Framework

- 6 credits semester long course for M.Tech. (CSE) students at IIT Bombay
- Significant component of experimentation with GCC
- Introduced in 2008-2009

Part 2

# Workshop Plan



Introduction: Workshop Plan 8/20 **Philosophy and Pedagogy** 

Twin goals of this workshop:

• Learning how to learn GCC

Our focus will be on

- giving you some core information
- showing you how to discover more information
- Striking a balance between theory and practice

Our focus will be on showing you how to

- discover concepts in a large code base and build abstractions
- take concepts and update a large code base
- relate the class room concepts of complilers to an industry strength compiler



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• Version: GCC 4.6.0

• Targets: i386, spim (mips simulator)

• Language: C

**Our Canvas** 

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## **Philosophy and Pedogogy**

#### • We will

- Explain configuration and building of GCC
- Explain essential abstractions related to compilation
   The key intermediate representations and their manipulations
- > Explain essential abstractions related to program analysis in GCC
- Explain essential abstractions related to generation of a compiler The machine descriptions and their influence on compilation

#### • You will

Essential Abstractions in GCC

- Build and run GCC
- Examine various IR dumps produced by GCC
- Add passes to GCC
- Add a new machine description and systematically enhance it



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Takeaways from this Workshop

- A programmer will get a better compiler
- A compiler professional will be able to deploy and enhance GCC much more easily.
- A compiler researcher will be able to use GCC for research much better.
- A compiler teacher will be able to strike a better balance between theory and practice.
- A compiler student will be exposed to issues in real compilers.

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# Day 1 Schedule (Thursday 30 June 2011)

09:45 to 10:00	Introductory remarks, Workshop plan	Lecture
10:00 to 10:30	An overview of compilation and GCC	Lecture
10:30 to 11:00	An external view of GCC	Lecture
11:00 to 11:30	Tea break	
11:30 to 12:15	First level gray box probing of GCC	Lecture
11:30 to 12:15	Gray box probing for machine	Lecture
	independent optimizations	
13:00 to 14:00	Lunch	
14:00 to 15:30	Gray box probing of GCC	Lab
15:30 to 15:45	Tea break	
15:45 to 16:15	Gray box probing of GCC	Lab
16:15 to 17:15	Configuration and building	Lecture
17:15	High Tea	
Optional	ctags, cscope, ddd, shell, make,	Demo
	screen, patch files	
20:30	Dinner	
Participants can continue to do the lab work until dinner		



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# Day 2 Schedule (Friday 1 July 2011)

09:30 to 11:00	Module Binding Mechanisms in GCC Lecture	
11:00 to 11:30	Tea break	
11:30 to 13:00	Adding Passes to GCC: Manipulating	Lecture
	GIMPLE and RTL IRs	
13:00 to 14:00	Lunch	
14:00 to 15:30	Adding GIMPLE interprocedural and	Lab
	intraprocedural passes	
15:30 to 15:45	Tea break	
15:45 to 17:15	Adding GIMPLE interprocedural and	Lab
	intraprocedural passes	
17:15	High Tea	
20:30	Dinner	
Participants can continue to do the lab work until dinner		



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Day 4 Schedule (Sunday 3 July 2011)

09:30 to 10:15	Introduction to Parallelization and	Lecture
	Vectorization	
10:15 to 11:00	Parallelization and Vectorization in GCC	Lecture
11:00 to 11:30	Tea break	
11:30 to 13:00	Parallelization and Vectorization in GCC	Lecture
13:00 to 14:00	Lunch	
14:00 to 15:30	Parallelization and Vectorization in GCC	Lab
15:30 to 15:45	Tea break	
15:45 to 17:15	Concluding Sesssion	
17:15	High Tea	

09:30 to 10:00	Introduction to Machine Descriptions	Lecture
10:00 to 10:30	Spim machine descriptions Levels 0, 1	Lecture
10:30 to 11:00	Machine description assignments	Lab
11:00 to 11:30	Tea break	-
11:30 to 13:00	Machine description assignments	Lab
13:00 to 14:00	Lunch	-
14:00 to 14:30	Spim machine descriptions Levels 2, 3, 4	Lecture
14:30 to 15:00	Advanced issues in machine descriptions	Lecture
15:00 to 15:30	Machine description assignments	Lab
15:30 to 15:45	Tea break	
15:45 to 16:45	Machine description assignments	Lab
16:45 to 17:15	The Retargetability Mechanism of GCC	Lecture
17:15	High Tea	
20:30	Dinner	
Participants can continue to do the lab work until dinner		

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## **Announcements and Questions**

Lecture and lab schedule

- Flexible
- Duration, time may be changed dynamically based on how well things are being received
- Lab and lectures may be interchanged too





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Lab arrangements:

- Assignments have to be done in groups of two.
  - Please use the tea time to finalize your group
  - A sheet will be circulated after the tea for group details
  - If you need a laptop, we will issue it during lunch. You will need to return it in the evening.
- Doing the assignments.
  - Do all exercises on your laptop, or
  - Use your laptop and log into our servers,
  - Use our laptop and log into our servers.
- How to connect to server?

Separate information sheet has been provided.

- Teaching assistants will help you in doing the assignments
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# **Announcements and Questions**

- Receipts of payments
  - If you have sent the DD earlier, your receipt may be ready.
  - Please collect from Nisha on Saturday during the lunch time.
- Concluding session
  - Informal discussions
  - Brief (10 minute) presentations by participating organizations/individuals
     If you are interested, please contact me today or latest tomorrow
  - Feedback forms
  - Announcement of best Teaching Assistant

- Dinner and breakfast arrangments
  - Breakfast available in the hostels
  - ► Tea: Available in the foyer
  - Dinner: Thu, Fri, Sat: Available at 20:30 in foyer
- Important requirement from the security: Please continue to wear your name badge throughout the IITB campus



11. Nisha Biju

22. Vini Kanvar

Overall coordination: Uday Khedker, Supratim Biswas, Amitabh Sanyal

