

Problem Statement

- \succ Given a cube bisected k times recursively along each dimension, and a set of points in the cube, generate a Space Filling Curve (SFC) to map each of the voxels to a *1-D* linear ordering, in parallel on the GPU
- > Construct, **in parallel**, nodes of the octree representing the points. Also support parallel queries

Motivation

- > Spatial Domain Decomposition (SDD) refers to the process of spatially partitioning the domain of the problem across processors in a manner that attempts to balance the work performed by each processor while minimizing the number and size of communication
- **SFC** is a key SDD method
- > **Application** : SDD is a first step in many particle based methods. In graphics, a triangular element can be represented by its⁼ centroid. In the picture [2] on the right, the surface of the dragon is represented by points intersecting a cubic grid cell.
- Octrees are useful in organizing the resultant point set



Prior Work



> However, the resulting top-down structure is intrinsically sequential. A bottom up representation (using SFC) can make use of large number of parallel GPU threads

Contributions

- First parallel SFC construction algorithm on GPU
- ➢ Fast, parallel octree on GPU supporting ✓ Parallel Post Order Traversal
- ✓ Parallel Nearest Neighbor
- ✓ Parallel Range Queries
- \checkmark Location of the cell containing the queried point
- ✓ Least Common Ancestor of two cells

A *d* dimensional hypercube bisected *k* times recursively along each dimension, results in 2^{dk} non-overlapping hypercells of equal size. The SFC is a mapping of these hypercells to a 1-D linear ordering. We use the z-SFC shown below



On the left we show a *2-D z*-SFC. On the right we show 10 points in a *2-D* space. The points are sequentially labeled in the z-SFC order.

Merit of SFC ordering: Partitioning points as per SFC order ensures load balancing. Also, as important we have data ownership, i.e., implicit knowledge of where each point lives

GPU-based Parallel SFC Construction Algorithm

- 2. In parallel do,
- 101110=46
- are sorted, then we have the correct order to consider nodes in a bottom up traversal of an octree
- Octrees can be viewed as multiple SFCs at varying resolutions ➢ A linear bottom up

Fast, Parallel, GPU-based Space Filling Curves and Octrees Prekshu Ajmera, Rhushabh Goradia, Sharat Chandran, Srinivas Aluru

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Space Filling Curve (SFC)



Consider a 3 dimensional particle space of side length D and let its bottom left corner be at the origin

For resolution k, integer coordinates of a cell having a point $P(P_x, P_y, P_z) \text{ is } \left(\left| 2^k P_x / D \right|, \left| 2^k P_y / D \right|, \left| 2^k P_z / D \right| \right)$

3. Allocate 8^k threads . In parallel do

Interleave each of the k bits of a cell coordinate starting from the first dimension to form a *3k* bit value. For example, SFC value of a cell with coordinates (3, 1, 2) = (11, 01, 10) is



SFC& Octrees

Construction of Parallel Octree

- \succ Removing the least *d* bits from the value of a cell gives the value of its parent
- Value of parent cell can be computed independently in parallel



GPU-based Parallel Octree Construction Algorithm

Input : SFC based sorted ordering of cells at resolution *k* **Output** : An Adaptive Octree (Leaves present at different levels)

- 1. Allocate $L_0, L_1, ..., L_k$ arrays of sizes $8^o, 8^1, ..., 8^k$ respectively
- 2. Loop for i=k to i=1
- a. Allocate 8^{i-1} threads
- b. Each thread checks 8 elements in L_i from SFC ids $(8*Thread_{id})$ to $(8*Thread_{id}+8)$
- c. If all 8 elements are empty then make all the elements NULL and their PARENT at level L_{i-1} as leaf (The 3-D) position of the parent of a node in the upper layer can directly be calculated from the *3-D* position of the child)

Note: Implementation is highly data parallel with *zero* communication between the GPU threads



Typical Queries

We use the bit representation of SFC values

> Is node C_1 contained in node C_2 ?

 C_1 is contained in C_2 if and only if the SFC value of C_2 is a prefix of the SFC value of C₁

\succ Given C₂ as a descendant of C₁, return child of C₁ containing C₂

For dimension d and level l, dl is the number of bits representing C_{1} . The required child is given by the first d(l + 1) bits of C₂

\rightarrow What is the Least Common Ancestor of nodes C₁ & C₂ ?

The longest common prefix of the SFC values of C_1 and C_2 which is a multiple of dimension *d* gives us the least common ancestor

Note: Computation is directly done on SFC values. Therefore performance loss due to many threads accessing the same node will not occur even if there are multiple queries

> Post Order Traversal

For each node in parallel do

- Compute post order number (PONA) in a notional nonadaptive tree (this is an O(1) computable formula)
- 2. Lookup previously computed number of empty nodes (*NE*) from a set of nodes that occur before the node in question
- 3. PONA $\sum NE$ is the final post order number of the node in question



Results

Results were generated on an AMD Opteron 2210, 64-bit dual core CPU & nVidia 8800 GTS using **CUDA** [3]. GPU timings in charts does not include data copy time from CPU to GPU.



SFC CONSTRUCTION (2 Million Points)







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OCTREE CONSTRUCTION (5 Million Points) 6000 4000 2000 GPU (ms) 0.605 0.331 0.481 0.319 0.369 1560.9 5244.17 10.01 489.42 83.1

POST ORDER OCTREE TRAVERSAL (2 Million Points)

2000 1500 1000					
Time 0	Height: 5	6	7	8	9
➡GPU (ms)	0.223	0.273	1.339	2.17	3.921
📥 CPU (ms)	1.27	4.57	30.23	220.13	1613.9

Similar results were obtained for parallel computation of finding

- Near neighbors for *n* points
- Locations in the octree of *n* query points

We observe that if the problem size is large, GPU vastly outperforms the CPU



Applying the SFC-based constructed parallel octree to an N-body problem for the Global Illumination solution in point models [4] using the Fast Multipole Method on GPU



- 1. Sagan H. Space Filling Curves. Springer-Verilag, '94
- 2. Lefebvre S., Hornus S., and Neyret F. GPU Gems 2, chapter Octree Textures on GPU, pages 595-614. Addison Wesley, '05
- 3. nVidia CUDA Programming Guide, developer.nvidia.com/cuda
- 4. Goradia R., Kanakanti A., Chandran S., and Datta A. *Visibility* Map for Global Illumination in Point Clouds. In Procs. of ACM SIGGRAPH GRAPHITE, pages 39-46. '07