

Tutorial on Essential Abstractions in GCC

Introductory Remarks

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Outline

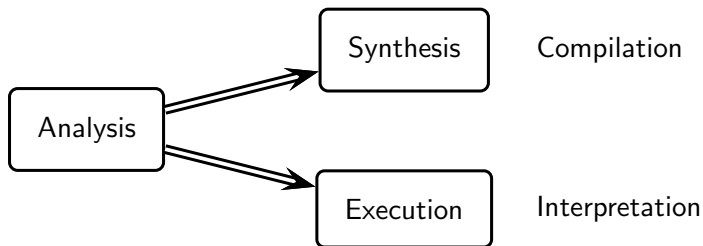
- An Overview of Compilation
Introduction, compilation models, retargetability
- GCC: The Great Compiler Challenge
Difficulties in understanding GCC
- Meeting the GCC Challenge
The tutorial plan



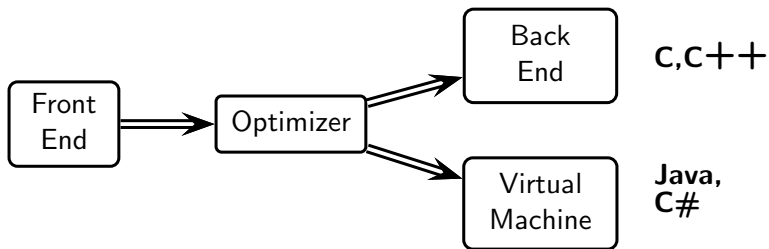
Part 1

Overview of Compilation

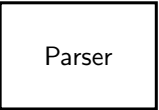
Language Implementation Models



Language Processor Models



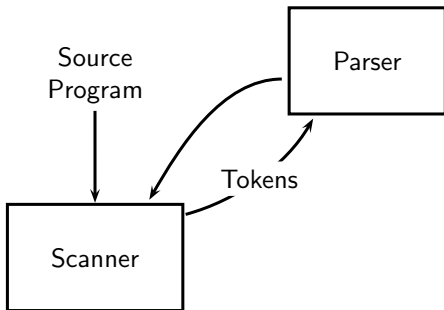
Typical Front Ends



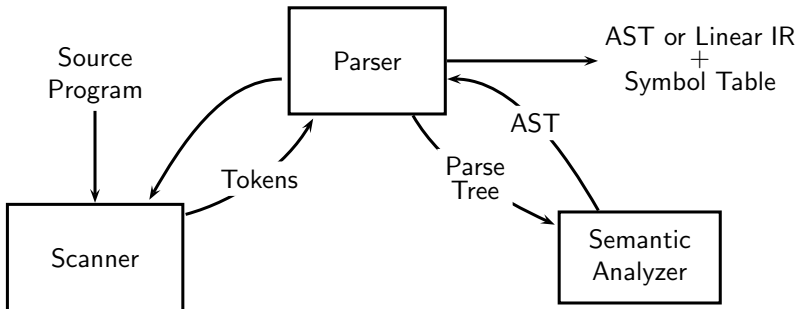
Parser



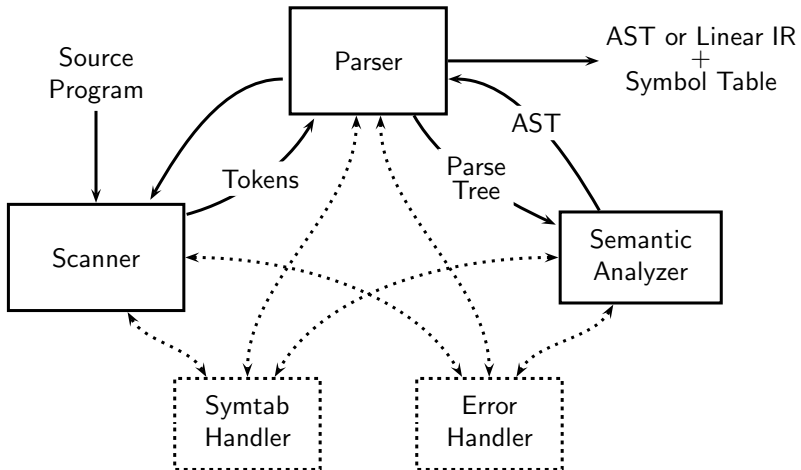
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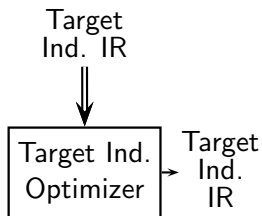
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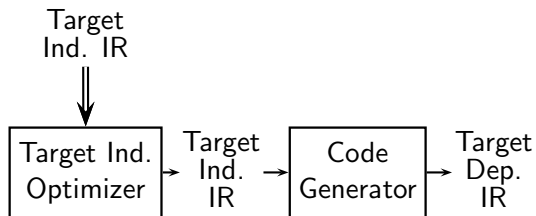
Typical Back Ends



- Compile time evaluations
- Eliminating redundant computations



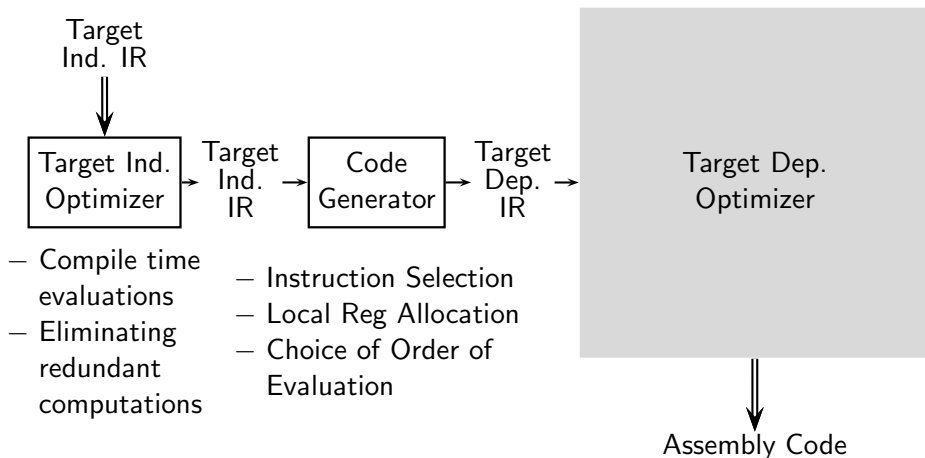
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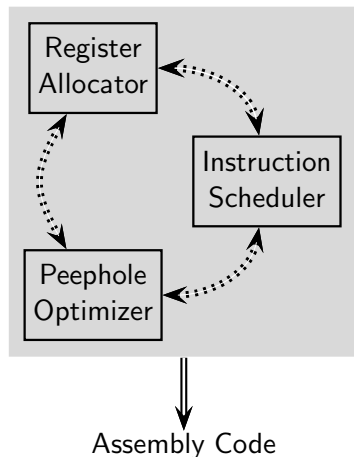
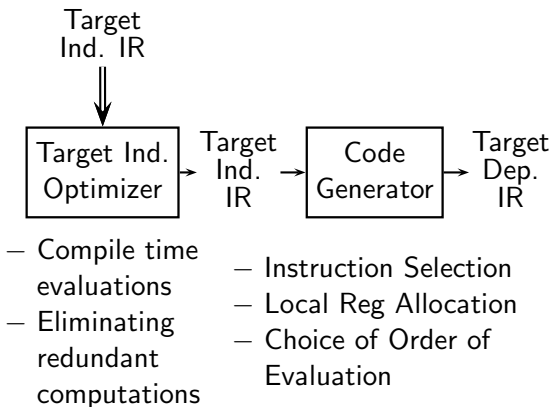
- Compile time evaluations
- Eliminating redundant computations
- Instruction Selection
- Local Reg Allocation
- Choice of Order of Evaluation



Typical Back Ends



Typical Back Ends



Compilation Models

*Aho Ullman
Model*

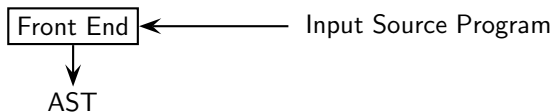
*Davidson Fraser
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Compilation Models

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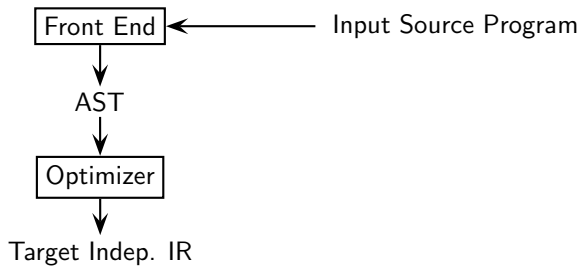
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Compilation Models

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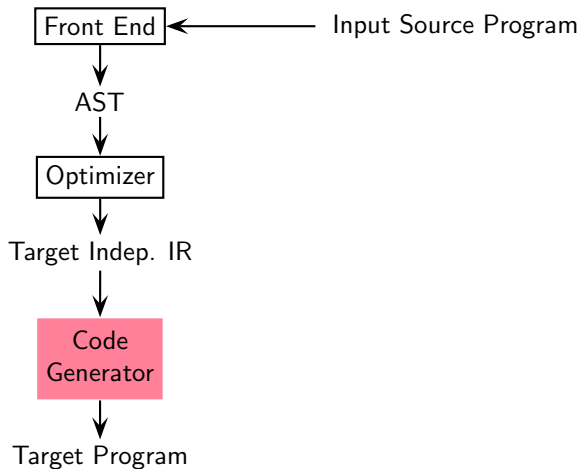
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Compilation Models

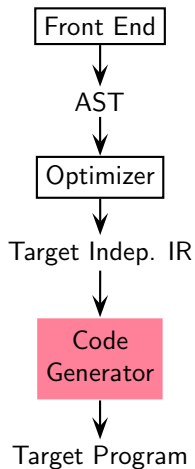
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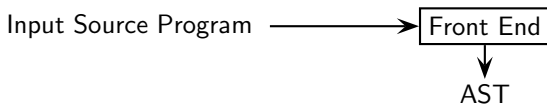


Compilation Models

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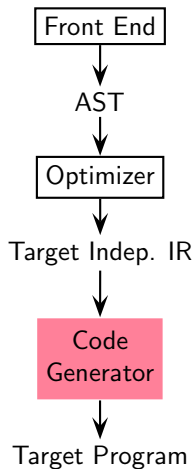


Davidson Fraser Model

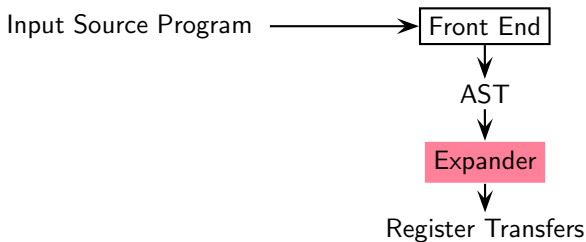


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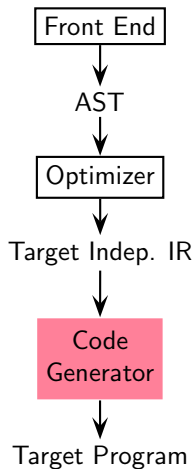


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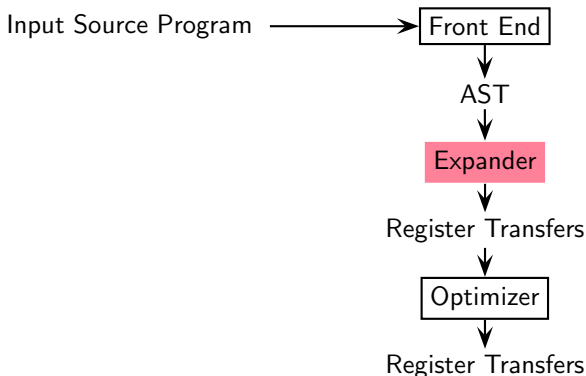


Compilation Models

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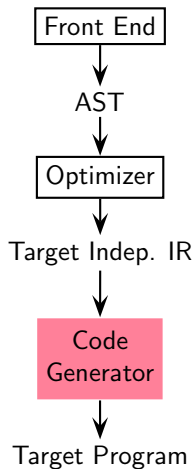


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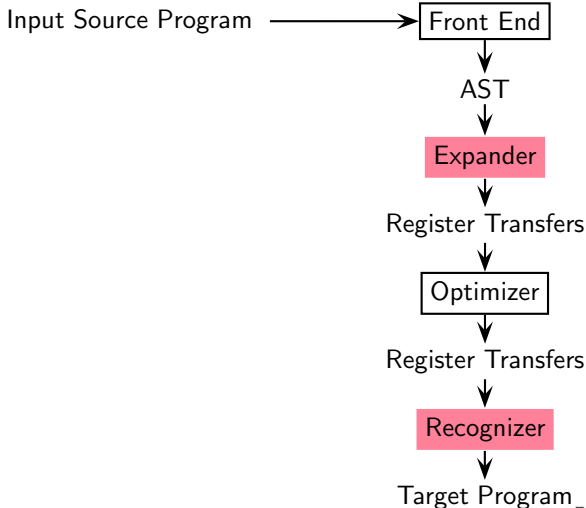


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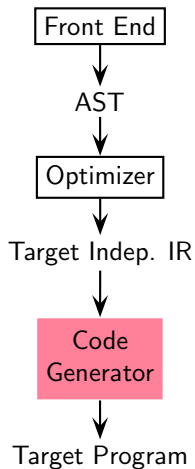


Davidson Fraser Model



Compilation Models

Aho Ullman Model



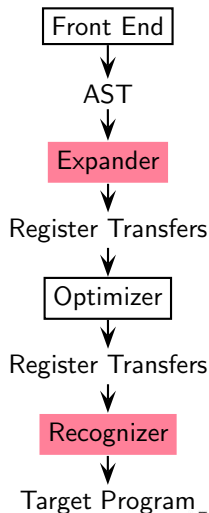
Aho Ullman: Instruction selection

- over optimized IR using
- cost based tree pattern matching

Davidson Fraser: Instruction selection

- over AST using
- structural tree pattern matching
- naive code which is
 - ▶ target dependent, and is
 - ▶ optimized subsequently

Davidson Fraser Model



Retargetability in Aho Ullman and Davidson Fraser Models

	Aho Ullman Model	Davidson Fraser Model
Instruction Selection	- Machine independent IR is expressed in the form of trees - Machine instructions are described in the form of trees - Trees in the IR are “covered” using the instruction trees	
Optimization		



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Optimization	Machine independent	Machine dependent
		Key Insight: <i>Register transfers are target specific but their form is target independent</i>

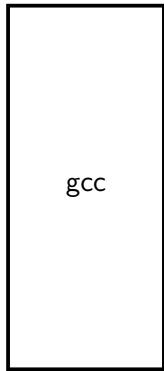


Part 2

$GCC \equiv$ The Great Compiler Challenge

The Gnu Tool Chain for C

Source Program



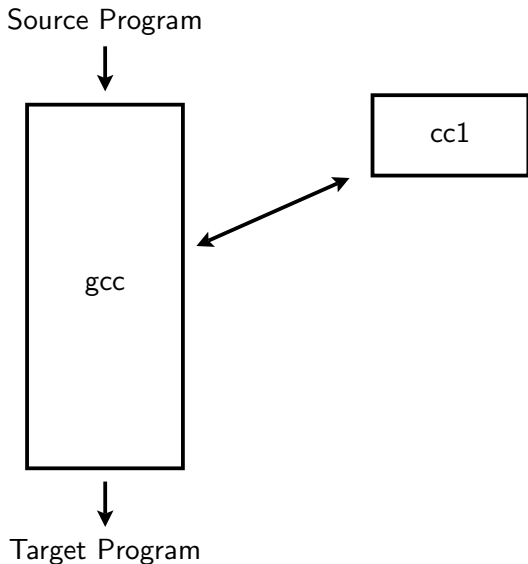
gcc



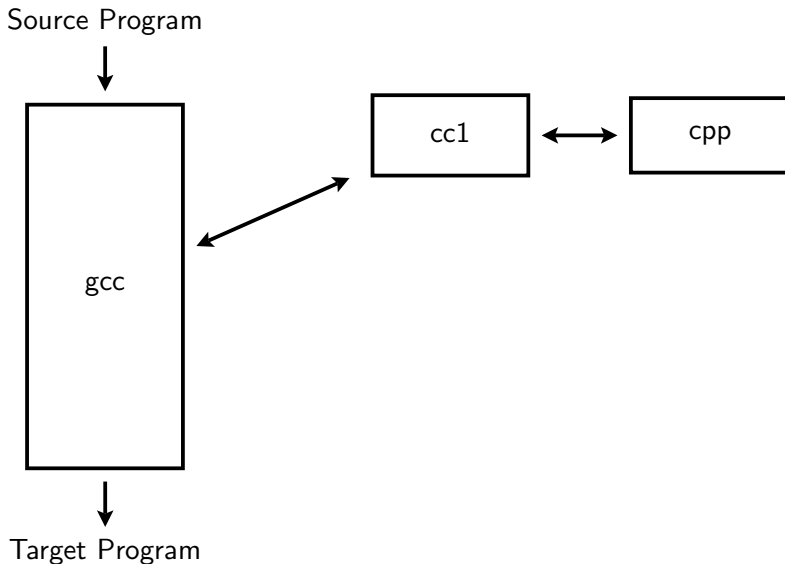
Target Program



The Gnu Tool Chain for C

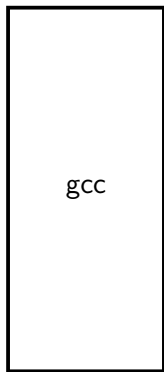


The Gnu Tool Chain for C

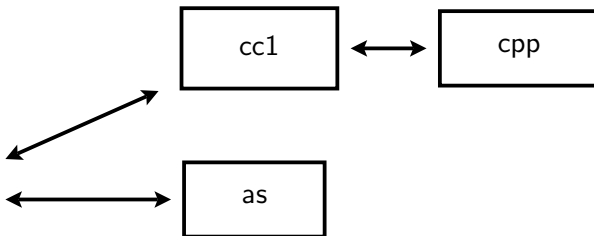


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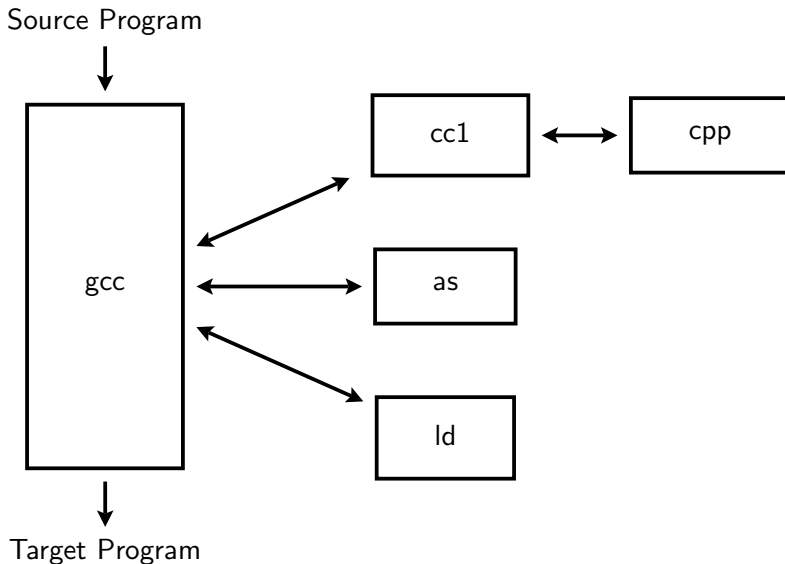
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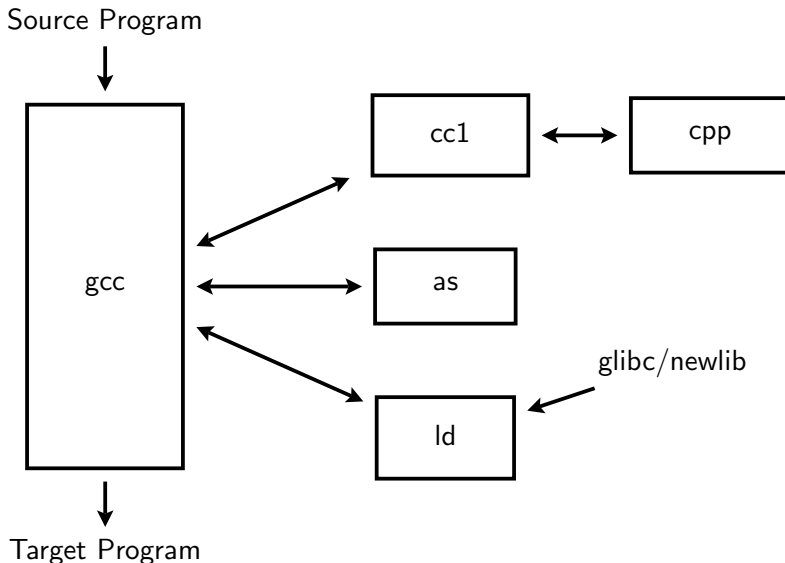
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The Gnu Tool Chain for C



The Gnu Tool Chain for C



Why is Understanding GCC Difficult?

- Some of the obvious reasons:
 - ▶ *Comprehensiveness*
GCC is a production quality framework in terms of completeness and practical usefulness
 - ▶ *Open development model*
Could lead to heterogeneity. Design flaws may be difficult to correct
 - ▶ *Rapid versioning*
GCC maintenance is a race against time. Disruptive corrections are difficult



Comprehensiveness of GCC: Wide Applicability

- Input languages supported:
C, C++, Objective-C, Objective-C++, Java, Fortran, and Ada
- Processors supported in standard releases:



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 - ▶ Additional processors independently supported:



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A29K,

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- **Processors supported in standard releases:**

- ▶ **Common processors:**

Alpha, ARM, Atmel AVR, Blackfin, HC12, H8/300, IA-32 (x86), x86-64, IA-64, Motorola 68000, MIPS, PA-RISC, PDP-11, PowerPC, R8C/M16C/M32C, SPU, System/390/zSeries, SuperH, SPARC, VAX

- ▶ **Lesser-known target processors:**

A29K, ARC, ETRAX CRIS, D30V, DSP16xx, FR-30, FR-V, Intel i960, IP2000,

- ▶ **Additional processors independently supported:**



Comprehensiveness of GCC: Wide Applicability

- **Input languages supported:**

C, C++, Objective-C, Objective-C++, Java, Fortran, and Ada

- **Processors supported in standard releases:**

- ▶ **Common processors:**

Alpha, ARM, Atmel AVR, Blackfin, HC12, H8/300, IA-32 (x86), x86-64, IA-64, Motorola 68000, MIPS, PA-RISC, PDP-11, PowerPC, R8C/M16C/M32C, SPU, System/390/zSeries, SuperH, SPARC, VAX

- ▶ **Lesser-known target processors:**

A29K, ARC, ETRAX CRIS, D30V, DSP16xx, FR-30, FR-V, Intel i960, IP2000, M32R,

- ▶ **Additional processors independently supported:**



Comprehensiveness of GCC: Wide Applicability

- **Input languages supported:**

C, C++, Objective-C, Objective-C++, Java, Fortran, and Ada

- **Processors supported in standard releases:**

- ▶ **Common processors:**

Alpha, ARM, Atmel AVR, Blackfin, HC12, H8/300, IA-32 (x86), x86-64, IA-64, Motorola 68000, MIPS, PA-RISC, PDP-11, PowerPC, R8C/M16C/M32C, SPU, System/390/zSeries, SuperH, SPARC, VAX

- ▶ **Lesser-known target processors:**

A29K, ARC, ETRAX CRIS, D30V, DSP16xx, FR-30, FR-V, Intel i960, IP2000, M32R, 68HC11,

- ▶ **Additional processors independently supported:**



Comprehensiveness of GCC: Wide Applicability

- **Input languages supported:**

C, C++, Objective-C, Objective-C++, Java, Fortran, and Ada

- **Processors supported in standard releases:**

- ▶ **Common processors:**

Alpha, ARM, Atmel AVR, Blackfin, HC12, H8/300, IA-32 (x86), x86-64, IA-64, Motorola 68000, MIPS, PA-RISC, PDP-11, PowerPC, R8C/M16C/M32C, SPU, System/390/zSeries, SuperH, SPARC, VAX

- ▶ **Lesser-known target processors:**

A29K, ARC, ETRAX CRIS, D30V, DSP16xx, FR-30, FR-V, Intel i960, IP2000, M32R, 68HC11, MCORE,

- ▶ **Additional processors independently supported:**



Comprehensiveness of GCC: Wide Applicability

- **Input languages supported:**

C, C++, Objective-C, Objective-C++, Java, Fortran, and Ada

- **Processors supported in standard releases:**

- ▶ **Common processors:**

Alpha, ARM, Atmel AVR, Blackfin, HC12, H8/300, IA-32 (x86), x86-64, IA-64, Motorola 68000, MIPS, PA-RISC, PDP-11, PowerPC, R8C/M16C/M32C, SPU, System/390/zSeries, SuperH, SPARC, VAX

- ▶ **Lesser-known target processors:**

A29K, ARC, ETRAX CRIS, D30V, DSP16xx, FR-30, FR-V, Intel i960, IP2000, M32R, 68HC11, MCORE, MMIX,

- ▶ **Additional processors independently supported:**



Comprehensiveness of GCC: Wide Applicability

- **Input languages supported:**

C, C++, Objective-C, Objective-C++, Java, Fortran, and Ada

- **Processors supported in standard releases:**

- ▶ **Common processors:**

Alpha, ARM, Atmel AVR, Blackfin, HC12, H8/300, IA-32 (x86), x86-64, IA-64, Motorola 68000, MIPS, PA-RISC, PDP-11, PowerPC, R8C/M16C/M32C, SPU, System/390/zSeries, SuperH, SPARC, VAX

- ▶ **Lesser-known target processors:**

A29K, ARC, ETRAX CRIS, D30V, DSP16xx, FR-30, FR-V, Intel i960, IP2000, M32R, 68HC11, MCORE, MMIX, MN10200,

- ▶ **Additional processors independently supported:**



Comprehensiveness of GCC: Wide Applicability

- Input languages supported:

C, C++, Objective-C, Objective-C++, Java, Fortran, and Ada

- Processors supported in standard releases:

- ▶ Common processors:

Alpha, ARM, Atmel AVR, Blackfin, HC12, H8/300, IA-32 (x86), x86-64, IA-64, Motorola 68000, MIPS, PA-RISC, PDP-11, PowerPC, R8C/M16C/M32C, SPU, System/390/zSeries, SuperH, SPARC, VAX

- ▶ Lesser-known target processors:

A29K, ARC, ETRAX CRIS, D30V, DSP16xx, FR-30, FR-V, Intel i960, IP2000, M32R, 68HC11, MCORE, MMIX, MN10200, MN10300,

- ▶ Additional processors independently supported:



Comprehensiveness of GCC: Wide Applicability

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C, C++, Objective-C, Objective-C++, Java, Fortran, and Ada

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- ▶ **Lesser-known target processors:**

A29K, ARC, ETRAX CRIS, D30V, DSP16xx, FR-30, FR-V, Intel i960, IP2000, M32R, 68HC11, MCORE, MMIX, MN10200, MN10300, Motorola 88000,

- ▶ **Additional processors independently supported:**



Comprehensiveness of GCC: Wide Applicability

- **Input languages supported:**

C, C++, Objective-C, Objective-C++, Java, Fortran, and Ada

- **Processors supported in standard releases:**

- ▶ **Common processors:**

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- ▶ **Additional processors independently supported:**



Comprehensiveness of GCC: Wide Applicability

- **Input languages supported:**

C, C++, Objective-C, Objective-C++, Java, Fortran, and Ada

- **Processors supported in standard releases:**

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- ▶ **Additional processors independently supported:**



Comprehensiveness of GCC: Wide Applicability

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C, C++, Objective-C, Objective-C++, Java, Fortran, and Ada

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- ▶ Additional processors independently supported:



Comprehensiveness of GCC: Wide Applicability

- **Input languages supported:**

C, C++, Objective-C, Objective-C++, Java, Fortran, and Ada

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- ▶ **Lesser-known target processors:**

A29K, ARC, ETRAX CRIS, D30V, DSP16xx, FR-30, FR-V, Intel i960, IP2000, M32R, 68HC11, MCORE, MMIX, MN10200, MN10300, Motorola 88000, NS32K, ROMP, Stormy16, V850,

- ▶ **Additional processors independently supported:**



Comprehensiveness of GCC: Wide Applicability

- **Input languages supported:**

C, C++, Objective-C, Objective-C++, Java, Fortran, and Ada

- **Processors supported in standard releases:**

- ▶ **Common processors:**

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- ▶ **Lesser-known target processors:**

A29K, ARC, ETRAX CRIS, D30V, DSP16xx, FR-30, FR-V, Intel i960, IP2000, M32R, 68HC11, MCORE, MMIX, MN10200, MN10300, Motorola 88000, NS32K, ROMP, Stormy16, V850, Xtensa,

- ▶ **Additional processors independently supported:**



Comprehensiveness of GCC: Wide Applicability

- Input languages supported:

C, C++, Objective-C, Objective-C++, Java, Fortran, and Ada

- Processors supported in standard releases:

- ▶ Common processors:

Alpha, ARM, Atmel AVR, Blackfin, HC12, H8/300, IA-32 (x86), x86-64, IA-64, Motorola 68000, MIPS, PA-RISC, PDP-11, PowerPC, R8C/M16C/M32C, SPU, System/390/zSeries, SuperH, SPARC, VAX

- ▶ Lesser-known target processors:

A29K, ARC, ETRAX CRIS, D30V, DSP16xx, FR-30, FR-V, Intel i960, IP2000, M32R, 68HC11, MCORE, MMIX, MN10200, MN10300, Motorola 88000, NS32K, ROMP, Stormy16, V850, Xtensa, AVR32

- ▶ Additional processors independently supported:



Comprehensiveness of GCC: Wide Applicability

- **Input languages supported:**

C, C++, Objective-C, Objective-C++, Java, Fortran, and Ada

- **Processors supported in standard releases:**

- ▶ **Common processors:**

Alpha, ARM, Atmel AVR, Blackfin, HC12, H8/300, IA-32 (x86), x86-64, IA-64, Motorola 68000, MIPS, PA-RISC, PDP-11, PowerPC, R8C/M16C/M32C, SPU, System/390/zSeries, SuperH, SPARC, VAX

- ▶ **Lesser-known target processors:**

A29K, ARC, ETRAX CRIS, D30V, DSP16xx, FR-30, FR-V, Intel i960, IP2000, M32R, 68HC11, MCORE, MMIX, MN10200, MN10300, Motorola 88000, NS32K, ROMP, Stormy16, V850, Xtensa, AVR32

- ▶ **Additional processors independently supported:**

D10V,



Comprehensiveness of GCC: Wide Applicability

- **Input languages supported:**

C, C++, Objective-C, Objective-C++, Java, Fortran, and Ada

- **Processors supported in standard releases:**

- ▶ **Common processors:**

Alpha, ARM, Atmel AVR, Blackfin, HC12, H8/300, IA-32 (x86), x86-64, IA-64, Motorola 68000, MIPS, PA-RISC, PDP-11, PowerPC, R8C/M16C/M32C, SPU, System/390/zSeries, SuperH, SPARC, VAX

- ▶ **Lesser-known target processors:**

A29K, ARC, ETRAX CRIS, D30V, DSP16xx, FR-30, FR-V, Intel i960, IP2000, M32R, 68HC11, MCORE, MMIX, MN10200, MN10300, Motorola 88000, NS32K, ROMP, Stormy16, V850, Xtensa, AVR32

- ▶ **Additional processors independently supported:**

D10V, LatticeMico32, MeP,



Comprehensiveness of GCC: Wide Applicability

- **Input languages supported:**

C, C++, Objective-C, Objective-C++, Java, Fortran, and Ada

- **Processors supported in standard releases:**

- ▶ **Common processors:**

Alpha, ARM, Atmel AVR, Blackfin, HC12, H8/300, IA-32 (x86), x86-64, IA-64, Motorola 68000, MIPS, PA-RISC, PDP-11, PowerPC, R8C/M16C/M32C, SPU, System/390/zSeries, SuperH, SPARC, VAX

- ▶ **Lesser-known target processors:**

A29K, ARC, ETRAX CRIS, D30V, DSP16xx, FR-30, FR-V, Intel i960, IP2000, M32R, 68HC11, MCORE, MMIX, MN10200, MN10300, Motorola 88000, NS32K, ROMP, Stormy16, V850, Xtensa, AVR32

- ▶ **Additional processors independently supported:**

D10V, LatticeMico32, MeP,



Comprehensiveness of GCC: Wide Applicability

- **Input languages supported:**

C, C++, Objective-C, Objective-C++, Java, Fortran, and Ada

- **Processors supported in standard releases:**

- ▶ **Common processors:**

Alpha, ARM, Atmel AVR, Blackfin, HC12, H8/300, IA-32 (x86), x86-64, IA-64, Motorola 68000, MIPS, PA-RISC, PDP-11, PowerPC, R8C/M16C/M32C, SPU, System/390/zSeries, SuperH, SPARC, VAX

- ▶ **Lesser-known target processors:**

A29K, ARC, ETRAX CRIS, D30V, DSP16xx, FR-30, FR-V, Intel i960, IP2000, M32R, 68HC11, MCORE, MMIX, MN10200, MN10300, Motorola 88000, NS32K, ROMP, Stormy16, V850, Xtensa, AVR32

- ▶ **Additional processors independently supported:**

D10V, LatticeMico32, MeP, Motorola 6809,



Comprehensiveness of GCC: Wide Applicability

- **Input languages supported:**

C, C++, Objective-C, Objective-C++, Java, Fortran, and Ada

- **Processors supported in standard releases:**

- ▶ **Common processors:**

Alpha, ARM, Atmel AVR, Blackfin, HC12, H8/300, IA-32 (x86), x86-64, IA-64, Motorola 68000, MIPS, PA-RISC, PDP-11, PowerPC, R8C/M16C/M32C, SPU, System/390/zSeries, SuperH, SPARC, VAX

- ▶ **Lesser-known target processors:**

A29K, ARC, ETRAX CRIS, D30V, DSP16xx, FR-30, FR-V, Intel i960, IP2000, M32R, 68HC11, MCORE, MMIX, MN10200, MN10300, Motorola 88000, NS32K, ROMP, Stormy16, V850, Xtensa, AVR32

- ▶ **Additional processors independently supported:**

D10V, LatticeMico32, MeP, Motorola 6809, MicroBlaze,



Comprehensiveness of GCC: Wide Applicability

- **Input languages supported:**

C, C++, Objective-C, Objective-C++, Java, Fortran, and Ada

- **Processors supported in standard releases:**

- ▶ **Common processors:**

Alpha, ARM, Atmel AVR, Blackfin, HC12, H8/300, IA-32 (x86), x86-64, IA-64, Motorola 68000, MIPS, PA-RISC, PDP-11, PowerPC, R8C/M16C/M32C, SPU, System/390/zSeries, SuperH, SPARC, VAX

- ▶ **Lesser-known target processors:**

A29K, ARC, ETRAX CRIS, D30V, DSP16xx, FR-30, FR-V, Intel i960, IP2000, M32R, 68HC11, MCORE, MMIX, MN10200, MN10300, Motorola 88000, NS32K, ROMP, Stormy16, V850, Xtensa, AVR32

- ▶ **Additional processors independently supported:**

D10V, LatticeMico32, MeP, Motorola 6809, MicroBlaze, MSP430,



Comprehensiveness of GCC: Wide Applicability

- **Input languages supported:**

C, C++, Objective-C, Objective-C++, Java, Fortran, and Ada

- **Processors supported in standard releases:**

- ▶ **Common processors:**

Alpha, ARM, Atmel AVR, Blackfin, HC12, H8/300, IA-32 (x86), x86-64, IA-64, Motorola 68000, MIPS, PA-RISC, PDP-11, PowerPC, R8C/M16C/M32C, SPU, System/390/zSeries, SuperH, SPARC, VAX

- ▶ **Lesser-known target processors:**

A29K, ARC, ETRAX CRIS, D30V, DSP16xx, FR-30, FR-V, Intel i960, IP2000, M32R, 68HC11, MCORE, MMIX, MN10200, MN10300, Motorola 88000, NS32K, ROMP, Stormy16, V850, Xtensa, AVR32

- ▶ **Additional processors independently supported:**

D10V, LatticeMico32, MeP, Motorola 6809, MicroBlaze, MSP430, Nios II and Nios,



Comprehensiveness of GCC: Wide Applicability

- Input languages supported:

C, C++, Objective-C, Objective-C++, Java, Fortran, and Ada

- Processors supported in standard releases:

- ▶ Common processors:

Alpha, ARM, Atmel AVR, Blackfin, HC12, H8/300, IA-32 (x86), x86-64, IA-64, Motorola 68000, MIPS, PA-RISC, PDP-11, PowerPC, R8C/M16C/M32C, SPU, System/390/zSeries, SuperH, SPARC, VAX

- ▶ Lesser-known target processors:

A29K, ARC, ETRAX CRIS, D30V, DSP16xx, FR-30, FR-V, Intel i960, IP2000, M32R, 68HC11, MCORE, MMIX, MN10200, MN10300, Motorola 88000, NS32K, ROMP, Stormy16, V850, Xtensa, AVR32

- ▶ Additional processors independently supported:

D10V, LatticeMico32, MeP, Motorola 6809, MicroBlaze, MSP430, Nios II and Nios, PDP-10,



Comprehensiveness of GCC: Wide Applicability

- Input languages supported:

C, C++, Objective-C, Objective-C++, Java, Fortran, and Ada

- Processors supported in standard releases:

- ▶ Common processors:

Alpha, ARM, Atmel AVR, Blackfin, HC12, H8/300, IA-32 (x86), x86-64, IA-64, Motorola 68000, MIPS, PA-RISC, PDP-11, PowerPC, R8C/M16C/M32C, SPU, System/390/zSeries, SuperH, SPARC, VAX

- ▶ Lesser-known target processors:

A29K, ARC, ETRAX CRIS, D30V, DSP16xx, FR-30, FR-V, Intel i960, IP2000, M32R, 68HC11, MCORE, MMIX, MN10200, MN10300, Motorola 88000, NS32K, ROMP, Stormy16, V850, Xtensa, AVR32

- ▶ Additional processors independently supported:

D10V, LatticeMico32, MeP, Motorola 6809, MicroBlaze, MSP430, Nios II and Nios, PDP-10, TIGCC (m68k variant),



Comprehensiveness of GCC: Wide Applicability

- **Input languages supported:**

C, C++, Objective-C, Objective-C++, Java, Fortran, and Ada

- **Processors supported in standard releases:**

- ▶ **Common processors:**

Alpha, ARM, Atmel AVR, Blackfin, HC12, H8/300, IA-32 (x86), x86-64, IA-64, Motorola 68000, MIPS, PA-RISC, PDP-11, PowerPC, R8C/M16C/M32C, SPU, System/390/zSeries, SuperH, SPARC, VAX

- ▶ **Lesser-known target processors:**

A29K, ARC, ETRAX CRIS, D30V, DSP16xx, FR-30, FR-V, Intel i960, IP2000, M32R, 68HC11, MCORE, MMIX, MN10200, MN10300, Motorola 88000, NS32K, ROMP, Stormy16, V850, Xtensa, AVR32

- ▶ **Additional processors independently supported:**

D10V, LatticeMico32, MeP, Motorola 6809, MicroBlaze, MSP430, Nios II and Nios, PDP-10, TIGCC (m68k variant), Z8000,



Comprehensiveness of GCC: Wide Applicability

- **Input languages supported:**

C, C++, Objective-C, Objective-C++, Java, Fortran, and Ada

- **Processors supported in standard releases:**

- ▶ **Common processors:**

Alpha, ARM, Atmel AVR, Blackfin, HC12, H8/300, IA-32 (x86), x86-64, IA-64, Motorola 68000, MIPS, PA-RISC, PDP-11, PowerPC, R8C/M16C/M32C, SPU, System/390/zSeries, SuperH, SPARC, VAX

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A29K, ARC, ETRAX CRIS, D30V, DSP16xx, FR-30, FR-V, Intel i960, IP2000, M32R, 68HC11, MCORE, MMIX, MN10200, MN10300, Motorola 88000, NS32K, ROMP, Stormy16, V850, Xtensa, AVR32

- ▶ **Additional processors independently supported:**

D10V, LatticeMico32, MeP, Motorola 6809, MicroBlaze, MSP430, Nios II and Nios, PDP-10, TIGCC (m68k variant), Z8000, PIC24/dsPIC,



Comprehensiveness of GCC: Wide Applicability

- **Input languages supported:**

C, C++, Objective-C, Objective-C++, Java, Fortran, and Ada

- **Processors supported in standard releases:**

- ▶ **Common processors:**

Alpha, ARM, Atmel AVR, Blackfin, HC12, H8/300, IA-32 (x86), x86-64, IA-64, Motorola 68000, MIPS, PA-RISC, PDP-11, PowerPC, R8C/M16C/M32C, SPU, System/390/zSeries, SuperH, SPARC, VAX

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- ▶ **Additional processors independently supported:**

D10V, LatticeMico32, MeP, Motorola 6809, MicroBlaze, MSP430, Nios II and Nios, PDP-10, TIGCC (m68k variant), Z8000, PIC24/dsPIC, NEC SX architecture



Comprehensiveness of GCC: Size

- Overall size

	Subdirectories	Files
gcc-4.4.2	3794	62301
gcc-4.5.0	4056	65639
gcc-4.6-20101225	4369	70374

- Core size (src/gcc)

	Subdirectories	Files
gcc-4.4.2	257	30163
gcc-4.5.0	283	32723
gcc-4.6-20101225	335	35986

- Machine Descriptions (src/gcc/config)

	Subdirectories	.c files	.h files	.md files
gcc-4.4.2	36	241	426	206
gcc-4.5.0	42	275	478	206
gcc-4.6-20101225	42	269	486	251



ohcount: Line Count of gcc-4.4.2

Total: 66139 file(s)

Language	Files	Code	Comment	Comment %	Blank	Total
c	15638	1840245	394682	17.7%	366815	2601742
cpp	19622	872775	190744	17.9%	189007	1252526
java	6342	681656	643045	48.5%	169465	1494166
ada	4206	638557	294881	31.6%	218000	1151438
autoconf	76	445046	393	0.1%	58831	504270
make	82	110064	3268	2.9%	13270	126602
html	480	103080	5658	5.2%	21438	130176
fortranfixed	2164	73366	1570	2.1%	9454	84390
assembler	183	42460	9607	18.5%	7084	59151
shell	137	39347	8832	18.3%	5485	53664
fortranfree	690	11852	2582	17.9%	1414	15848
objective_c	395	10562	1768	14.3%	2951	15281
automake	61	6014	853	12.4%	956	7823
perl	24	4111	1138	21.7%	732	5981
scheme	1	2775	153	5.2%	328	3256
ocaml	5	2482	538	17.8%	328	3348
python	6	1135	211	15.7%	220	1566
awk	9	1127	324	22.3%	193	1644
pascal	4	1044	141	11.9%	218	1403
csharp	9	879	506	36.5%	230	1615
dcl	2	497	99	16.6%	30	626
tcl	1	392	113	22.4%	72	577
haskell	48	149	0	0.0%	16	165
emacsclisp	1	59	21	26.2%	4	84
matlab	2	57	0	0.0%	7	64
Total	50312	4938881	1567750	24.1%	1071986	7578617



Total: 66139 file(s)

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emacsclisp	1	59	21	26.2%	4	84
matlab	2	57	0	0.0%	7	64
Total	50312	4938881	1567750	24.1%	1071986	7578617

ohcount: Line Count of gcc-4.5.0

Total: 69739 file(s)

Language	Files	Code	Comment	Comment %	Blank	Total
c	16985	1967826	413941	17.4%	391883	2773650
cpp	20813	912618	210084	18.7%	199605	1322307
java	6342	681810	643127	48.5%	169483	1494420
ada	4412	647372	302226	31.8%	222481	1172079
autoconf	79	358996	422	0.1%	55631	415049
html	487	144535	5667	3.8%	31773	181975
make	93	114490	3438	2.9%	14434	132362
fortranfixed	2535	85905	1817	2.1%	11394	99116
assembler	197	45098	10082	18.3%	7528	62708
shell	136	39789	8984	18.4%	5511	54284
scheme	7	13725	1192	8.0%	1524	16441
fortranfree	760	12955	2889	18.2%	1546	17390
objective_c	396	10782	1835	14.5%	2959	15576
automake	64	6388	914	12.5%	994	8296
perl	25	4144	1139	21.6%	739	6022
xslt	20	2805	436	13.5%	563	3804
ocaml	5	2515	540	17.7%	328	3383
python	10	1686	322	16.0%	383	2391
awk	10	1352	372	21.6%	218	1942
pascal	4	1044	141	11.9%	218	1403
csharp	9	879	506	36.5%	230	1615
dcl	2	402	84	17.3%	13	499
tcl	1	392	113	22.4%	72	577
haskell	49	153	0	0.0%	17	170
emacsclisp	1	59	21	26.2%	4	84
matlab	1	5	0	0.0%	0	5



Total: 69739 file(s)

Language	Files	Code	Comment	Comment %	Blank	Total
c	16985	1967826	413941	17.4%	391883	2773650
cpp	20813	912618	210084	18.7%	199605	1322307
java	6342	681810	643127	48.5%	169483	1494420
ada	4412	647372	302226	31.8%	222481	1172079
autoconf	79	358996	422	0.1%	55631	415049
html	487	144535	5667	3.8%	31773	181975
make	93	114490	3438	2.9%	14434	132362
fortranfixed	2535	85905	1817	2.1%	11394	99116
assembler	197	45098	10082	18.3%	7528	62708
shell	136	39789	8984	18.4%	5511	54284
scheme	7	13725	1192	8.0%	1524	16441
fortranfree	760	12955	2889	18.2%	1546	17390
objective_c	396	10782	1835	14.5%	2959	15576
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emacsclisp	1	59	21	26.2%	4	84
matlab	1	5	0	0.0%	0	5

ohcount: Line Count of gcc-4.6-20101225 snapshot

Total: 74787 file(s)

Language	Files	Code	Comment	Comment %	Blank	Total
c	18311	2089300	441364	17.4%	415623	2946287
cpp	21813	977852	227979	18.9%	213239	1419070
java	6342	681938	645505	48.6%	169819	1497262
ada	4601	680002	315946	31.7%	234447	1230395
autoconf	91	397682	513	0.1%	61417	459612
html	446	141275	5391	3.7%	30812	177478
make	99	121013	3615	2.9%	15539	140167
fortranfixed	2852	96084	1920	2.0%	13196	111200
shell	148	47937	10414	17.8%	6566	64917
assembler	209	47015	10287	18.0%	7877	65179
objective_c	815	26409	4669	15.0%	7584	38662
scheme	7	13731	1192	8.0%	1524	16447
fortranfree	806	13667	3104	18.5%	1675	18446
automake	67	9103	971	9.6%	1355	11429
perl	28	4445	1316	22.8%	837	6598
ocaml	6	2814	576	17.0%	378	3768
xslt	20	2805	436	13.5%	563	3804
awk	11	1729	396	18.6%	257	2382
python	10	1725	322	15.7%	383	2430
pascal	4	1044	141	11.9%	218	1403
csharp	9	879	506	36.5%	230	1615
dcl	2	402	84	17.3%	13	499
tcl	1	392	113	22.4%	72	577
haskell	49	153	0	0.0%	17	170
matlab	1	5	0	0.0%	0	5
Total	56846	5408876	1683047	23.7%	1189286	8281209



Total: 74787 file(s)

Language	Files	Code	Comment	Comment %	Blank	Total
c	18311	2089300	441364	17.4%	415623	2946287
cpp	21813	977852	227979	18.9%	213239	1419070
java	6342	681938	645505	48.6%	169819	1497262
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objective_c	815	26409	4669	15.0%	7584	38662
scheme	7	13731	1192	8.0%	1524	16447
fortranfree	806	13667	3104	18.5%	1675	18446
automake	67	9103	971	9.6%	1355	11429
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csharp	9	879	506	36.5%	230	1615
dcl	2	402	84	17.3%	13	499
tcl	1	392	113	22.4%	72	577
haskell	49	153	0	0.0%	17	170
matlab	1	5	0	0.0%	0	5
Total	56846	5408876	1683047	23.7%	1189286	8281209

ohcount: Line Count of gcc-4.4.2/gcc

Total: 30421 file(s)

Language	Files	Code	Comment	Comment %	Blank	Total
c	13296	1254253	282582	18.4%	283766	1820601
ada	4196	636876	294321	31.6%	217401	1148598
cpp	7418	184186	52163	22.1%	54048	290397
fortranfixed	2086	67988	1521	2.2%	9079	78588
assembler	132	31092	7243	18.9%	4770	43105
autoconf	3	26996	10	0.0%	3383	30389
fortranfree	652	10898	2376	17.9%	1314	14588
objective_c	391	10155	1654	14.0%	2830	14639
make	3	5340	1027	16.1%	814	7181
scheme	1	2775	153	5.2%	328	3256
ocaml	5	2482	538	17.8%	328	3348
shell	16	2256	712	24.0%	374	3342
awk	7	1022	251	19.7%	187	1460
perl	1	772	205	21.0%	137	1114
haskell	48	149	0	0.0%	16	165
matlab	2	57	0	0.0%	7	64
Total	28258	2242738	647591	22.4%	579484	3469812



Total: 30421 file(s)

Language	Files	Code	Comment	Comment %	Blank	Total
c	13296	1254253	282582	18.4%	283766	1820601
ada	4196	636876	294321	31.6%	217401	1148598
cpp	7418	184186	52163	22.1%	54048	290397
fortranfixed	2086	67988	1521	2.2%	9079	78588
assembler	132	31092	7243	18.9%	4770	43105
autoconf	3	26996	10	0.0%	3383	30389
fortranfree	652	10898	2376	17.9%	1314	14588
objective_c	391	10155	1654	14.0%	2830	14639
make	3	5340	1027	16.1%	814	7181
scheme	1	2775	153	5.2%	328	3256
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shell	16	2256	712	24.0%	374	3342
awk	7	1022	251	19.7%	187	1460
perl	1	772	205	21.0%	137	1114
haskell	48	149	0	0.0%	16	165
matlab	2	57	0	0.0%	7	64
Total	28258	2242738	647591	22.4%	579484	3469813



ohcount: Line Count of gcc-4.5.0/gcc

Total: 33007 file(s)

Language	Files	Code	Comment	Comment %	Blank	Total
c	14565	1368937	300284	18.0%	305671	1974892
ada	4402	645691	301666	31.8%	221882	1169239
cpp	7984	197798	54719	21.7%	57312	309829
fortranfixed	2453	80403	1768	2.2%	11008	93179
assembler	136	31802	7431	18.9%	4864	44097
autoconf	3	27317	10	0.0%	3876	31203
scheme	7	13725	1192	8.0%	1524	16441
fortranfree	722	12001	2683	18.3%	1446	16130
objective_c	392	10375	1721	14.2%	2838	14934
make	3	5886	1039	15.0%	854	7779
ocaml	5	2515	540	17.7%	328	3383
shell	14	2101	642	23.4%	347	3090
awk	8	1247	299	19.3%	212	1758
perl	2	805	206	20.4%	144	1155
haskell	49	153	0	0.0%	17	170
matlab	1	5	0	0.0%	0	5
Total	30747	2406202	677035	22.0%	613025	3696262



Total: 33007 file(s)

Language	Files	Code	Comment	Comment %	Blank	Total
c	14565	1368937	300284	18.0%	305671	1974892
ada	4402	645691	301666	31.8%	221882	1169239
cpp	7984	197798	54719	21.7%	57312	309829
fortranfixed	2453	80403	1768	2.2%	11008	93179
assembler	136	31802	7431	18.9%	4864	44097
autoconf	3	27317	10	0.0%	3876	31203
scheme	7	13725	1192	8.0%	1524	16441
fortranfree	722	12001	2683	18.3%	1446	16130
objective_c	392	10375	1721	14.2%	2838	14934
make	3	5886	1039	15.0%	854	7779
ocaml	5	2515	540	17.7%	328	3383
shell	14	2101	642	23.4%	347	3090
awk	8	1247	299	19.3%	212	1758
perl	2	805	206	20.4%	144	1155
haskell	49	153	0	0.0%	17	170
matlab	1	5	0	0.0%	0	5
Total	30747	2406202	677035	22.0%	613025	3696262



ohcount: Line Count of gcc-4.6-20101225/gcc snapshot

Total: 36322 file(s)

Language	Files	Code	Comment	Comment %	Blank	Total
c	15638	1452351	319224	18.0%	321806	2093381
ada	4591	678321	315386	31.7%	233848	1227555
cpp	8527	248085	60722	19.7%	66383	375190
fortranfixed	2767	90244	1871	2.0%	12800	104915
assembler	138	31871	7506	19.1%	4882	44259
autoconf	3	28604	12	0.0%	4011	32627
objective_c	810	25860	4492	14.8%	7436	37788
scheme	7	13731	1192	8.0%	1524	16447
fortranfree	768	12713	2893	18.5%	1575	17181
make	4	6124	1070	14.9%	893	8087
tex	1	5441	2835	34.3%	702	8978
ocaml	6	2814	576	17.0%	378	3768
shell	16	1980	597	23.2%	338	2915
awk	9	1624	323	16.6%	251	2198
perl	3	866	225	20.6%	158	1249
haskell	49	153	0	0.0%	17	170
matlab	1	5	0	0.0%	0	5
Total	33338	2600787	718924	21.7%	657002	397671



Total: 36322 file(s)

Language	Files	Code	Comment	Comment %	Blank	Total
c	15638	1452351	319224	18.0%	321806	2093381
ada	4591	678321	315386	31.7%	233848	1227555
cpp	8527	248085	60722	19.7%	66383	375190
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perl	3	866	225	20.6%	158	1249
haskell	49	153	0	0.0%	17	170
matlab	1	5	0	0.0%	0	5
Total	33338	2600787	718924	21.7%	657002	3976713



Why is Understanding GCC Difficult?

Deeper reason: GCC is not a *compiler* but a *compiler generation framework*

There are two distinct gaps that need to be bridged:

- Input-output of the generation framework: The target specification and the generated compiler
- Input-output of the generated compiler: A source program and the generated assembly program



Why is Understanding GCC Difficult?

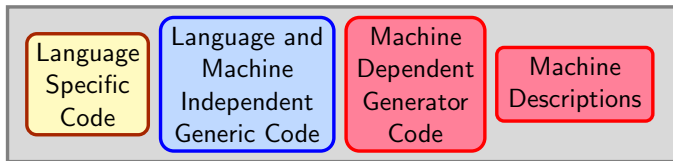
Deeper technical reasons

- GCC is not a compiler but a *compiler generation framework*
Two distinct gaps that need to be bridged
 - ▶ Input-output of the generation framework
The target specification and the generated compiler
 - ▶ Input-output of the generated compiler
A source program and the generated assembly program
- GCC generated compiler uses a derivative of the Davidson-Fraser model of compilation
 - ▶ Early instruction selection
 - ▶ Machine dependent intermediate representation
 - ▶ Simplistic instruction selection and retargetability mechanism



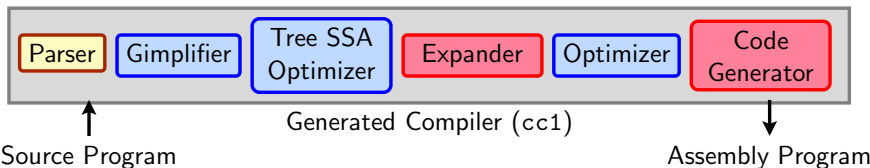
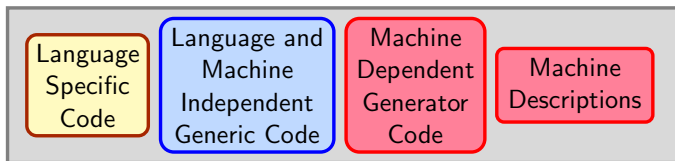
The Architecture of GCC

Compiler Generation Framework

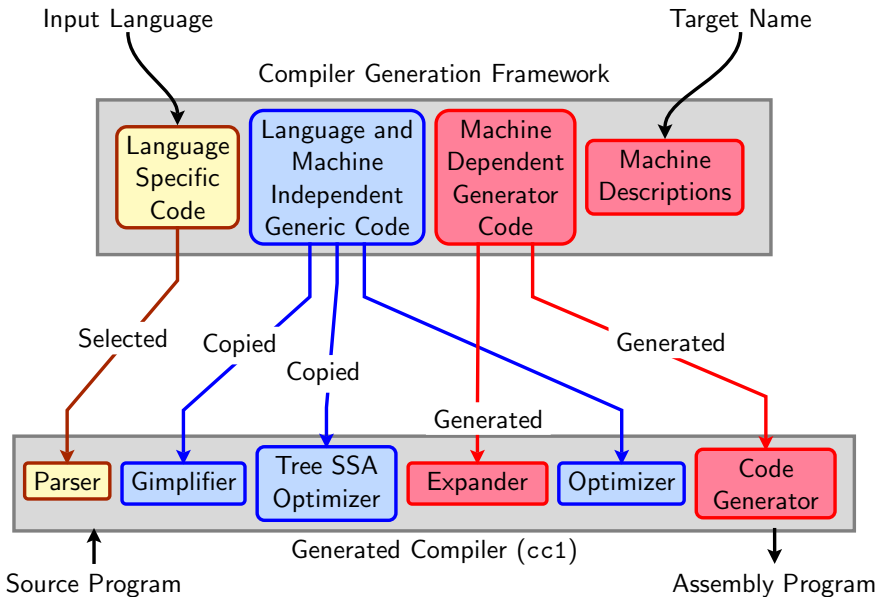


The Architecture of GCC

Compiler Generation Framework



The Architecture of GCC



An Example of The Generation Related Gap

- Predicate function for invoking the loop distribution pass

```
static bool  
gate_tree_loop_distribution (void)  
{  
    return flag_tree_loop_distribution != 0;  
}
```



An Example of The Generation Related Gap

- Predicate function for invoking the loop distribution pass

```
static bool  
gate_tree_loop_distribution (void)  
{  
    return flag_tree_loop_distribution != 0;  
}
```

- There is no declaration of or assignment to variable `flag_tree_loop_distribution` in the entire source!



An Example of The Generation Related Gap

- Predicate function for invoking the loop distribution pass

```
static bool  
gate_tree_loop_distribution (void)  
{  
    return flag_tree_loop_distribution != 0;  
}
```

- There is no declaration of or assignment to variable `flag_tree_loop_distribution` in the entire source!
- It is described in `common.opt` as follows

```
ftree-loop-distribution  
Common Report Var(flag_tree_loop_distribution) Optimization  
Enable loop distribution on trees
```



An Example of The Generation Related Gap

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static bool  
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- It is described in `common.opt` as follows

```
ftree-loop-distribution  
Common Report Var(flag_tree_loop_distribution) Optimization  
Enable loop distribution on trees
```

- The required C statements are generated during the build



Another Example of The Generation Related Gap

Locating the `main` function in the directory `gcc-4.5.0/gcc` using `cscope`



Another Example of The Generation Related Gap

Locating the main function in the directory gcc-4.5.0/gcc using cscope

File	Line	
0 collect2.c	1111	main (int argc, char **argv)
1 fp-test.c	85	main (void)
2 gcc.c	6803	main (int argc, char **argv)
3 gcov-dump.c	76	main (int argc ATTRIBUTE_UNUSED, char **argv)
4 gcov-iov.c	29	main (int argc, char **argv)
5 gcov.c	355	main (int argc, char **argv)
6 genattr.c	89	main (int argc, char **argv)
7 genattrtab.c	4439	main (int argc, char **argv)
8 genautomata.c	9475	main (int argc, char **argv)
9 genchecksum.c	67	main (int argc, char ** argv)
a gencodes.c	51	main (int argc, char **argv)
b genconditions.c	209	main (int argc, char **argv)
c genconfig.c	261	main (int argc, char **argv)
d genconstants.c	50	main (int argc, char **argv)
e genemit.c	825	main (int argc, char **argv)
f genextract.c	401	main (int argc, char **argv)



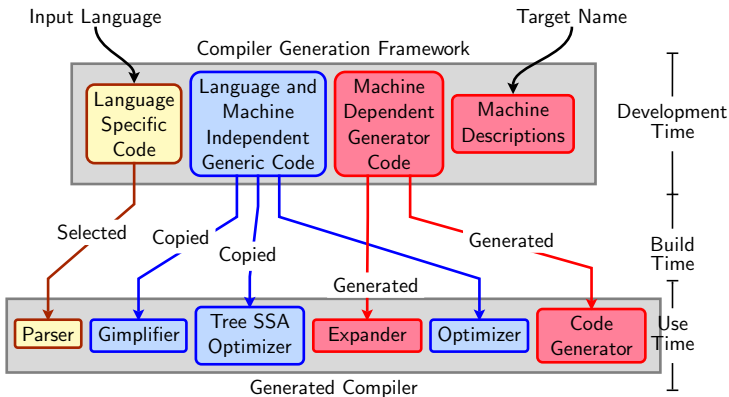
Another Example of The Generation Related Gap

Locating the main function in the directory gcc-4.5.0/gcc using cscope

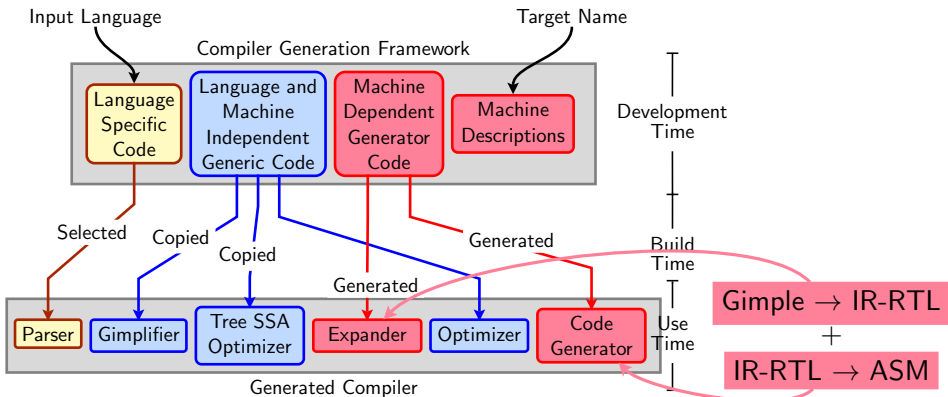
File	Line	
g genflags.c	250	main (int argc, char **argv)
h gengenrtl.c	350	main (int argc, char **argv)
i gengtype.c	3694	main (int argc, char **argv)
j genmddeps.c	45	main (int argc, char **argv)
k genmodes.c	1376	main (int argc, char **argv)
l genopinit.c	469	main (int argc, char **argv)
m genoutput.c	1023	main (int argc, char **argv)
n genpeep.c	353	main (int argc, char **argv)
o genpreds.c	1404	main (int argc, char **argv)
p genrecog.c	2722	main (int argc, char **argv)
q lto-wrapper.c	412	main (int argc, char *argv[])
r main.c	33	main (int argc, char **argv)
s mips-tdump.c	1393	main (int argc, char **argv)
t mips-tfile.c	655	main (void)
u mips-tfile.c	4695	main (int argc, char **argv)
v tlink.c	61	const char *main;



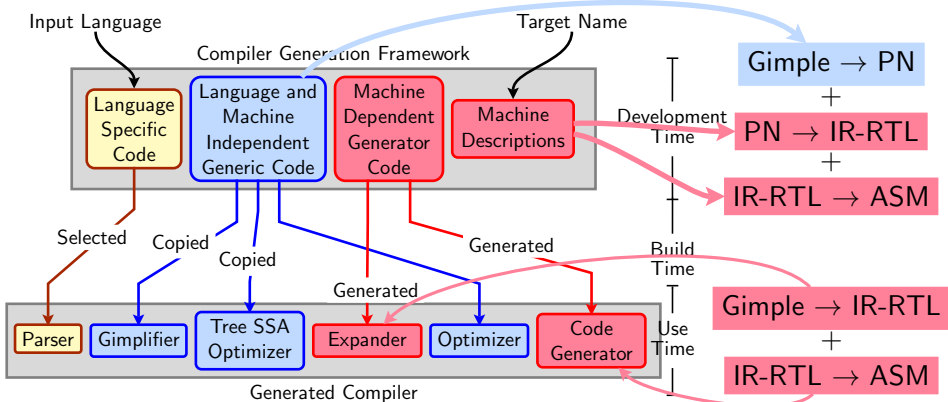
GCC Retargetability Mechanism



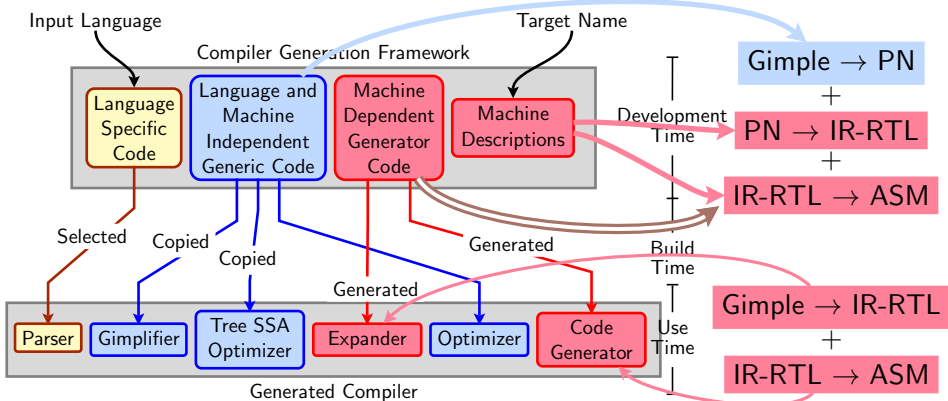
GCC Retargetability Mechanism



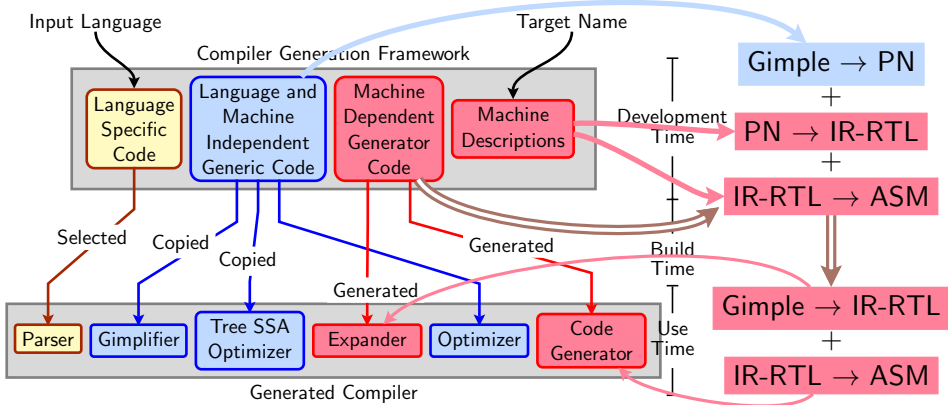
GCC Retargetability Mechanism



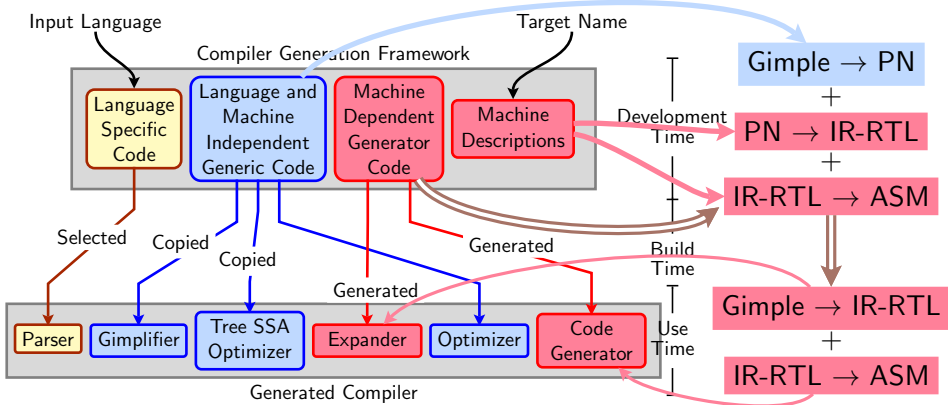
GCC Retargetability Mechanism



GCC Retargetability Mechanism



GCC Retargetability Mechanism



The generated compiler uses an adaptation of the Davison Fraser model

- Generic expander and recognizer
- Machine specific information is isolated in data structures
- Generating a compiler involves generating these data structures



The GCC Challenge: Poor Retargetability Mechanism

Symptoms:

- Machine descriptions are large, verbose, repetitive, and contain large chunks of C code

Size in terms of line counts

Files	i386	mips
*.md	35766	12930
*.c	28643	12572
*.h	15694	5105



The GCC Challenge: Poor Retargetability Mechanism

Symptoms:

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Size in terms of line counts

Files	i386	mips
*.md	35766	12930
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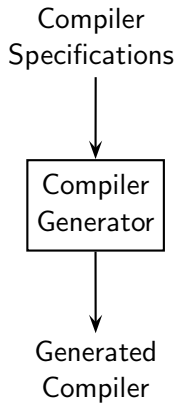
- Machine descriptions are difficult to construct, understand, debug, and enhance



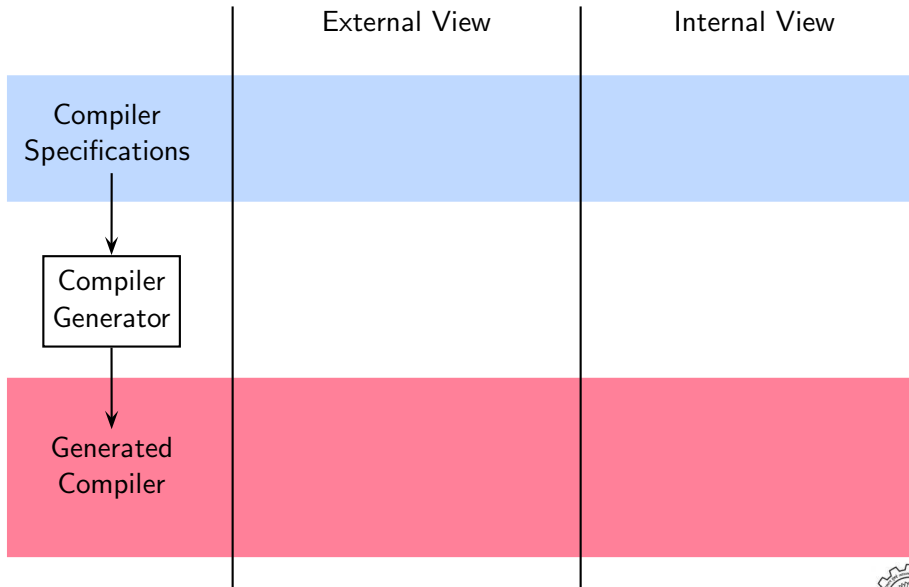
Part 3

Meeting the GCC Challenge

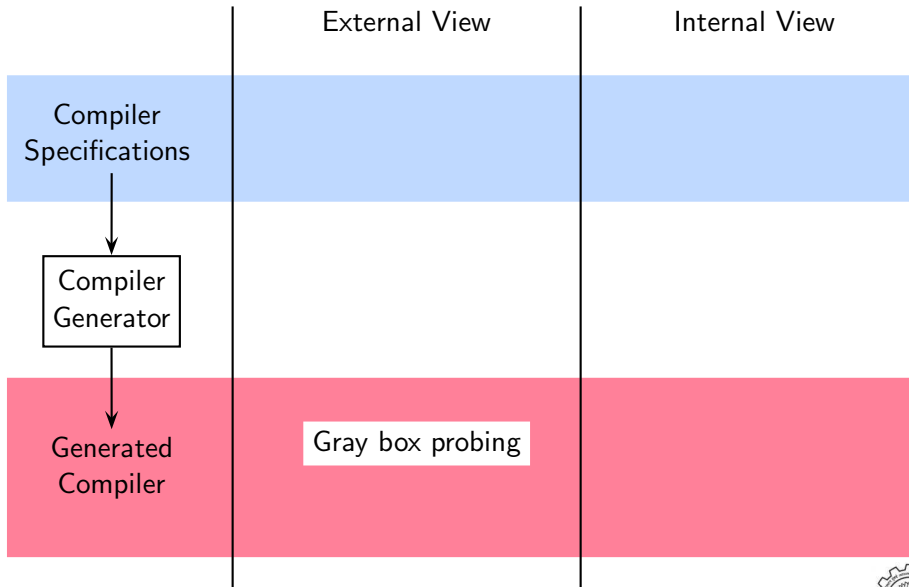
Tutorial Plan



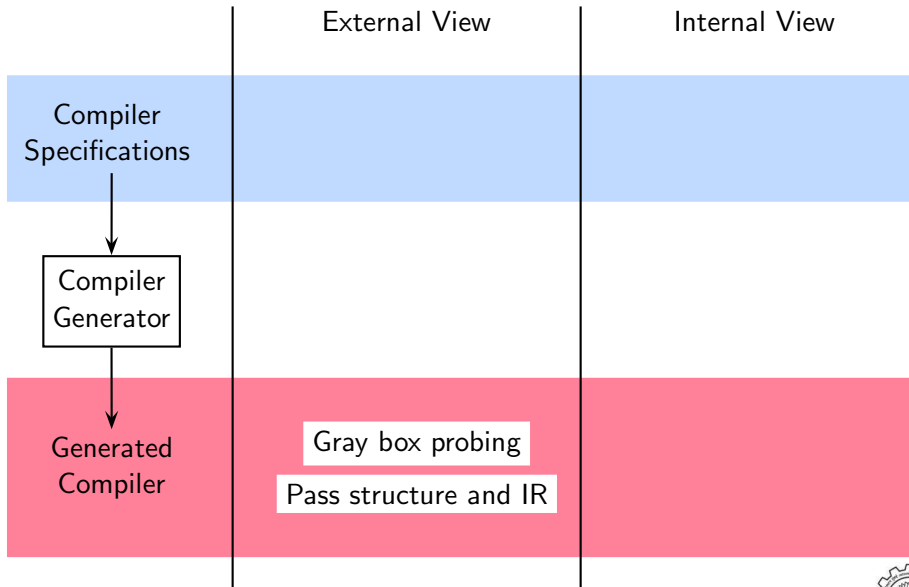
Tutorial Plan



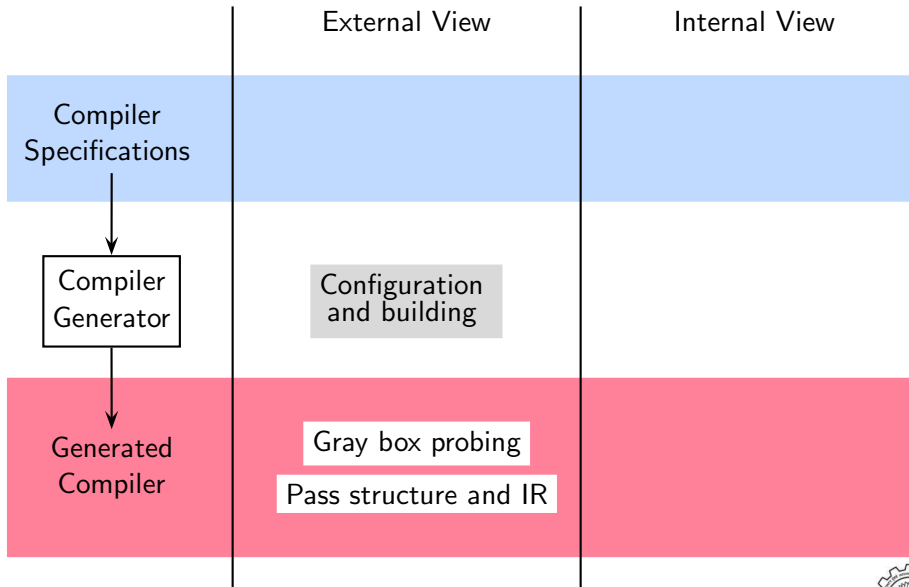
Tutorial Plan



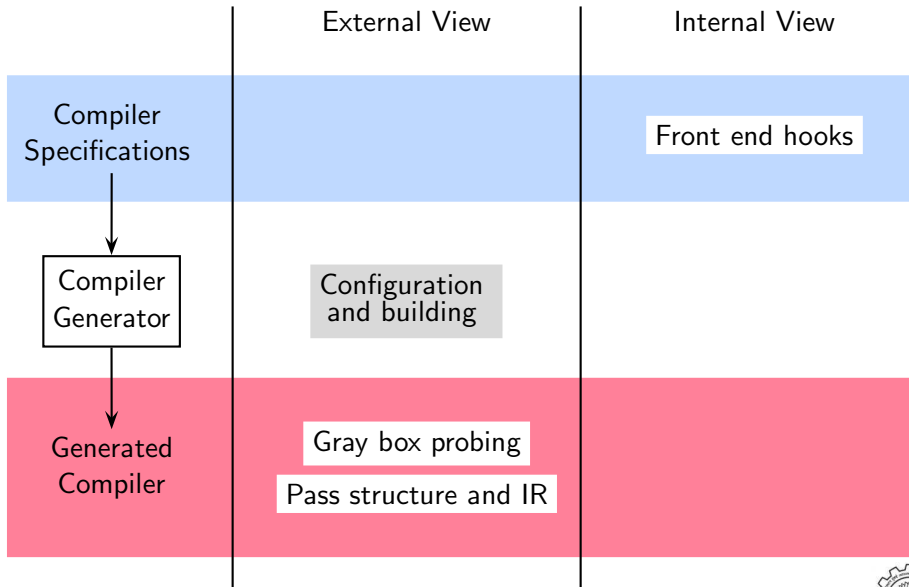
Tutorial Plan



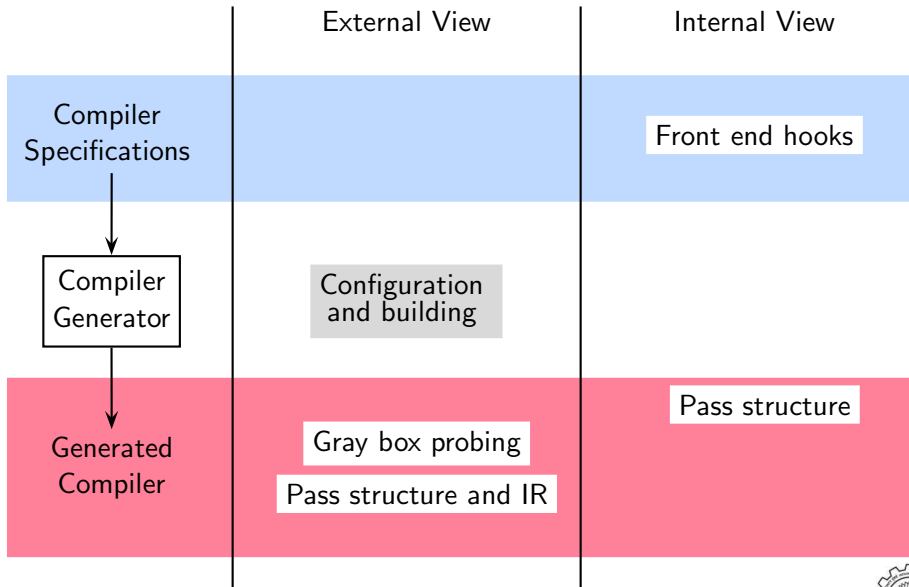
Tutorial Plan



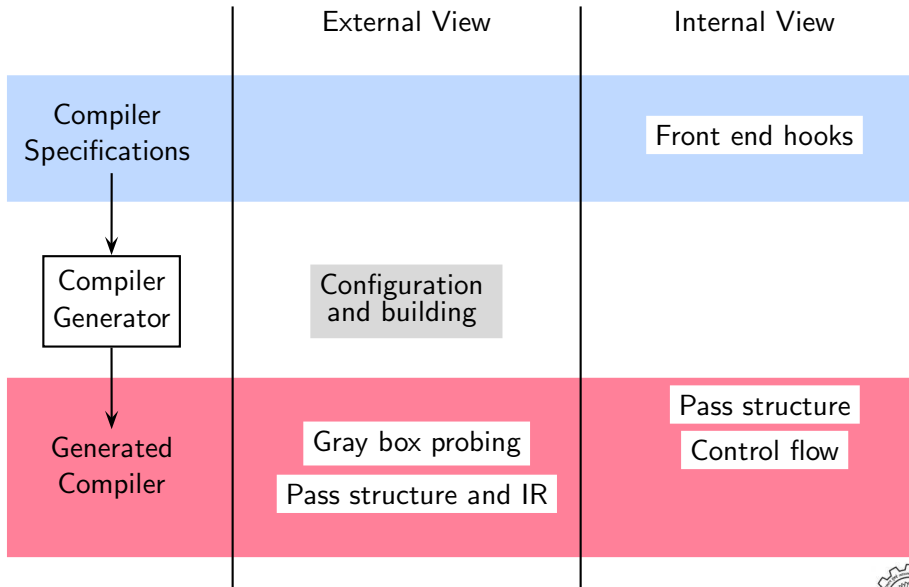
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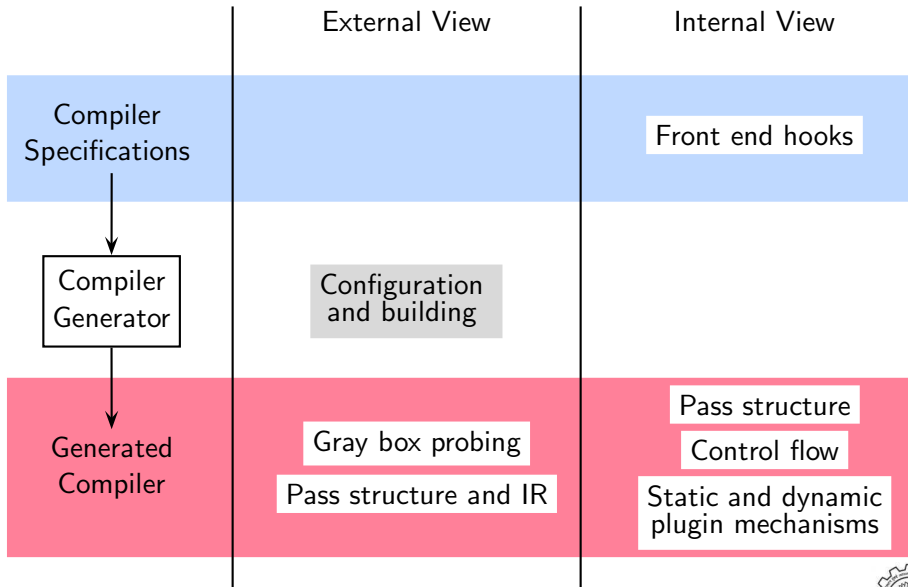
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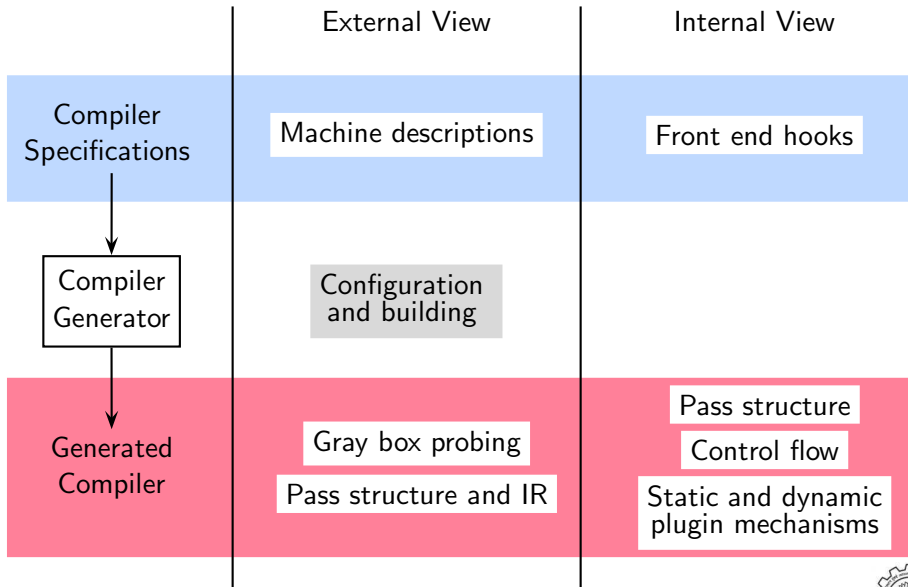
Tutorial Plan



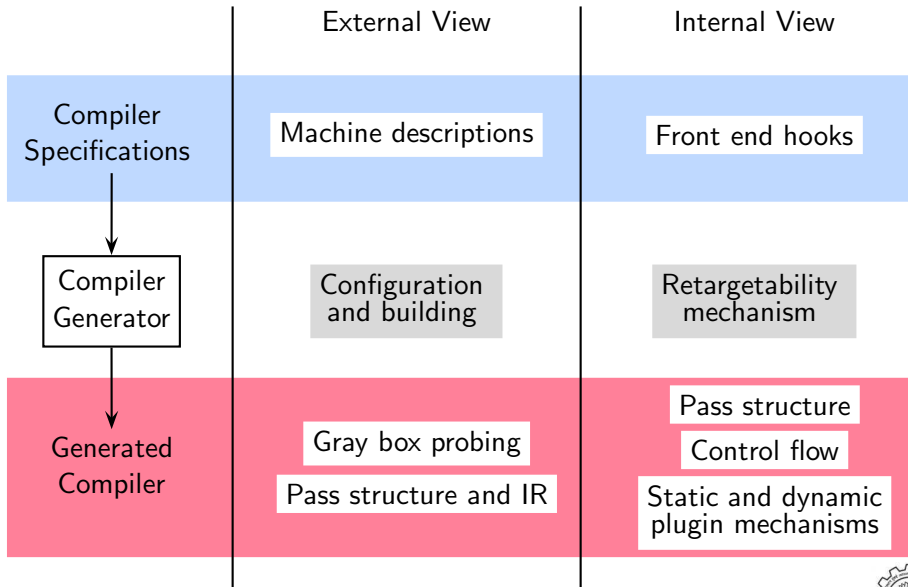
Tutorial Plan



Tutorial Plan



Tutorial Plan



Tutorial Plan: Before the Break

Topic		Main Parts	Expected Duration
1	Introductory Remarks	Overview of Compilation	20
		GCC $\equiv$ Great Compiler Challenge	
		Meeting The GCC Challenge	
2	Gray Box Probing	Preliminaries	30
		Examining GIMPLE dumps	
		Examining RTL dumps	
		Examining Assembly dumps	
3	Configuration and Building	Basic Concepts	30
		Detailed Instructions, Testing	
4	Module Binding Mechanisms and Control Flow	Motivation	25
		Static Plugins	
		Dynamic Plugins	
		Control Flow	



Tutorial Plan: After the Break

Topic		Main Parts	Expected Duration
5	Introduction to Machine Descriptions	Organization	20
		Essential Constructs	
		Essence of Retargetability	
6	Constructing Spim Machine Descriptions	Incremental Construction	25
		Level 0 of Spim MD	
		Level 1 of Spim MD	
		Levels 2, 3, 4	
7	More Details of Machine Descriptions		20
8	Retargetability Mechanisms	Constructing Code Generators	30
		Using Code Generators	
9	Concluding Remarks	Essential Abstractions in GCC	10
		About GCC Resource Center	



What is Not Covered?

- Slides included but topics not covered:
 - ▶ Details of configurations and building
 - ▶ Testing
 - ▶ Spim machine descriptions 2, 3, and 4
- Topics completely excluded
 - ▶ Gray Box Probing for Optimizations
 - ▶ Manipulating GIMPLE and RTL IR
 - ▶ Parallelization and vectorization
 - ▶ Generic data flow analyzer
- No lab exercises!

