

Automated reasoning 2016

Lecture 1: SMT Solvers

Instructor: Ashutosh Gupta

TIFR, India

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Moving away from verification

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Now we will move away from verification and learn the technology of the solvers

Where are we and where are we going?

We have seen in previous course

- ▶ DPLL($\mathcal{T}$) for solving quantifier free(QF) formulas in some theory $\mathcal{T}$
- ▶ theory of equality and function symbols(EUF)

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- ▶ $\text{DPLL}(\mathcal{T})$ for solving quantifier free(QF) formulas in some theory $\mathcal{T}$
- ▶ theory of equality and function symbols(EUF)

We will see in this lecture

- ▶ reminder of $\text{DPLL}(\mathcal{T})$ and EUF
- ▶ efficient algorithms for implementing $\text{DPLL}(\text{EUF})$
- ▶ Z3 implementation of EUF

Install tools

Let us install a few tools.

- ▶ linux
- ▶ emacs
- ▶ git
- ▶ python
- ▶ g++
- ▶ make
- ▶ gdb
- ▶ z3 source <https://github.com/Z3Prover/z3>
 - ▶ compile in debug and trace mode

Topic 1.1

SMT solver reminder

DPLL($\mathcal{T}$) - some notation

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Example 1.1

- ▶ $f(x) \approx g(h(x, y))$ is a formula in QF_EUF.
- ▶ $x > 0 \vee y + x \approx 3.5z$ is a formula in QF_LRA.

Boolean encoder

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Example 1.2

Let $F = x < 2 \vee (y > 0 \vee x \geq 2)$
and $e = \{x_1 \mapsto x < 2, x_2 \mapsto y > 0\}$
 $e(F) = x_1 \vee (x_2 \vee \neg x_1)$

Recall: CDCL($\mathcal{T}$)

Algorithm 1.1: CDCL($\mathcal{T}$)

Input: CNF F , boolean encoder e

ADDCLAUSES($e(F)$); $m := \text{UNITPROPAGATION}()$; $dl := 0$; $dstack := \lambda x.0$;

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if m is partial **then**

$dstack(dl) := m.size()$;

$dl := dl + 1$; $m := \text{DECIDE}()$; $m := \text{UNITPROPAGATION}()$;

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while $\exists x \{x \mapsto 0, x \mapsto 1\} \subseteq m$ **do**

if $dl = 0$ **then return** *unsat*;

$(C, dl) := \text{ANALYZECONFLICT}(m)$; // clause learning

$m.\text{resize}(dstack(dl))$; ADDCLAUSES($\{C\}$); $m := \text{UNITPROPAGATION}()$;

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if $\forall x \{x \mapsto 0, x \mapsto 1\} \not\subseteq m$ **then**

$(Cs, dl') := \text{THEORYDEDUCTION}(\bigwedge e^{-1}(m))$;

if $dl' < dl$ **then** $\{dl = dl'; m.\text{resize}(dstack(dl)); \}$;

 ADDCLAUSES($e(Cs)$); $m := \text{UNITPROPAGATION}()$;

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returns a clause set
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if $dl' < dl$ **then** $\{dl = dl'; m.\text{resize}(dstack(dl)); \}$;

 ADDCLAUSES($e(Cs)$); $m := \text{UNITPROPAGATION}()$;

while m is partial or $\exists x \{x \mapsto 0, x \mapsto 1\} \subseteq m$;

return *sat*

stands for decision level

dstack records history
for backtracking

returns a clause set
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Theory propagation

THEORYDEDUCTION looks at the atoms assigned so far and checks

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Any implementation must comply with the following goals

- ▶ Correctness: boolean model is consistent with $\mathcal{T}$
- ▶ Termination: unsat partial models are never repeated

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Cs may contain the clauses of the form

$$(\bigwedge L) \Rightarrow \ell$$

where $\ell \in \text{lits}(F) \cup \{\perp\}$ and $L \subseteq e^{-1}(m)$.

Note: The RHS need not be a single literal

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- ▶ If $\bigwedge e^{-1}(m)$ is unsat in $\mathcal{T}$ then Cs must contain a clause with $\ell = \perp$.
- ▶ if $\bigwedge e^{-1}(m)$ is sat then $dl' = dl$.
Otherwise, dl' is the decision level immediately after which the unsatisfiability occurred (clearly stated shortly).

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After $\text{ADDCLAUSES}(e(Cs)); m := \text{UNITPROPAGATION}()$

$$m = \{x_4 \mapsto 1, x_2 \mapsto 0, x_1 \mapsto 1, x_1 \mapsto 0\} \leftarrow \text{conflict}$$

Reminder: theory propagation implementation

Algorithm 1.2: THEORYDEDUCTION

Input: Set of literals Ls

Read only input: m partial model, $dstack$ decision depths, $d/$ current decision level

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Read only input: m partial model, $dstack$ decision depths, dl current decision level

foreach $\ell \in Ls$ **do**

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if $DP_{\mathcal{T}}.checkSat() == unsat$ **then**

$Ls' := DP_{\mathcal{T}}.unsatCore();$ // minimize clause

$dl' := \max\{dl'' \mid \exists \ell \in Ls', i. dstack(dl'') < i \wedge m[i] = e(\ell) \mapsto \neg\};$

return $(\{\neg \bigwedge Ls'\}, dl')$

else

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 //implied clauses

$Cs := \emptyset;$

foreach $\ell \in Lits(F)$ **do**

$DP_{\mathcal{T}}.push(\neg \ell);$

if $DP_{\mathcal{T}}.checkSat() == unsat$ **then**

$Ls' := DP_{\mathcal{T}}.unsatCore();$ // ℓ is called implied model and $\neg \ell \in Ls'$

$Cs := Cs \cup \{\neg \bigwedge Ls'\};$

$DP_{\mathcal{T}}.pop();$

return (Cs, dl)

Topic 1.2

Theory of equality and function symbols (EUF)

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The theory axioms include

1. $\forall x. x \approx x$
2. $\forall x, y. x \approx y \Rightarrow y \approx x$
3. $\forall x, y, z. x \approx y \wedge y \approx z \Rightarrow x \approx z$
4. for each $f/n \in \mathbf{F}$,

$$\forall x_1, \dots, x_n, y_1, \dots, y_n. x_1 \approx y_1 \wedge \dots \wedge x_n \approx y_n \Rightarrow f(x_1, \dots, x_n) \approx f(y_1, \dots, y_n)$$

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Since the axioms are valid in FOL with equality, the theory is sometimes referred as the base theory.

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Note: Predicates can be easily added if desired

Proofs in $\mathcal{T}_{EUF}$

Proof rules of $\mathcal{T}_{EUF}$

$$\frac{x = y}{y = x} \text{Symmetry}$$

$$\frac{x = y \quad y = z}{x = z} \text{Transitivity}$$

$$\frac{x_1 = y_1 \quad \dots \quad x_n = y_n}{f(x_1, \dots, x_n) = f(y_1, \dots, y_n)} \text{Congruence}$$

Example 1.3

Consider: $y = z \wedge y = z \wedge f(x, u) \neq f(z, u)$

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$$\frac{\frac{\frac{y = z}{x = y} \quad y = z}{x = z} \quad f(x, u) = f(z, u) \quad f(x, u) \neq f(z, u)}{\perp}$$

DP_{EUF}

Decides conjunction of literals with interface push, pop, checkSat and unsatCore

General idea: maintain equivalence classes among terms

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Algorithm 1.3: $DP_{EUF}.push(t_1 \bowtie t_2)$

globals: set of terms $Ts := \emptyset$, set of pair of classes $DisEq := \emptyset$, bool $conflictFound := 0$

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$Ts := Ts \cup subTerms(t_1) \cup subTerms(t_2);$

$C_1 := getClass(t_1); C_2 := getClass(t_2);$ // if t_1 and t_2 are seen first time, create new classes

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$C_1 := getClass(t_1)$; $C_2 := getClass(t_2)$; // if t_1 and t_2 are seen first time, create new classes

if $\bowtie = \approx$ **then**

if $C_1 = C_2$ **then return** ;

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General idea: maintain equivalence classes among terms

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Exercise 1.1

Run $DP_{EUF}.push$ on $x \approx f(x) \wedge f(f(f(x))) \not\approx f(f(x))$.

checkSat and pop

- ▶ $DP_{EUF}.checkSat()$ { **return** *conflictFound*; }
- ▶ $DP_{EUF}.pop()$ is implemented by recording the time stamp of pushes and undoing all the mergers happened in after last push.

Unsat core

Definition 1.2

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  |  $\text{reason} := \bigcup_i \text{getReason}(s_i, u_i)$   
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else  
  |  $\text{reason} := \{t'_1 \approx t'_2\}$   
Set  $= \text{getReason}(t_1, t'_1) \cup \text{reason} \cup \text{getReason}(t'_2, t_2)$ 
```

Topic 1.3

Algorithms for EUF

DP_{EUF} implementation - union-find

Equivalence classes are usually implemented using union-find data structure

- ▶ each class is represented using a tree over its member terms
- ▶ root of the tree represents the class

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Efficient data-structure: for n pushes, run time is $O(n \log n)$

Exercise 1.2

Prove the above complexity

Example: union-find

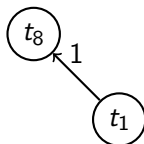
Consider:

$$\underbrace{t_1 = t_8}_1 \wedge \underbrace{t_7 = t_2}_2 \wedge \underbrace{t_7 = t_1}_3 \wedge \underbrace{t_6 = t_7}_4 \wedge \underbrace{t_9 = t_3}_5 \wedge \underbrace{t_5 = t_4}_6 \wedge \underbrace{t_4 = t_3}_7 \wedge \underbrace{t_5 = t_7}_8 \wedge \underbrace{t_1 \neq t_4}_9$$

Example: union-find

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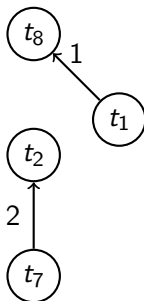
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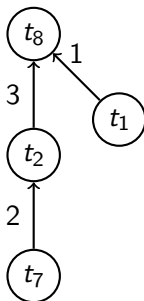
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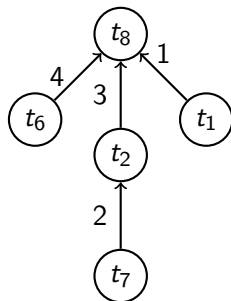
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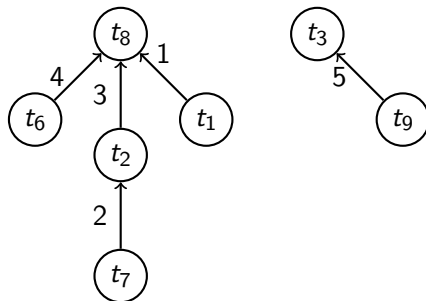
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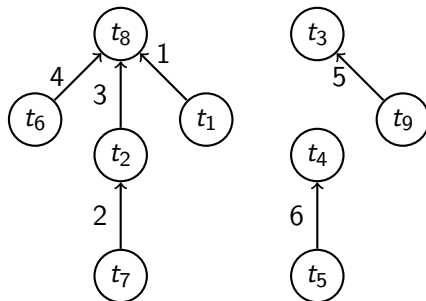
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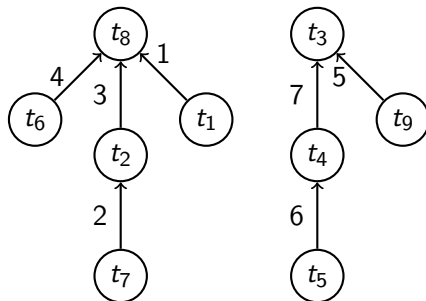
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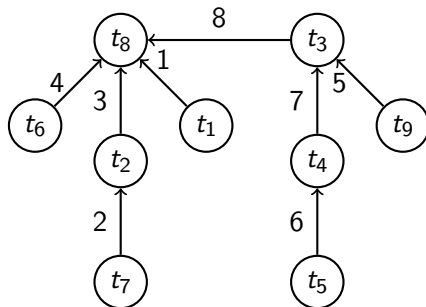
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unsatCore using union find

- ▶ generate proof of unsatisfiability using union find
- ▶ collect leaves of the proof, which can serve as an unsat core

Proof generation in union-find

Proof generation from union find data structure for an unsat input.

The proof is constructed **bottom up**.

1. There must be a dis-equality $s \neq v$ that was violated.

We need to find the proof for $s = v$.

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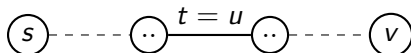
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Proof generation in union-find

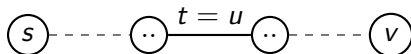
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- 2.1 Recursively, find the proof of $s = t$ and $u = v$.

Proof generation in union-find

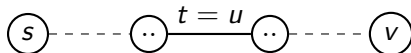
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- 2.1 Recursively, find the proof of $s = t$ and $u = v$.

- 2.2 Construct a proof for $s = v$, which is either

- ▶ in input or
- ▶ due to congruence.

- 2.3 In the congruence case, let us suppose $t = f(t_1, \dots, t_n) = f(u_1, \dots, u_n) = u$.

Then, we also recursively search for proofs of $t_i = u_i$.

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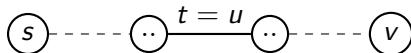
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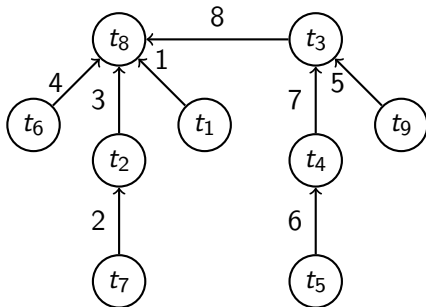
- 2.3 In the congruence case, let us suppose $t = f(t_1, \dots, t_n) = f(u_1, \dots, u_n) = u$. Then, we also recursively search for proofs of $t_i = u_i$.

Stitch the proofs appropriately.

Example: union-find proof generation

Consider:

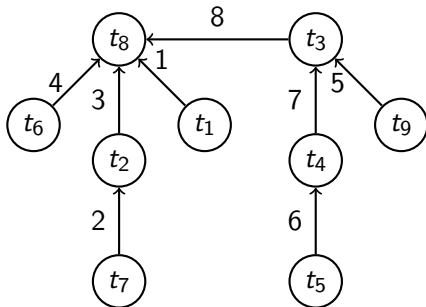
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1. $t_1 \neq t_4$ is violated.

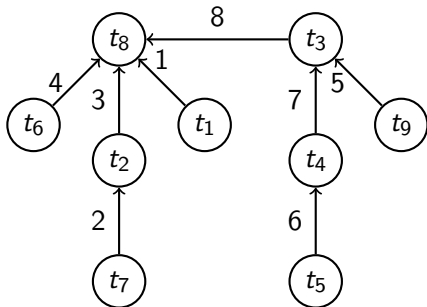
$$t_1 \neq t_4$$

⊥

Example: union-find proof generation

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1. $t_1 \neq t_4$ is violated.
2. 8 is the latest edge in the path between t_1 and t_4

 $t_1 \neq t_4$

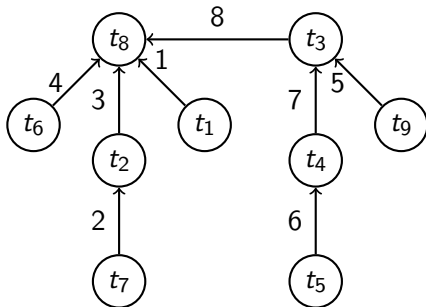
 $t_1 = t_4$

 $\perp$

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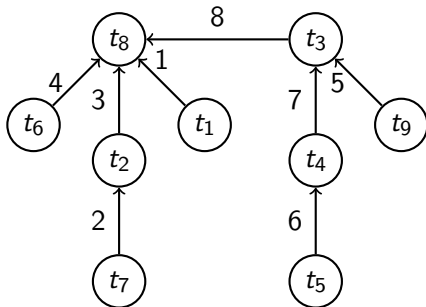
1. $t_1 \neq t_4$ is violated.
2. 8 is the latest edge in the path between t_1 and t_4
3. 8 is due to $t_5 = t_7$

$$\frac{\frac{t_5 = t_7}{t_7 = t_5}}{t_1 \neq t_4 \quad t_1 = t_4} \perp$$

Example: union-find proof generation

Consider:

$$\underbrace{t_1 = t_8}_1 \wedge \underbrace{t_7 = t_2}_2 \wedge \underbrace{t_7 = t_1}_3 \wedge \underbrace{t_6 = t_7}_4 \wedge \underbrace{t_9 = t_3}_5 \wedge \underbrace{t_5 = t_4}_6 \wedge \underbrace{t_4 = t_3}_7 \wedge \underbrace{t_5 = t_7}_8 \wedge \underbrace{t_1 \neq t_4}_9$$



1. $t_1 \neq t_4$ is violated.
2. 8 is the latest edge in the path between t_1 and t_4
3. 8 is due to $t_5 = t_7$
4. Now look for proof of $t_1 = t_7$ and $t_5 = t_4$

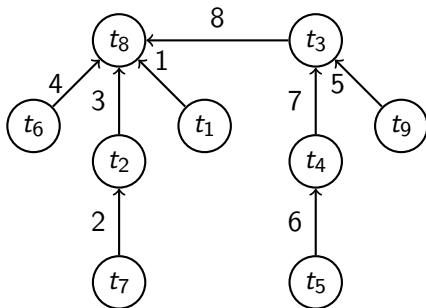
$$\frac{t_5 = t_7}{t_7 = t_5}$$

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5. 3 is the latest edge between t_1 and t_7 , which is due to $t_1 = t_7$.

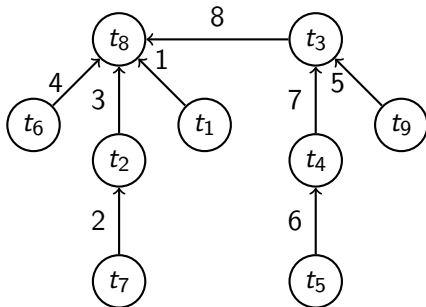
$$\frac{t_7 = t_1 \quad t_5 = t_7}{t_1 = t_7 \quad t_7 = t_5}$$

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$$\frac{t_7 = t_1}{t_1 = t_7} \quad \frac{t_5 = t_7}{t_7 = t_5}$$

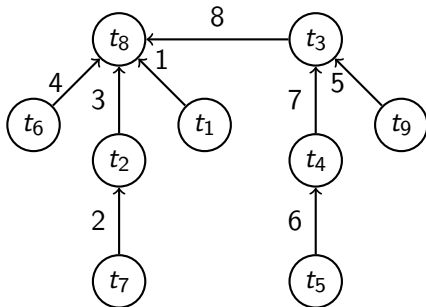
$$\frac{t_1 \neq t_4 \quad \frac{t_5 = t_4}{t_1 = t_4}}{\perp}$$

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$$\frac{}{t_1 = t_4}$$

$$\perp$$

Another example proof generation

Example 1.4

Run union find on $\underbrace{f^5(a) = a}_1 \wedge \underbrace{f^3(a) = a}_2 \wedge \underbrace{f(a) \neq a}_3$

Another example proof generation

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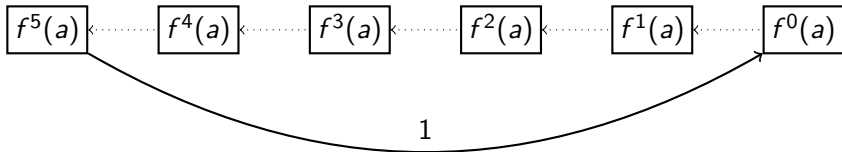


Another example proof generation

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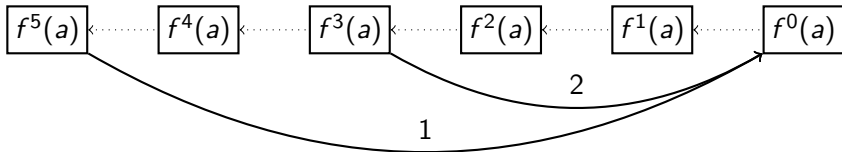


Another example proof generation

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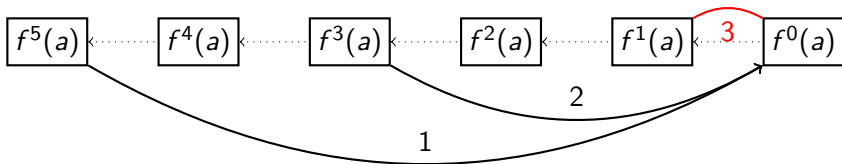


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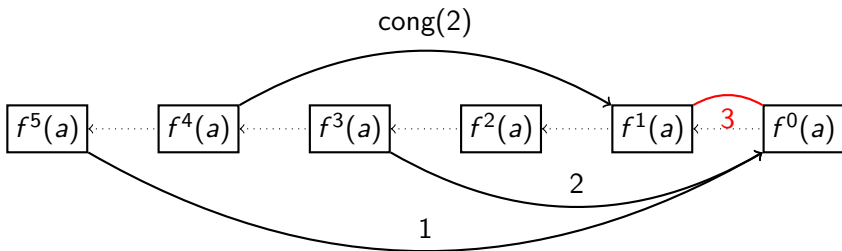


Another example proof generation

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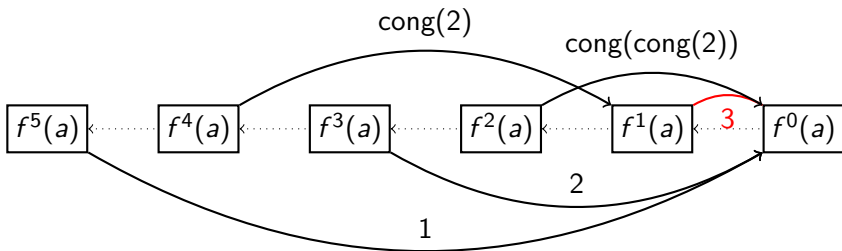


Another example proof generation

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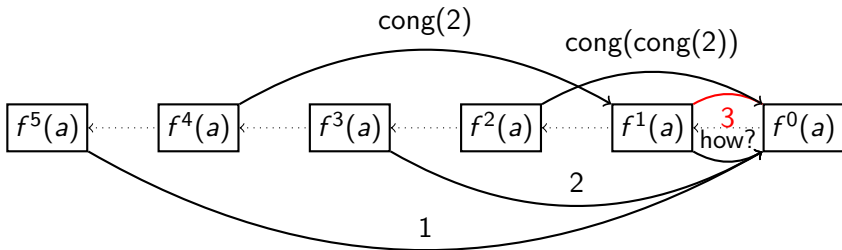


Another example proof generation

Example 1.4

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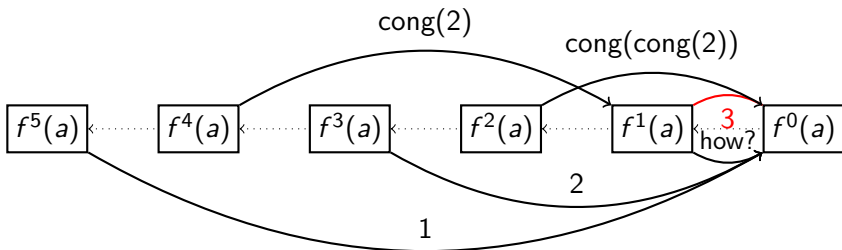


Another example proof generation

Example 1.4

Run union find on $\underbrace{f^5(a) = a}_1 \wedge \underbrace{f^3(a) = a}_2 \wedge \underbrace{f(a) \neq a}_3$

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Extract proof from the above graph?

Topic 1.4

Engineering for EUF

Engineering DPLL(QF_EUF)

Now we will look into the internals of Z3.

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Key ideas to learn in implementation design

- ▶ term management in Z3
- ▶ tactics layer
- ▶ DPLL implementation
- ▶ base theory(QF_EUF) implementation

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- ▶ term management in Z3
- ▶ tactics layer
- ▶ DPLL implementation
- ▶ base theory(QF_EUF) implementation

Key ideas to learn in software design

- ▶ be comfortable with large code base
- ▶ memory management
- ▶ customized library support

emacs and gdb

We need to learn to use an ide and a debugger before start looking inside Z3

emacs and gdb

We need to learn to use an ide and a debugger before start looking inside Z3

Here, we will use emacs23 and gdb on linux.

emacs and gdb

We need to learn to use an ide and a debugger before start looking inside Z3

Here, we will use emacs23 and gdb on linux.

I know you love some other tool chain. Please cooperate.

Topic 1.5

Problems

Problem

Exercise 1.3 (1.5 points)

Prove/Disprove that the following formula is unsat.

$$(f^4(a) \approx a \vee f^6(a) \approx a) \wedge f^3(a) \approx a \wedge f(a) \not\approx a$$

If unsat give a proof otherwise give a satisfying assignment.

Please show a run of $DPLL(\mathcal{T})$ and union-find on the above example.

Problem

Exercise 1.4 (2.5 points)

Read Z3 code for maintenance of parent relation in EUF solving.

Write a short explanation of optimizations implemented to achieve efficient operations on the parent relation.

Files of interest are:

- ▶ *src/smt/smt_enode.cpp // class for nodes in union-find*
- ▶ *src/smt/smt_enode.h*
- ▶ *src/smt/smt_cg_table.cpp // class for parent relation*
- ▶ *src/smt/smt_cg_table.h*
- ▶ *src/smt/smt_context.cpp // class for smt solver*
- ▶ *src/smt/smt_context.h*
- ▶ *src/smt/smt_internalizer.cpp:902*
- ▶ *src/smt/smt_internalizer.cpp:968*

SMT solving begins at src/smt/smt_context.cpp:3100

context object in smt_context.h has many display functions. Use them to print current state during debugging

End of Lecture 1