

CS766: Analysis of concurrent programs (first half) 2021

Lecture 11: Proof systems for concurrent programs

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Compile date: 2021-02-20

Explicit events analysis is limited

- ▶ We have seen analysis of concurrent programs with a bounded set of events
- ▶ How do we analyze when we do not have such limits?

We need a proof system.

Topic 11.1

Proof systems for programs

Hoare logic for sequential programs

- ▶ Hoare logic is one of **the frameworks** for the reasoning over programs
- ▶ Other logics reason over sets of traces and transitions instead of states
- ▶ Can we develop **something** for concurrent programs?

Proof systems for concurrent programs

- ▶ Näive extension of Hoare logic by treating the vector of program counters as a variable
Not a practical solution_(why?)
- ▶ Two proof systems that extend Hoare logic for concurrency
 1. Owicki-Gries
 2. Rely-Guarantee (not covered in this lecture)

Introducing parallel composition

We add parallel composition in our simple programming language.

$$c \parallel c$$

We define interleaved semantics as follows

$$((v, c_1 \parallel c_2), (v', c'_1 \parallel c_2)) \in T \quad \text{if} \quad ((v, c_1), (v', c'_1)) \in T$$

$$((v, c_1 \parallel c_2), (v', c_1 \parallel c'_2)) \in T \quad \text{if} \quad ((v, c_2), (v', c'_2)) \in T$$

$$((v, \text{skip} \parallel \text{skip}), (v, \text{skip})) \in T$$

Topic 11.2

Owicki-Gries proof system

How can we reason over parallel composition?

- ▶ Consider all possible interleavings
- ▶ Reasoning needs ability to summarize effect of all of them in state formulas

Commentary: A state formula only refers to variables of a program and does not relate value the variables at different time points.

Example 11.1

Consider

Assume assignments are atomic

$$x := x + 1 \quad || \quad x := x + 2$$

We may conclude : if initially $x = 0$, the program finishes with $x = 3$.

We may write Hoare triple

$$\{x = 0\} \quad x := x + 1 \quad || \quad x := x + 2 \quad \{x = 3\}$$

How can we derive the Hoare triple from the behavior of parts?

Soundness vs completeness

We will design the proof rule for parallel composition.

As we go along, we may be unsound or incomplete, or both.

We will fix those issues in small steps.

Attempt 1: Let us model it like nondeterminism (Incomplete and unsound)

$$[\text{PARLIKEONDET}] \frac{\{P\}_{c_1}\{Q\} \quad \{P\}_{c_2}\{Q\}}{\{P\}_{c_1||c_2}\{Q\}}$$

Example 11.2

$$\frac{\{x = 0\}x := x + 1\{x = 1\} \quad \{y = 0\}y := y + 1\{y = 1\}}{\{x = y = 0\}x := x + 1||y := y + 1\{x = y = 1\}} \text{ Rejected by the rule}$$

Example 11.3

$$\frac{\{x = 0\}x := x + 1\{x = 1\} \quad \{x = 0\}x := x + 1\{x = 1\}}{\{x = 0\}x := x + 1||x := x + 1\{x = 1\}} \text{ X}$$

We need to combine the effect of both the programs.

Attempt 2: Conjunction of precondition and postcondition (Unsound)

$$[\text{PARCONJUNCTIVE}] \frac{\{P_1\}c_1\{Q_1\} \quad \{P_2\}c_2\{Q_2\}}{\{P_1 \wedge P_2\}c_1||c_2\{Q_1 \wedge Q_2\}}$$

Example 11.4

$$\frac{\{y = 1\}x := 1\{y = 1\} \quad \{\top\}y := 0\{\top\}}{\{y = 1\}x := 1||y := 0\{y = 1\}} \quad \text{X}$$

What went wrong? Thread two interfered with truth value of (pre)postcondition of thread one.

We need to detect interference.

Attempt 3: Monitor interference

(Still unsound)

The following condition says that program c does not modify any variable in set of formulas Σ .

$$NoMod(c, \Sigma) \triangleq modifyVars(c) \cap FreeVars(\Sigma) = \emptyset$$

Commentary: We choose $FreeVars$ because we may have quantified formulas in our pre/postcondition

$$[PARNoMOD] \frac{\{P_1\}c_1\{Q_1\} \quad \{P_2\}c_2\{Q_2\}}{\{P_1 \wedge P_2\}c_1 || c_2\{Q_1 \wedge Q_2\}} NoMod(c_2, \{P_1, Q_1\}) \text{ and } NoMod(c_1, \{P_2, Q_2\})$$

Example 11.5

Is the above rule applicable?

$$\frac{\{z = 0\}x := z; y := x\{y = 0\} \quad \{\top\}x := 2\{\top\}}{\{z = 0\}x := z; y := x || x := 2\{y = 0\}} \quad \text{X}$$

What went wrong? We did not check for **interference on intermediate formulas**.

We need to **detect interference at all intermediate steps**.

Example : interference explicated

Example 11.6

Let us look at our example again and write the expanded proof.

$$\frac{\frac{\frac{\{z = 0\} x := z; \{x = 0\}}{\{z = 0\} x := z; y := x \{y = 0\}} \quad \{x = 0\} y := x \{y = 0\}}{\{z = 0\} x := z; y := x \{y = 0\}} \quad \{T\} x := 2 \{T\} \quad \text{X}$$

$x := 2$ interferes with $x = 0$

Collect intermediate formulas

We modify all proof rules to collect intermediate formulas. For example,

$$\begin{array}{c} [\text{ASSIGN}] \frac{}{\{P[\text{exp}/x]\}x := \text{exp}\{P, \{P, P[\text{exp}/x]\}\}} \quad [\text{SEQ}] \frac{\{P\}c_1\{Q, \Sigma_1\} \quad \{Q\}c_2\{R, \Sigma_2\}}{\{P\}c_1; c_2\{R, \Sigma_1 \cup \Sigma_2\}} \end{array}$$

Example 11.7

$$\overline{\{x > 1\}x := x - 1\{x > 0, \{x > 1, x > 0\}\}}$$

Exercise 11.1

Write collecting version of all the rules of Hoare logic.

Attempt 4: No interference on collected formulas (Sound, but incomplete)

$$[\text{PARNoMODCOLLECT}] \frac{\{P_1\}_{c_1}\{Q_1, \Sigma_1\} \quad \{P_2\}_{c_2}\{Q_2, \Sigma_2\}}{\{P_1 \wedge P_2\}_{c_1 || c_2}\{Q_1 \wedge Q_2, \Sigma_1 \cup \Sigma_2\}} \text{NoMod}(c_2, \Sigma_1) \text{ and } \text{NoMod}(c_1, \Sigma_2)$$

This proof rule is correct.
But **too restrictive**

Example 11.8

A good derivation:

$$\frac{\{x > 0\}y := z; \{x > 0, \{x > 0\}\} \quad \{\top\}x := x + 1\{\top, \{\top\}\}}{\{x > 0\}y := z || x := x + 1\{x > 0, \{x > 0, \top\}\}} \text{Rejected by the rule!}$$

Because $\text{NoMod}(x := x + 1, \{x > 0\})$ is false.

What went wrong? We went overboard. NoMod is a syntactic check.

Let us make *NoMod* false only if modifications really interfere.

Collect writes

Since only writes interfere, let us collect them explicitly.

We modify all proof rules to collect writes along with intermediate formulas. For example,

$$\begin{array}{c} [\text{ASSIGN}] \frac{}{\{P[\text{exp}/x]\} x := \text{exp} \{P, \{P, P[\text{exp}/x]\}, \{x := \text{exp}\}\}} \\ \\ [\text{SEQ}] \frac{\{P\} c_1 \{Q, \Sigma_1, Wrs_1\} \quad \{Q\} c_2 \{R, \Sigma_2, Wrs_2\}}{\{P\} c_1; c_2 \{R, \Sigma_1 \cup \Sigma_2, Wrs_1 \cup Wrs_2\}} \end{array}$$

Example 11.9

$$\frac{}{\{x > 0\} y := z; \{x > 0, \{x > 0\}, \{y := z\}\}}$$

Exercise 11.2

Write collecting version of all the rules of Hoare logic.

Attempt 5: Semantic no interference condition

(Still incomplete)

The following condition checks writes in Ws do not interfere invariants in Σ .

$$NoI(Ws, \Sigma) \triangleq \bigwedge_{c \in Ws} \bigwedge_{P \in \Sigma} \{P\}_c \{P\} \text{ holds}$$

$$[PARNOINTER] \frac{\{P_1\}_{c_1} \{Q_1, \Sigma_1, Ws_1\} \quad \{P_2\}_{c_2} \{Q_2, \Sigma_2, Ws_2\}}{\{P_1 \wedge P_2\}_{c_1 || c_2} \{Q_1 \wedge Q_2, \Sigma_1 \cup \Sigma_2, Ws_1 \cup Ws_2\}} NoI(Ws_2, \Sigma_1) \text{ and } NoI(Ws_1, \Sigma_2)$$

Example 11.10

$$\frac{\{x > 0\}_y := z; \{x > 0, \{x > 0\}, \{y := z\}\} \quad \{\top\}_x := x + 1 \{\top, \{\top\}, \{x := x + 1\}\}}{\{x > 0\}_y := z || x := x + 1 \{x > 0, \{x > 0, \top\}, \{y := z, x := x + 1\}\}} \checkmark$$

Are we done?

Not quite.

Example 11.11

Consider the following correct derivation which is disallowed by [PARNoINTER].

$$\frac{\{x > 10\}y := x; \{y > 10, \{x > 10, y > 10\}, \{y := x\}\} \quad \{x > 20\}x := x - 1\{\top, \{x > 20\}, \{x := x - 1\}\}}{\{x > 20\}y := x || x := x - 1\{y > 10, \{\dots\}, \{\dots\}\}}$$

The derivation is not possible because

$$\begin{aligned} & \text{Not}(\{x := x - 1\}, \{x > 10, y > 10\}) \\ &= \underbrace{\{x > 10\}x := x - 1\{x > 10\}}_{\text{Does not hold}} \text{ and } \{y > 10\}x := x - 1\{y > 10\} = \perp \end{aligned}$$

We are not complete. We are **still rejecting** good proofs.

How can we weaken our rule, while preserving soundness?

Collect writes with context

We modify [ASSIGN] rule again to collect writes with their contexts. For example,

$$[\text{ASSIGN}] \frac{}{\{P[\text{exp}/x]\}x := \text{exp} \{P, \{P, P[\text{exp}/x]\}, \{ \{P[\text{exp}/x]\}x := \text{exp} \} \}}$$

We also need to modify [HAVOC]. Rest remains the same.

Example 11.12

$$\frac{}{\{x > 0\}y := z; \{x > 0, \{x > 0\}, \{ \{x > 0\}y := z \} \}}$$

Write with the condition under which it executes.

The following condition checks writes in Ws do not interfere invariants in Σ .

$$NoInter(Ws, \Sigma) \triangleq \bigwedge_{\{Q\}c \in Ws} \bigwedge_{P \in \Sigma} \{P \wedge Q\}c\{P\} \text{ holds}$$

$$[PAR] \frac{\{P_1\}c_1\{Q_1, \Sigma_1, Ws_1\} \quad \{P_2\}c_2\{Q_2, \Sigma_2, Ws_2\}}{\{P_1 \wedge P_2\}c_1 || c_2\{Q_1 \wedge Q_2, \Sigma_1 \cup \Sigma_2, Ws_1 \cup Ws_2\}} NoInter(Ws_2, \Sigma_1) \text{ and } NoInter(Ws_1, \Sigma_2)$$

Example: interference checking with context

Example 11.13

$$\frac{\{x > 1\}y := x; \{y > 1, \{x > 1, y > 1\}, \{\{x > 1\}y := x\}\} \quad \{x > 3\}x := x - 1 \{\top, \{x > 3\}, \{\{x > 3\}x := x - 1\}\}}{\{x > 3\}y := x \parallel x := x - 1 \{y > 1, \{\dots\}, \{\dots\}\}}$$

The above derivation is acceptable by the PAR rule because the side conditions are satisfied.

$$\begin{aligned} \text{NoInter}(Ws_2, \Sigma_1) &= \text{NoInter}(\{\{x > 3\}x := x - 1\}, \{x > 1, y > 1\}) \\ &= \{x > 1 \wedge x > 3\}x := x - 1 \{x > 1\} \text{ and } \{y > 1 \wedge x > 3\}x := x - 1 \{y > 1\} = \top \end{aligned}$$

Exercise 11.3

Show $\text{NoInter}(Ws_1, \Sigma_2)$ is true.

Example: Let us prove a program

Let us prove.

$$\{x = 0\} x := x + 1 \parallel x := x + 2 \{x = 3\}$$

Let us display the Owicki-Gries proof in a more convenient notation

$$\begin{array}{ccc} \{x = 0\} & & \\ \{P_1 : x = 0 \vee x = 2\} & \parallel & \{P_2 : x = 0 \vee x = 1\} \\ x := x + 1; & & x := x + 2; \\ \{Q_1 : x = 1 \vee x = 3\} & & \{Q_2 : x = 2 \vee x = 3\} \\ \{x = 3\} & & \end{array}$$

Noninterference checks:

- ▶ $\{P_2 \wedge P_1\} x := x + 1 \{P_2\}$
- ▶ $\{Q_2 \wedge P_1\} x := x + 1 \{Q_2\}$

- ▶ $\{P_1 \wedge P_2\} x := x + 2 \{P_1\}$
- ▶ $\{Q_1 \wedge P_2\} x := x + 2 \{Q_1\}$

Exercise 11.4

a. Check $x = 0 \Rightarrow P_1 \wedge P_2$ and $Q_1 \wedge Q_2 \Rightarrow x = 3$.

b. Check noninterference checks.

Example: let us prove one more

Let us suppose we need to prove.

$$\{x = 0\} x := x + 1 \parallel x := x + 1 \{x = 2\}$$

Here is a Owicki-Gries proof.

$$\{x = 0 \wedge pc_1 = 0 \wedge pc_2 = 0\}$$

$$\begin{array}{l} \{pc_1 = 0 \wedge (pc_2 = 0 \Rightarrow x = 0) \wedge (pc_2 = 1 \Rightarrow x = 1)\} \\ x := x + 1; pc_1 := 1; \end{array} \parallel \begin{array}{l} \{pc_2 = 0 \wedge (pc_1 = 0 \Rightarrow x = 0) \wedge (pc_1 = 1 \Rightarrow x = 1)\} \\ x := x + 1; pc_2 := 1; \end{array}$$
$$\{pc_1 = 1 \wedge (pc_2 = 0 \Rightarrow x = 1) \wedge (pc_2 = 1 \Rightarrow x = 2)\} \quad \{pc_2 = 1 \wedge (pc_1 = 0 \Rightarrow x = 1) \wedge (pc_1 = 1 \Rightarrow x = 2)\}$$
$$\{x = 2\}$$

Noninterference check remain the same. Please verify!

Locals may appear in the proof of the other thread.

Thread modular proofs

Definition 11.1

*An Owicki-Gries proof is **thread modular** if the proof of a thread only refer to its locals and the globals.*

Proofs are **not thread modular**, when globals **lack information** to describe the invariants.

Example 11.14

In a mutual exclusion protocol, if globals do not record who has the lock, then we need to refer to program counters of threads in the proofs.

Non-thread modular proofs tend to be cumbersome. As a principle, it is desirable to minimize reference to the locals of other threads.

Another example: proving victim mutual exclusion

$\{P_1 : \top\}$ 0 : $victim = 0$; $\{Q_1 : (pc_2 \neq 1 \Rightarrow victim = 0)\}$ 1 : while($victim == 0$); $\{R_1 : pc_1 = 2 \wedge pc_2 = 1 \wedge victim = 1\}$ 2 : //critical section	$\{\top\}$ \parallel $\{\perp\}$	$\{P_2 : \top\}$ 0 : $victim = 1$; $\{Q_2 : (pc_1 \neq 1 \Rightarrow victim = 1)\}$ 1 : while($victim == 1$); $\{R_2 : pc_2 = 2 \wedge pc_1 = 1 \wedge victim = 0\}$ 2 : //critical section
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Says both the threads cannot finish

Some noninterference checks for thread 1 invariants against thread 2 writes:

- ▶ No write can interfere with P_1 , since it is \top .
 - ▶ $\{Q_1 \wedge \top\} pc_2 = 0 \wedge victim' = 1 \wedge pc_2' = 1 \{Q_1\}$
 - ▶ $\{Q_1 \wedge (pc_1 \neq 1 \Rightarrow victim = 1)\} pc_2 = 1 \wedge victim = 0 \wedge victim = victim' \wedge pc_2' = 2; \{Q_1\}$
- Exit branch of the loop in the second thread

Exercise 11.5

- a. Show R_1, P_2, Q_2, R_2 are free from interference.
- b. How many noninterference checks are needed?

Since exit from the loop modifies pc_2 , we need to check the formulas that mention it.

End of Lecture 11