

A #SAT Algorithm for Small Constant-Depth Circuits with PTF gates.

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Joint work with

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IIT Bombay, India.

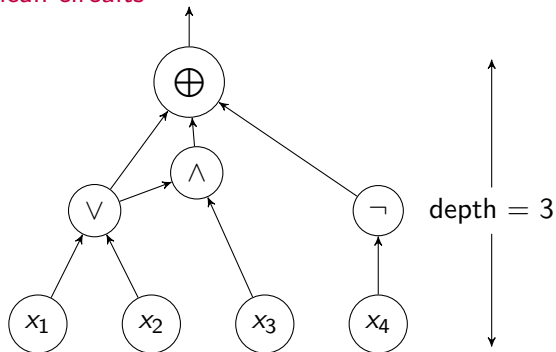
Complexity, Algorithms, Automata and Logic Meet (CAALM 2019)
Chennai Mathematical Institute, January 2019.

Circuit satisfiability algorithms

Boolean circuits

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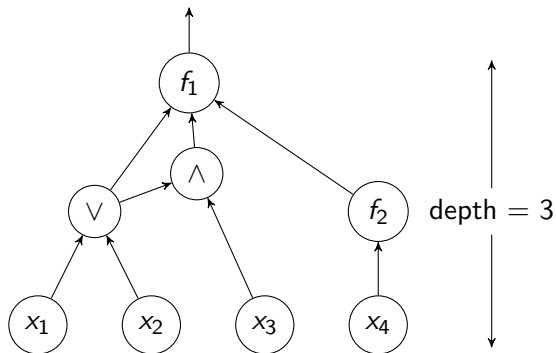
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Size = number of gates = 4

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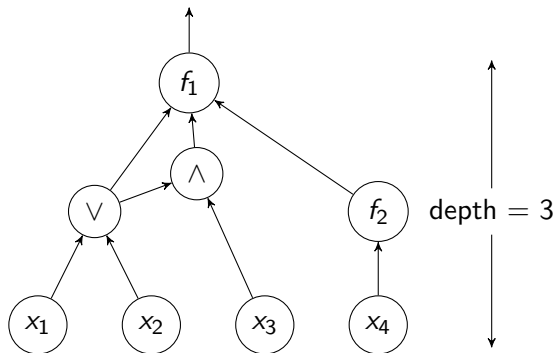
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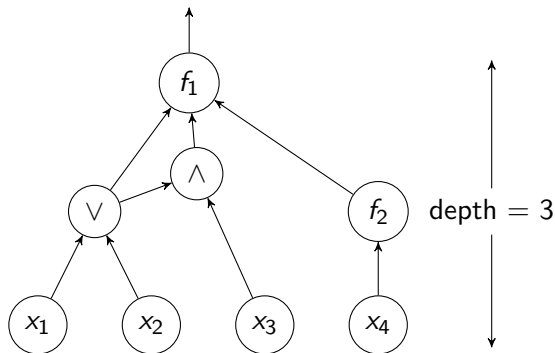


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Number of input variables: n

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Constant-depth circuits: d is independent of n .

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Can we design an algorithm that takes time $2^n / n^{\omega(1)}$ when $|C|$ is small, say $\text{poly}(n)$?

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This intuitive connection has been formalised to derive lowerbounds for various interesting classes of circuits.

[Paturi, Pudlák, Zane 1997],[Paturi, Pudlák, Saks, Zane 2005],
[Williams 2010], [Williams 2011].

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Polynomial Threshold circuits are a natural generalization of TC^0 .

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For constant depth PTF circuits of size $n^{1+\epsilon_d}$, where d depends on the depth of the circuit, and sparsity $n^{2-\Omega(1)}$.

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Fix any constant k , there is a zero-error randomized algorithm that solves the #SAT problem for a single k -PTF in time $\text{poly}(n, M) \cdot 2^{n-s}$, where $s = \tilde{\Omega}(n^{1/k+1})$.

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Some comments on zero-error randomized algorithms.

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If look-up can be done in $\text{poly}(|C|)$ time, then this step takes time $O(2^{n-m} \cdot \text{poly}(|C|)) \ll 2^n$.

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Expensive to compute

Even for LTFs computing Chow parameters is known to be NP-hard. [O'Donnell, Servedio 2011].

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Using Linear Descision Tree

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The tree can be constructed in time $2^{O(\Delta)} = \exp(m^{k+1})$ time.

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For each $\sigma : \{x_{m+1}, \dots, x_n\} \rightarrow \{-1, 1\}$,

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Time: $\text{poly}(n, M)$.

Query the tree T using $\text{coeff}(P'_\sigma)$ and compute the answer.

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Our approach

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Time: $\exp(m^{k+1}) + 2^{n-m} \text{poly}(n, M) = 2^{n-m} \text{poly}(n, M)$ if
 $m = n^{1/k+1} / \log n$.

Small-depth k -PTF circuits

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Develop strategies that work for these two steps even when the k -PTF gates are not sparse.

Satisfiability algorithms vs. SAT solving

Circuit satisfiability algorithms

worst case guarantees

SAT solving

guarantees for a subset
of instances

Satisfiability algorithms vs. SAT solving

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Circuit satisfiability algorithms	SAT solving
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Some techniques in this talk could be of general interest!

Conclusion and open problems

Better than brute-force algorithms for

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Constant depth k -PTF circuits with slightly superlinear size.

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Other connections from learning algorithms to satisfiability algorithms?

Can this technique inspire any SAT solving heuristic?

Thank You!