

process mgmt. mechanisms. (with xv6)

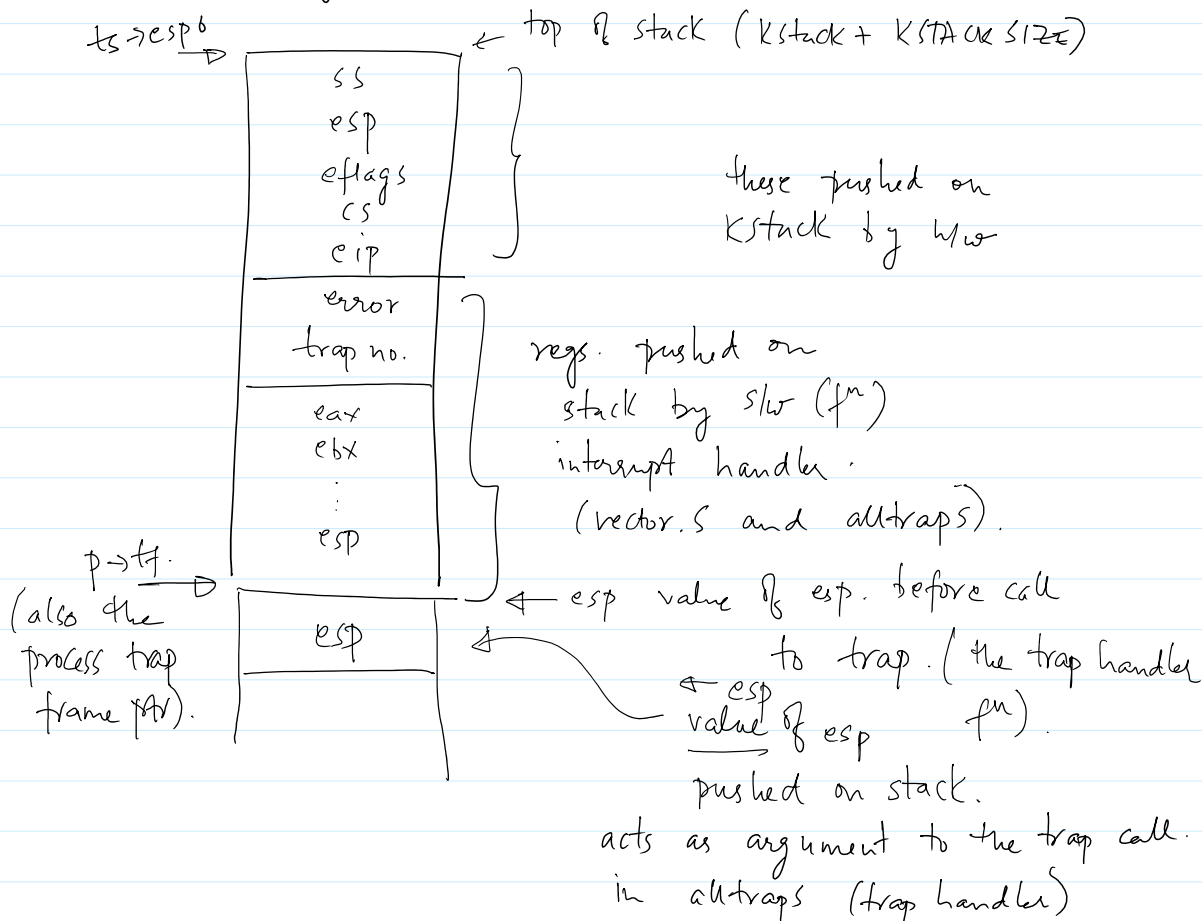
- ① the interrupting process. (e.g. the int instruction)
- ② the context switch mechanism
- ③ the child process creation & execution mechanism
- ④ the first process and its execution.

① the 'int' instruction.

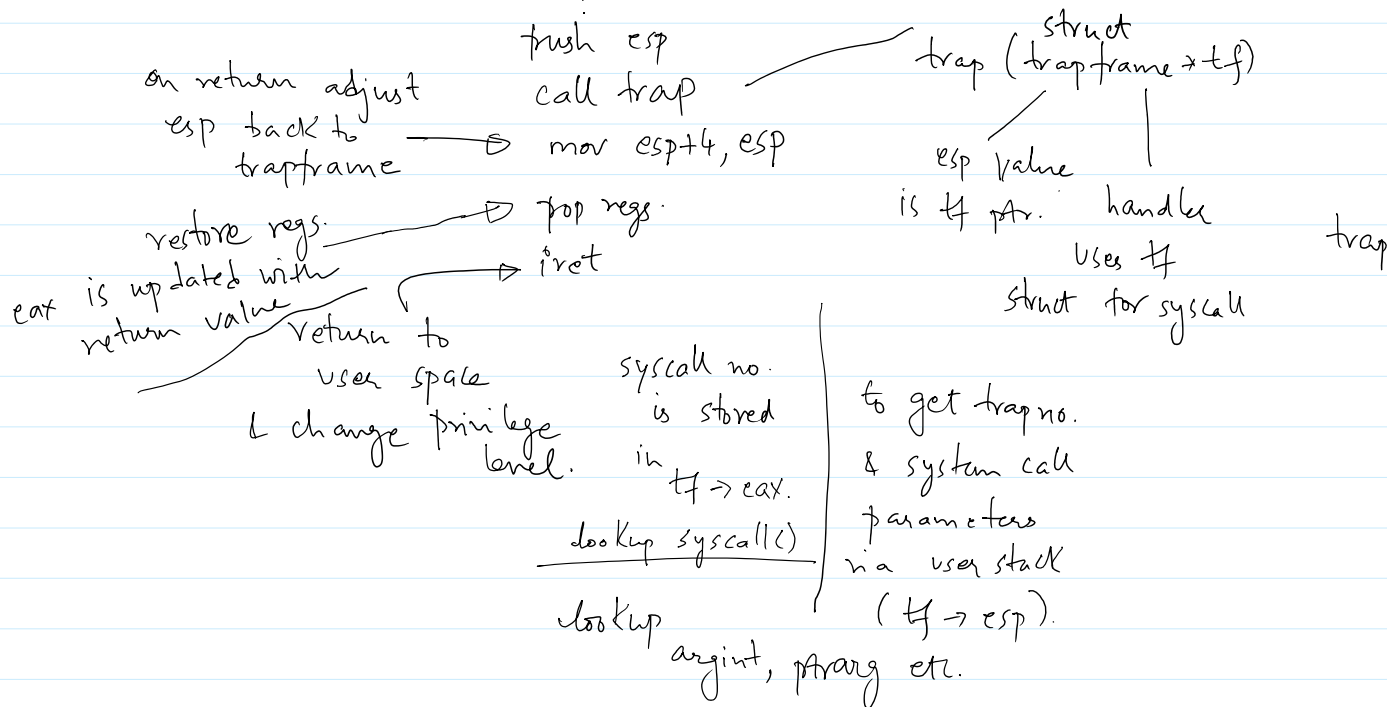
- (i) check privilege levels to execute int.
- (ii) find address of handler for interrupt
- (iii) find address of process $kstack(p \rightarrow kstack + KSTACKSIZE)$ from the task segment. regs. (which holds locⁿ. of the task segment descriptor)
- (iv) push user stack and execution info./state on $kstack$ of process. ||
initialized with max. address of $kstack$ of current process.
- (v) jump to interrupt handler.

steps (i), (ii), (iii) & (iv) happen in hardware as part of 'int' execution.

following is the $kstack$ of a process ^{on} after an 'int'



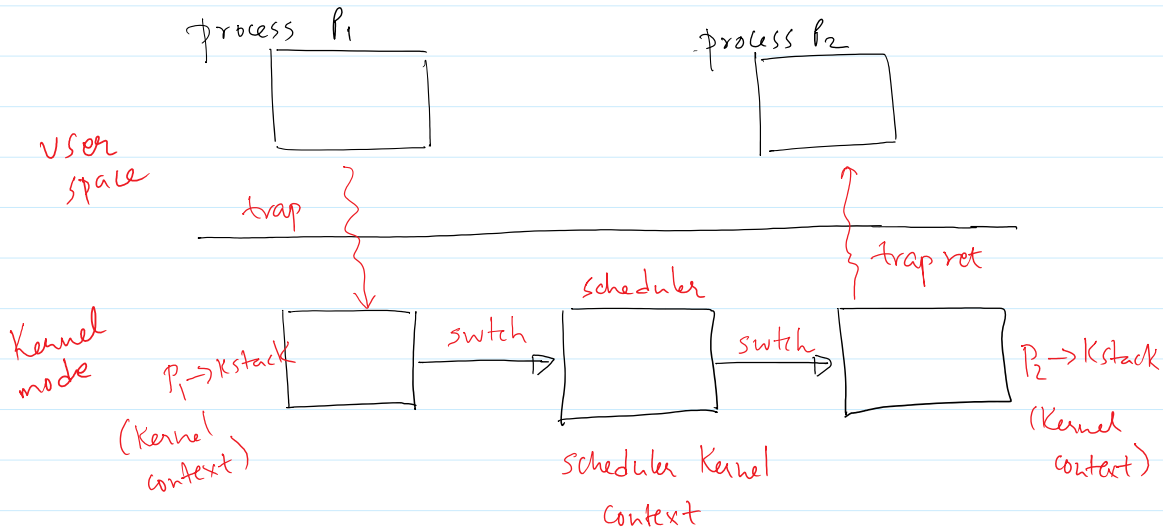
in all traps (trap handler)



② context switch mechanism.

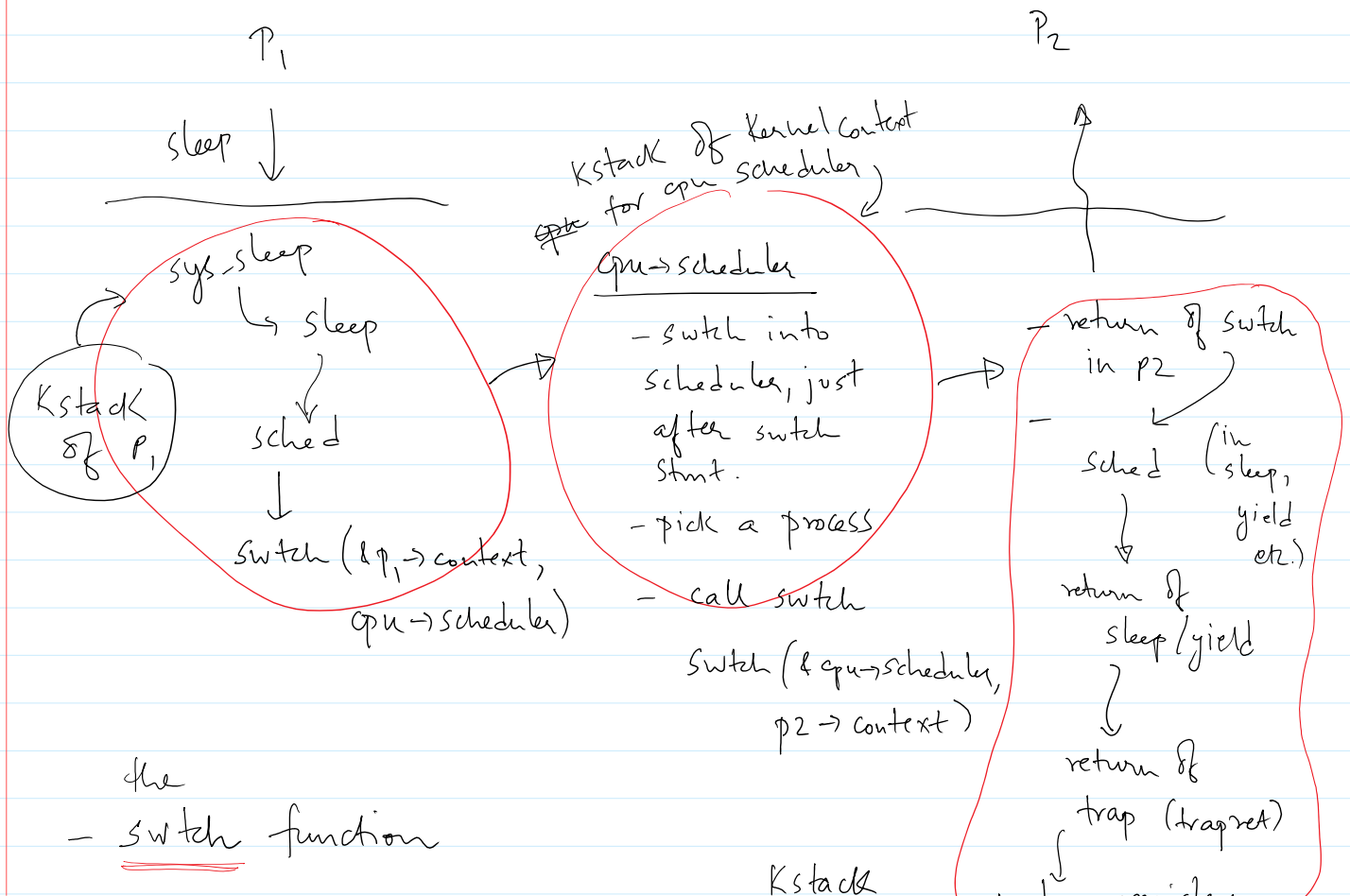
- a process will not voluntarily give up the CPU, usually. it may 'YIELD' at some times (yield, sleep etc.)
- or CPU is switched to another process by the scheduler on some interrupt processing (interrupts, system etc.)
 new calls
- the calls that ~~to~~ are important are sched and switch.
 - to initiate the scheduler
 - to initiate context switch between processes / contexts
- sched is called in exit, sleep, yield
 - sched in turn calls switch.
 - additionally, the gnu scheduler calls switch.
- with xv6, process preemption is a two step process.
 - step 1: process is switched out & kernel scheduler (with different kernel context) is switched in.

step 2: Kernel scheduler picks a process and switches to kernel context of the new process.



~ assume a process 'P1' calls the sleep system call, and its state is updated in the sleep function and sched is called.

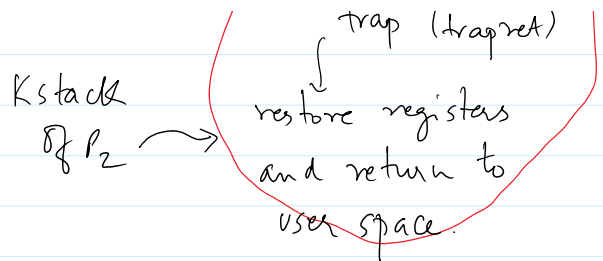
a two-step
after sched, the context switch results in different process context for execution.



the
- switch function

- switch function

(1) `switch (&p → context,
cpu → scheduler)`



- both arguments related to addresses of kernel stack.

- `&p → context` — address of context variable in PCB

`cpu → scheduler` — value of stack pointer of the scheduler top of

~ how does switch switch?

switch

`mov 4(esp), eax` — store 1st argument at (esp+4) in eax
`mov 8(esp), edx` — store 2nd argument (at esp+8) in edx.

push registers

`mov esp, (%eax)` — store current esp in address stored in eax.

`mov edx, esp` — i.e. `p → context = esp`

pop registers

`iret`

switch Kstack (to scheduler) ^{cpu →}

pop registers on new stack and return from switch (in new context).

this is where switch happens!.