

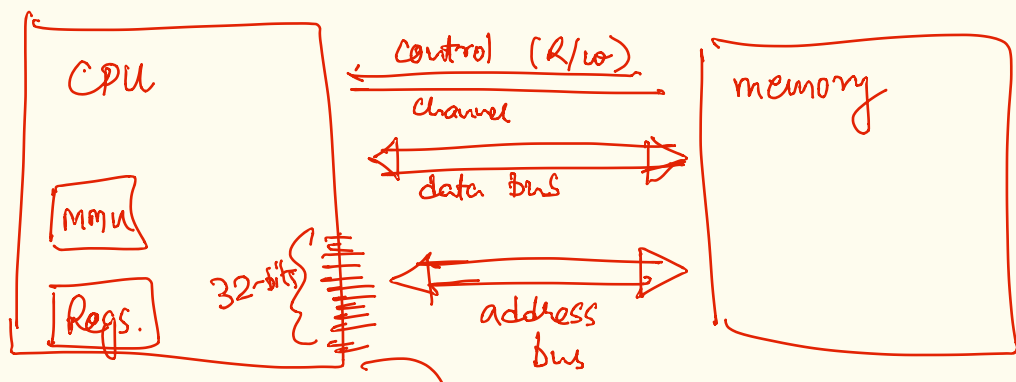
memory virtualization

* CPU virtualization

every program/process "thinks" that it owns the CPU.
 this is a scam!

save & restore

multiplexing / sharing (temporal)



addressable range: 0 to $2^{32}-1 \Rightarrow 4GB$

processes need memory!

code/text and data
 von Neumann model.

fetch-decode-text.

memory virtualization.

defn 1: share/multiplex across processes!

requirements: (i) isolation

(ii) flexible allocation/sharing.

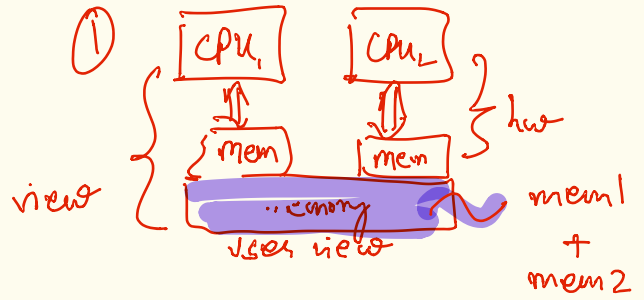
(iii) efficiency

(iv) reusability

(v) transparency

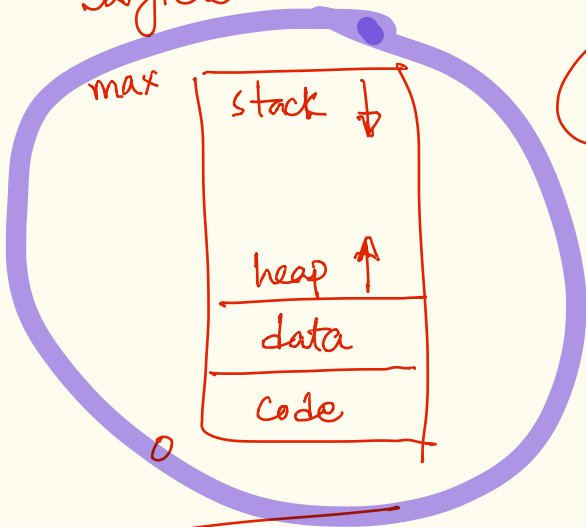
(vi) full addressability.

local vs. far-away allocation.



view of memory for each process.

logical



Q

how to provide this view of memory to each process?

design - 1

- similar to LDE model
- process issues address

↓
trapped/interrupted.

↓
OS issues actual address.

