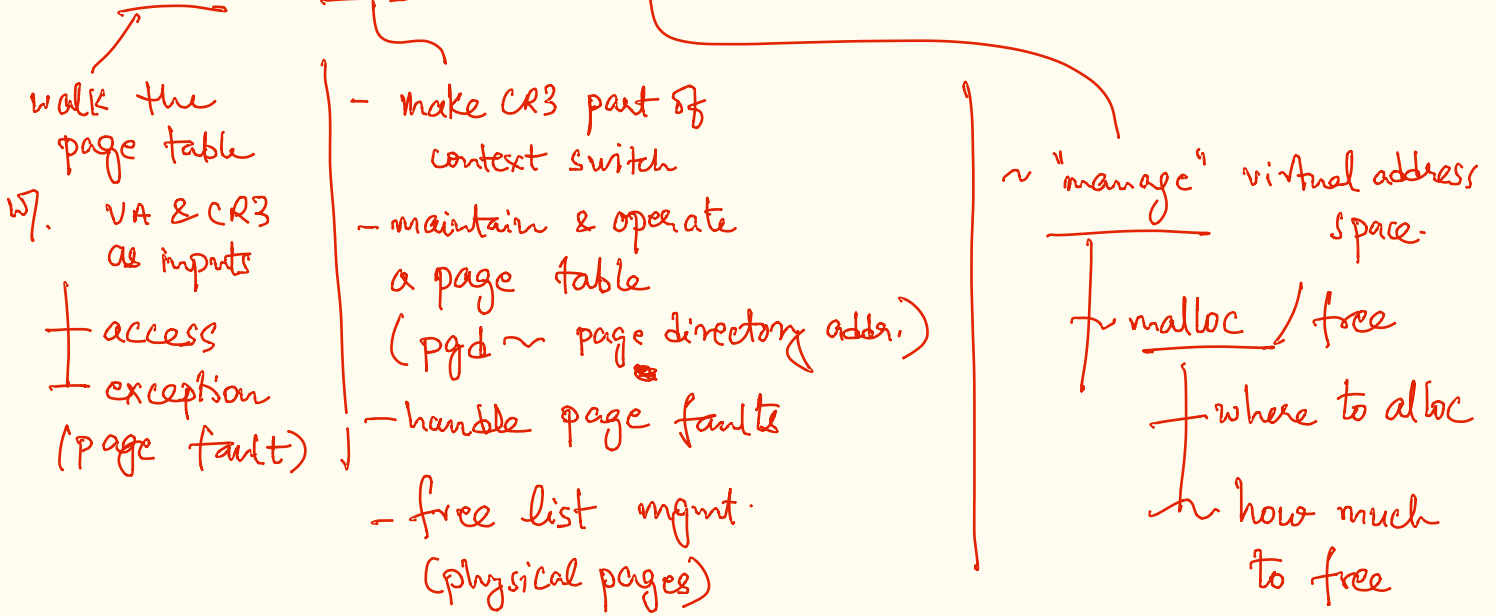


⊛ paging, demand paging, swapping, TLB, address space, (virtual) address space mgmt.

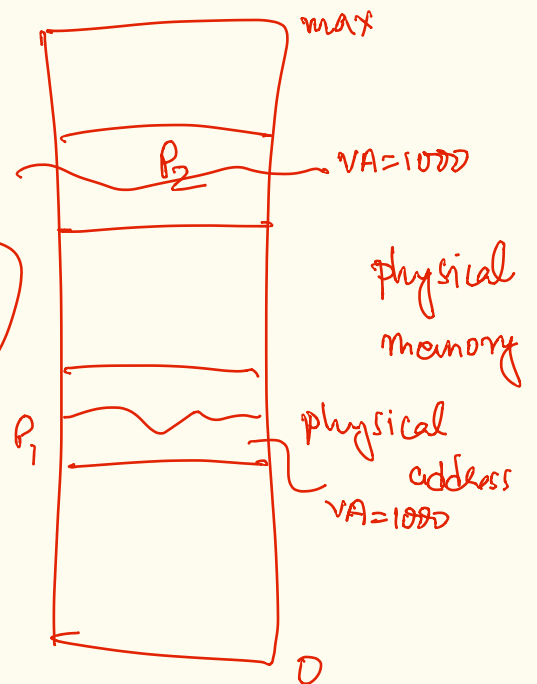
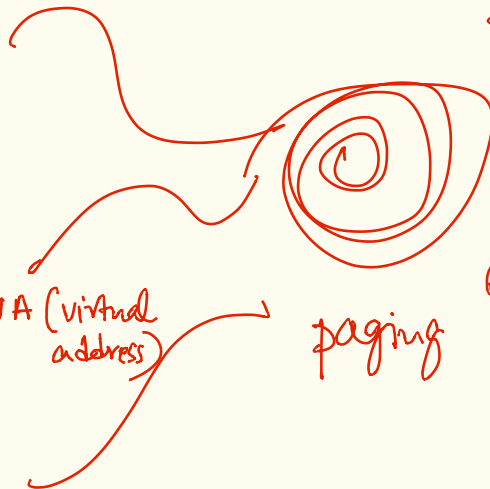
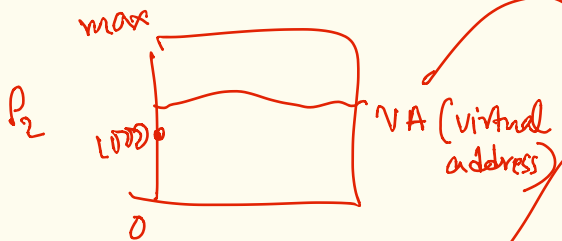
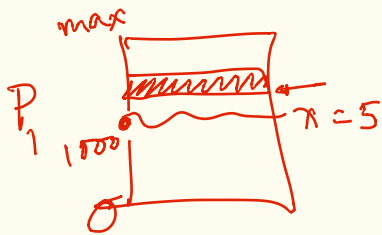
- MMU, OS, process.

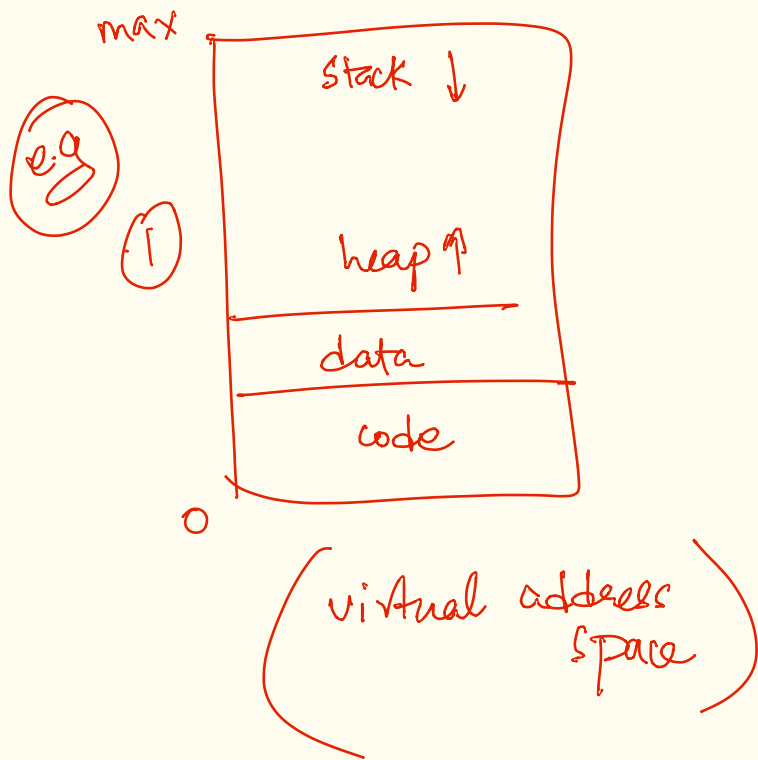


⊛ address space abstraction

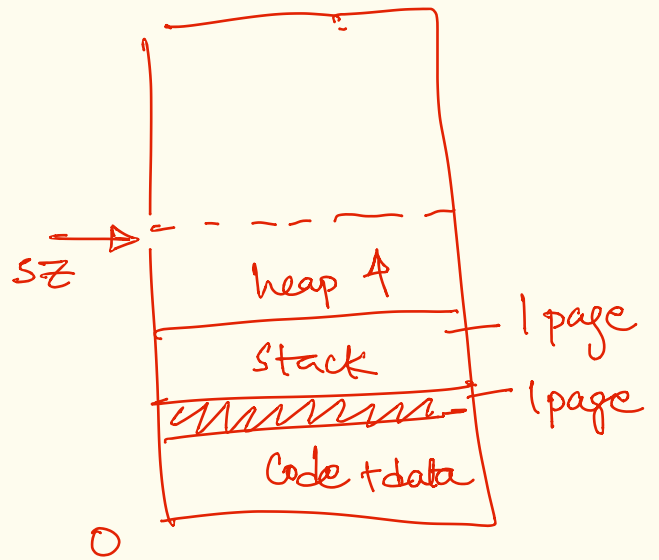
(virtual) each process has a zero-starting, linearly addressable & full addressability (?) on this address range.

process view





② AV6



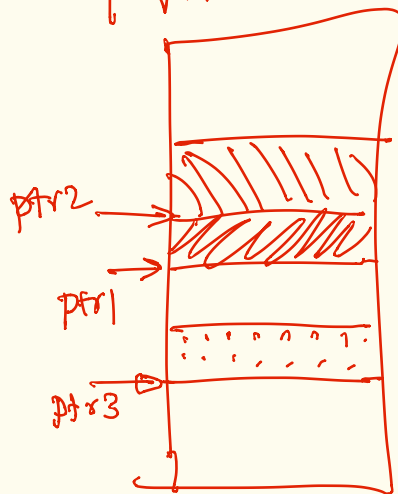
~ paging

— v2p mapping created on VA usage indication/request.

② demand paging

— v2p mapping created only on page fault.

VA.



32-bit address space

4KB pages — 4096 bytes per page

PTE : 4 bytes each. $\frac{\# \text{PTEs}}{\text{page}} = 2^{10}$

VA: 00808080

0000 0000 1000 0000 1000, 0000 1000 0000

