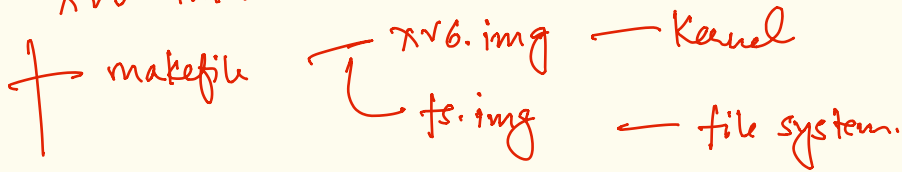


lecture #9

CS 347 | Operating System

relap: xv6 intro.



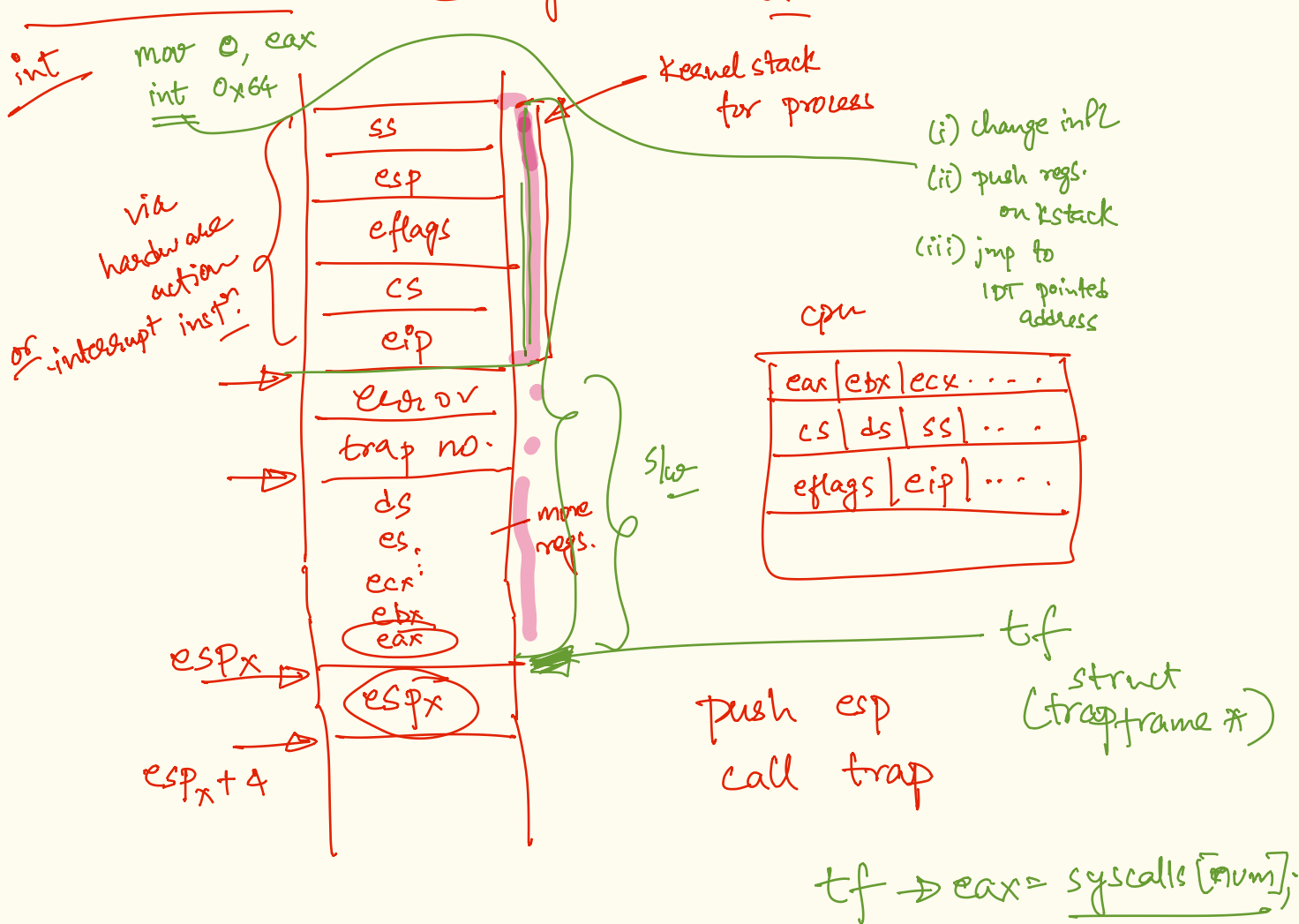
traps.h ~ list of all interrupts.

vectors.S ~ IDT entry for each trap / interrupt vector

trapasm.S ~ contains the entry pt. all traps

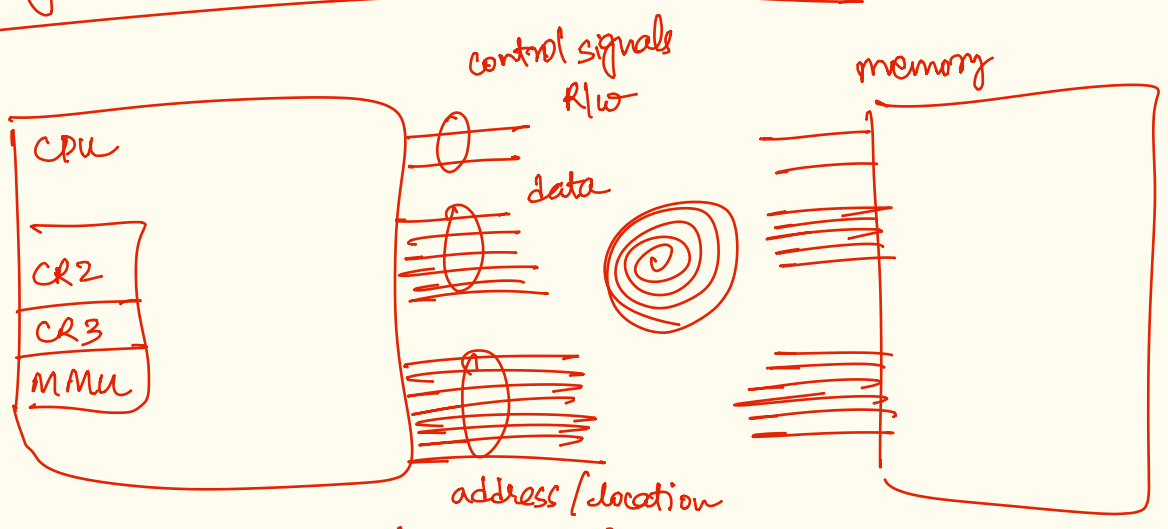
trap.c ~ generic trap handler (trap handling setup)

syscall.c ~ syscall handler.



memory virtualization

M/w



address/location
(address bus) ⇒ 8 bit vs 16 bit vs 32 bit vs 64 bit

↳ length of the address bus.
width CPUs.

32-bit ⇒ 2^{32}