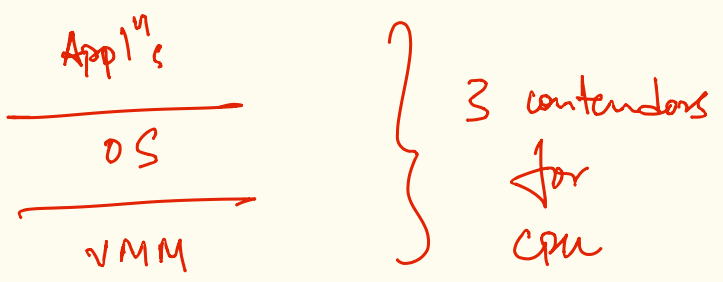


* Popek & Goldberg 1974

Req. for VMM design

- efficiency
- control
- equivalence



Designs of CPU virtualization with VMS by VMMs.

① Trap & Emulate/Virtualize

Native

Ring 3 (user space) App'l's

Ring 0 (kernel mode) OS

VMM

App'l's 3

OS 1

VMM 0

In the x86 ISA

S: set of sensitive instructions

↑ they influence "system" behaviour

↓ they need certain special privileges

⇒ CPU always operates with diff. PL configs.

CPL — current execution PL

⇒ CPL = 0 or CPL < req PL

if not enough privilege generate a trap.

OS handles trap.

* Guest OS issues a write to CR3

- trap!

- VMM can handle the trap.

- X: guest OS key to

X+200: VMM virtualizing CR3

- error checks

- bound checks

Issues

① VMM needs to know semantics

of OS state! e.g.: interrupt handlers.

② sensitive instructions do not generate a trap!

all

C: critical instructions: do not generate a trap on not enough privileges. S which

~ popf ~ which pops top of stack & updates the EFLAGS registers

w/o privileges

bit #9

enable/disable interrupts.

- no trap

- no update.

⇒ breaks equivalence & missed virtualization opportunity.

③ mov %CS, mem locⁿ } examine the 2 LSB bits to find the CPL.
%SS

- sidtr ~ stores locⁿ of IDT to a memory addr.

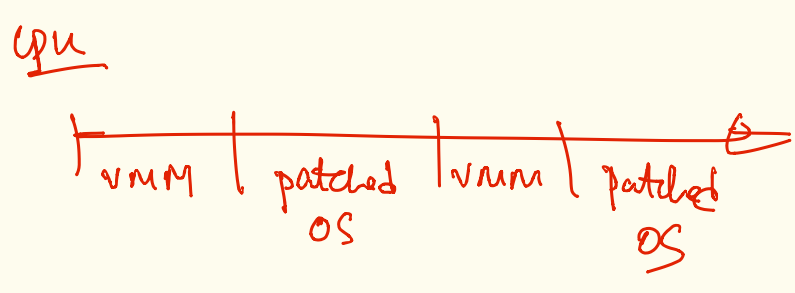
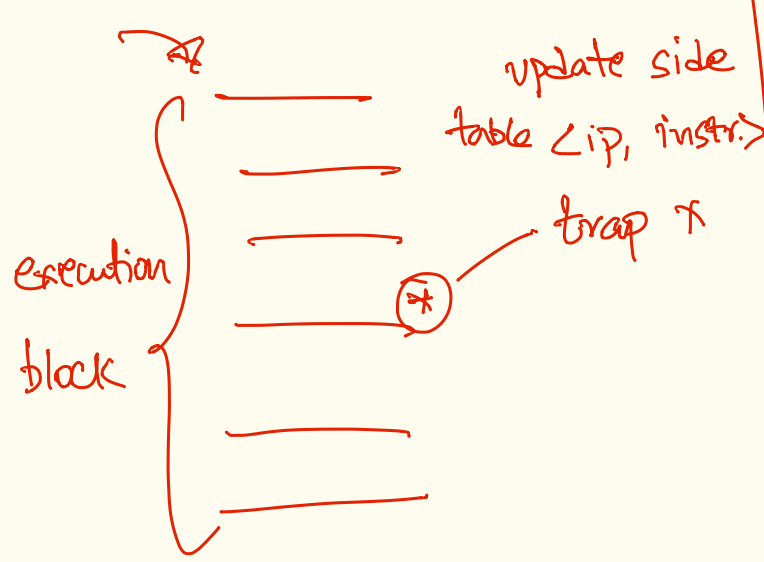
④ info crosses layers with no/invalid semantics.

efficiency → ?

control → x / ?

equivalence - ~~⇒~~ x

② scan-and-patch
binary translation.
= vmware ~~or~~ esx



- equivalence
- control
- efficiency. ?

③ para-virtualization
- xen

~ do not issue critical instructions.
⇒ request hypervisor for all sensitive tasks. the h/w
⇒ (Guest) OS knows that ~~it~~ is being virtualized.

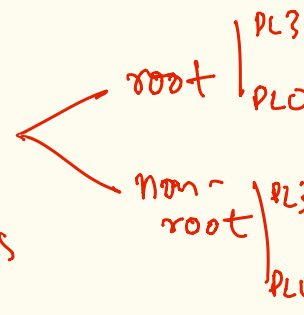
hypercall ~ (ABI)
└ mmu-update (CR3 values, ...)

- equivalence
- control
- efficiency.

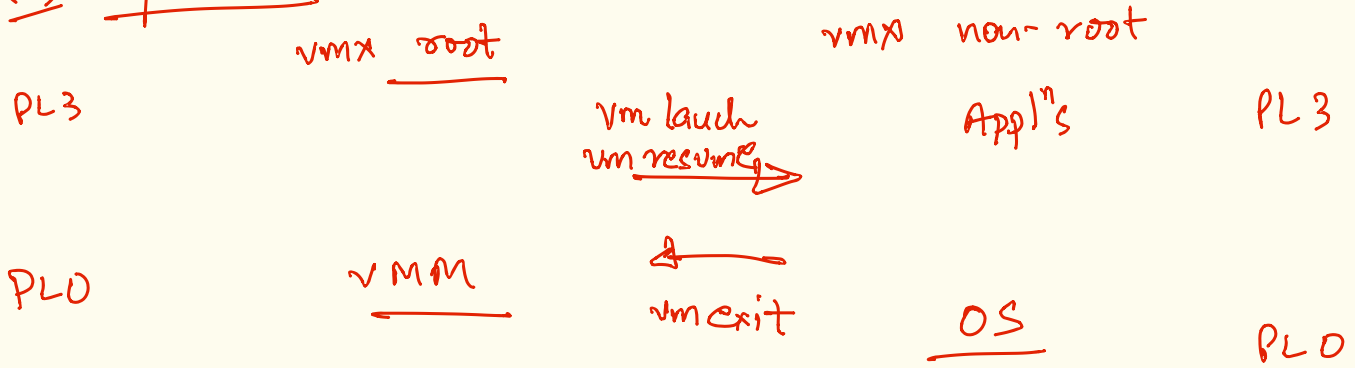
④ Hardware-assisted, virtualization.

Intel VT-X }
AMD - V }
cpu

- (i) vmx modes
- (ii) vmx instructions
- (iii) vmx state



(i) operations



(i) non-root is configured by the VMM.
(execution context)

(ii) program/configure the vmexit conditions.