

# Discovering Dependencies in Courseware Repositories

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by

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## **Abstract**

Nowadays elearning has become popular, especially with the availability of large courseware repositories such as NPTEL and OCW. A variety of e-learning tools and systems are also available. However, suppose a user wants to learn about a particular subject, the search tools typically just return a large number of links to the user in response to his/her query. Many of these are not directly relevant, so the user does not know which links to follow in order to enhance his knowledge.

In this project we have built a system which not only provides the user with the most relevant learning module for his query, but also provides him/her with the relevant pre-requisite and follow-up modules also. This is done by creating a dependency graph of the courseware modules in the repository. We have implemented and tested our system on six courses taken from the NPTEL repository. For effective evaluation of our solution approach, we have not only compared the results for four different heuristics but also compared them with the dependencies determined by an expert in the subject area.



# Contents

<b>1</b>	<b>Introduction</b>	<b>1</b>
1.1	Availability of Repositories . . . . .	1
1.2	Motivation for MTP . . . . .	2
1.3	Goal of MTP . . . . .	2
1.4	Solutions Approach . . . . .	3
1.5	Organization of Thesis . . . . .	3
<b>2</b>	<b>Related Work</b>	<b>5</b>
2.1	Repositories surveyed . . . . .	5
2.1.1	National Program on Technology Enhanced Learning (NPTEL) . . . . .	5
2.1.2	MIT's OCW . . . . .	5
2.1.3	CDEEP IIT Bombay . . . . .	6
2.2	Learning Management Systems . . . . .	6
2.2.1	ATutor . . . . .	7
2.2.2	OLAT . . . . .	8
2.3	Choice of Search Engine . . . . .	8
<b>3</b>	<b>Overview of Solutions</b>	<b>9</b>
3.1	Parsing . . . . .	9
3.2	Indexing text data . . . . .	9
3.3	Analysing the Index . . . . .	10
3.3.1	Refining counts . . . . .	10
3.3.2	Without any Threshold . . . . .	11
3.3.3	Mean Threshold . . . . .	12
3.3.4	Percentage ThreshHold . . . . .	12
3.4	Find out Pre-requisites and Follow-ups . . . . .	13
3.4.1	H1: Take count of words . . . . .	13
3.4.2	H2: Take position of the file . . . . .	13
3.4.3	H3: Take the Average count . . . . .	15
3.4.4	H4: Find correlation . . . . .	16
3.5	Generate DAG . . . . .	16
3.5.1	Refining the Graph . . . . .	16

<b>4</b>	<b>Implementation Details</b>	<b>19</b>
4.1	Parsing with PDFBox . . . . .	19
4.1.1	Using Lucene to Index and Search . . . . .	19
4.1.2	Indexing . . . . .	20
4.1.3	Searching the Index . . . . .	22
4.2	Using LUKE . . . . .	23
4.3	Generating graph with DOT . . . . .	23
<b>5</b>	<b>Experiments</b>	<b>27</b>
5.1	Testing . . . . .	27
5.2	Evaluation of the System . . . . .	40
<b>6</b>	<b>Self-Assessment for the User</b>	<b>41</b>
6.1	Evaluation for the learner . . . . .	41
6.2	Functioning of the System . . . . .	41
6.3	Feedback for the subject matter expert . . . . .	42
<b>7</b>	<b>Conclusion and Future Work</b>	<b>43</b>
	<b>Appendices</b>	<b>45</b>
<b>A</b>	<b>Lucene Scoring Formula</b>	<b>45</b>
<b>B</b>	<b>More Experimental Results</b>	<b>51</b>

# List of Tables

5.1	Requisites generated by our Program . . . . .	28
5.2	Evaluation results . . . . .	40
B.1	Requisites generated by our Program . . . . .	51



# List of Figures

2.1	Screen-shot of NPTEL . . . . .	6
2.2	Screen-shot of MIT's OCW . . . . .	7
3.1	Screen-shot of the workflow . . . . .	10
3.2	Screen-shot of Luke . . . . .	11
3.3	Screen-shot of the Keyword Count table . . . . .	12
3.4	Screen-shot of the TopKWords for each file table . . . . .	14
3.5	DAG for the Computer Networks course . . . . .	18
4.1	Architecture of Lucene [Lucene] . . . . .	20
4.2	Screen-shot of Luke . . . . .	23
5.1	DAG for the Computer Networks course- Heuristic H3 . . . . .	38
5.2	DAG for the Computer Networks course - Heuristic H4 . . . . .	39
B.1	DAG for the Embedded Systems course - Heuristic H4 . . . . .	65
B.2	DAG for the Artificial Intelligence course - Heuristic H4 . . . . .	66
B.3	DAG for the Software Engineering course - Heuristic H4 . . . . .	67
B.4	DAG for the Operating Systems course - Heuristic H4 . . . . .	68
B.5	DAG for the Systems Analysis and Design course - Heuristic H4 . . . . .	69



# Chapter 1

## Introduction

eLearning is becoming quite important due to the emerging technologies. It is used as a means of learning by all categories of people from science, engineers, working professionals etc. A number of benefits can be obtained through the use of eLearning:

- Increases conceptual understanding through the use of interactivity and animation, and through the use of audio and video.
- Learners can learn at ones own pace and time. This is especially important for learners who cannot attend school physically during the regular hours.
- The system may retain records of discussion and allows for later reference through the use of threaded discussion on bulletin boards.
- eLearning permits instructors to develop materials using the world-wide resources of the Web.

For more details about elearning and its importance see [Sie04].

### 1.1 Availability of Repositories

There are huge amounts of data available on the World Wide Web (WWW). Different types of tools are available which help users in different ways from simply viewing the available content to creating lessons with the help of authoring tools. Many institutes/Universities have also made their courses open source and have published them on the web. However, the content is scattered over the WWW in the form of wikis, e-books, tutorials [GC05] etc. Some examples of content repositories are:

**MIT OpenCourseWare** [MIT] is an idea developed by MIT to advance knowledge and educate students in science, technology, and other areas also. There are over 1,800 courses in 33 academic disciplines. This content is available for download freely in the form of MIT's Open Course Ware and there is a dedicated website for this dissemination [MIT].

The **National Programme on Technology Enhanced Learning (NPTEL)** [NPT], a project

funded by the Ministry of Human Resource Development (MHRD), India, was initiated in 1999 to pave the way for introducing multimedia and web technology to enhance learning of basic science and engineering concepts. In order to facilitate the distribution of course material, two modes of operation have been suggested, namely, digital video lectures of courses and web based courses.

There are other repositories also including **CDEEP IIT Bombay**[CDE], **Stanford University's open education initiative**[Sta] and **utahs university e-learning program**[Uta] etc.

## 1.2 Motivation for MTP

Because of emerging technologies today, students have more options in choosing ways of learning[Kha05]. Many academic institutions/organizations have their own search facilities added to their content repository web sites [MIT], [NPT], [Sta]. However, suppose a user wants to learn about a particular subject, the search tools typically just return a large number of links to the user in response to his/her query. Many of these are not directly relevant, so the user does not know which links to follow in order to enhance his knowledge.

We looked upon the search facility provided by NPTEL, CDEEP, and MITs OCW. We acted as a user and gave queries to learn about topics such as TCP/IP, Ethernet etc. Although these topics are covered in courses available at these websites, the search facilities did not give the desired results. In this project we have built a system which not only provides the user with the most relevant learning module for his query, but also provides him/her with the relevant pre-requisite and follow-up modules also. There is no existing tool which actually serves this purpose.

## 1.3 Goal of MTP

Given a set of lecture files from a courseware repository, we aim to build a system that provides the user the most relevant answer (course module) to his query. We also create a dependency graph for the entire course so that the user can refer to the previous and advanced topics as required. We do not assume any prior knowledge of the ordering of the concepts covered in the modules.

We have divided our system into three parts: providing learning module to the user, feedback for the course co-ordinator and the proposed design of the overall system. We mainly focus on providing the user the most relevant learning module for the query entered. We also provide the prerequisites and follow ups for that topic. User can always refer to the prerequisites in case of any doubts and follow-ups to enhance his/her knowledge further. The user can judge himself by taking the self-evaluation quiz. The quiz will consist of multiple choice questions of that field.

## 1.4 Solutions Approach

Given the contents of a course from a repository (NPTEL in our case), we do the following:

1. We have used the Lucene, open source information retrieval library, in order to index the content available.
2. We use this index in order to not only retrieve the relevant documents but also to determine the dependencies among the modules.
3. Different strategies (heuristics) are used to order documents and then to arrange them in the order of pre-requisites and follow ups.
4. We have implemented and tested our system on several courses taken from NPTEL. For effective evaluation of our solution approach, we have not only compared the results for the various heuristics but also compared them with the dependencies as determined by an expert in the subject area.
5. Subsequently we have proposed a mechanism to present questions to the user in the form of a self-evaluation quiz. The response of the quiz may be used to provide feedback not only to the user but also to the content creator.

## 1.5 Organization of Thesis

In chapter 2, we look on different content repositories available emphasizing on NPTEL, CDEEP and MITs OCW in particular. In chapter 3 we describe the different solution approaches followed to implement our system. Chapter 4 gives the implementation details. In chapter 5 we show the different experiments conducted to evaluate the performance of the system. In chapter 6, we present the quiz and feedback module. In chapter 7, we present the conclusions and future extensions.



# Chapter 2

## Related Work

E-learning is used interchangeably in a wide variety of contexts. E-Learning can be used in academic settings as well as for corporate training [Kha05], in this report, we focus only on the use of eLearning in academic settings. The range of eLearning software spans the following extreme end points: using simple presentation tools such as Microsoft PowerPoint; the use of animation in tools such as JAWAA ; intelligent tutoring systems such as Andes etc.

### 2.1 Repositories surveyed

We have surveyed a number of content repositories available on different sites. These provide access to all of it's course's contents free of cost.

#### 2.1.1 National Program on Technology Enhanced Learning (NPTEL)

The National Programme on Technology Enhanced Learning (NPTEL) [NPT], a project funded by the Ministry of Human Resource Development (MHRD) was initiated in 1999 to pave the way for introducing multimedia and web technology to enhance learning of basic science and engineering concepts. In order to facilitate the distribution of course material, two modes of operation have been suggested, namely, digital video lectures of courses and web based courses. E-Learning material are being created in such a form that it can be expanded and updated continuously. Simple course management packages that provide features like e-mail queries by students, bulletin board and Frequently Asked Questions (FAQ) are being incorporated.

There is no efficient search facility provided in NPTEL. The search option provided just works for the course names and one can't get any information about a particular query if it does not appear in the course name.

Sample screen shot for the query TCP/IP is shown below in the Figure 2.1

#### 2.1.2 MIT's OCW

MIT OpenCourseWare is an idea developed by the MIT faculty to advance knowledge and educate students in science, technology, and other areas of scholarship to best serve the world. In 2000, MIT published the first proof- of-concept site in 2002, containing 50 courses. Now,

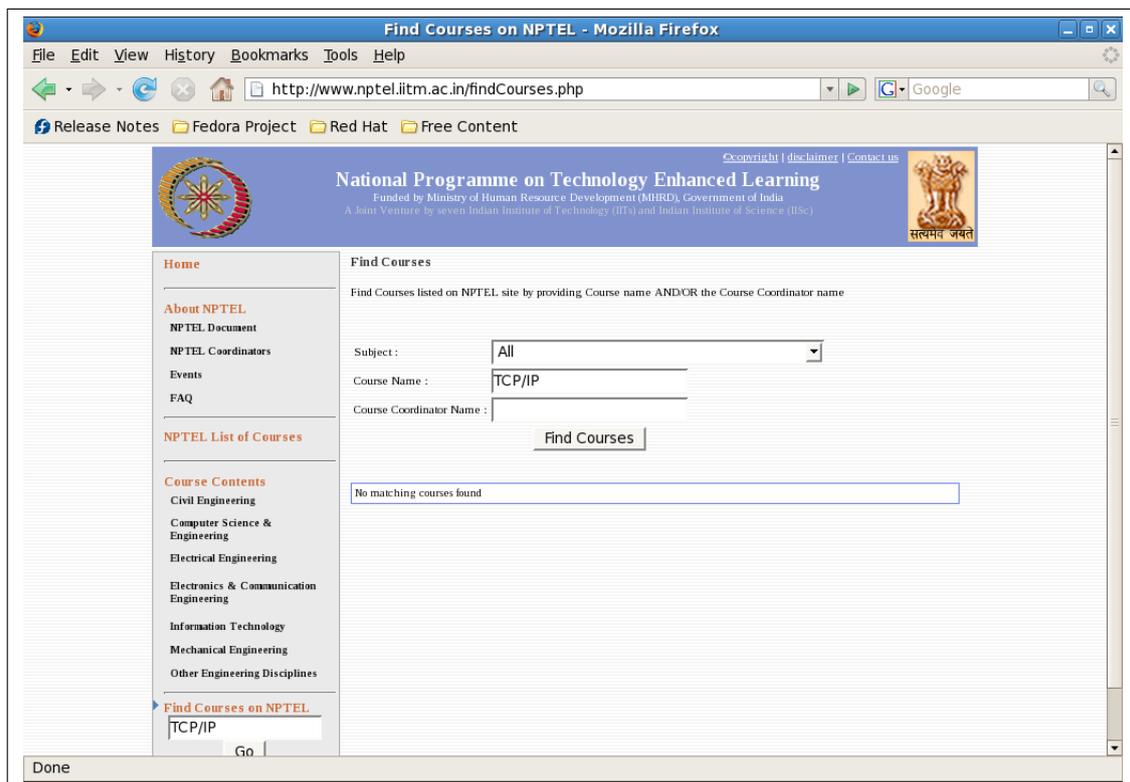


Figure 2.1: Screen-shot of NPTEL

there are over 1,800 courses in 33 academic disciplines. This content is available for download freely in the form of MIT's Open Course Ware and there is a dedicated website for this. Most of the content has been made available in the form of PDF documents. Some of these contain scans of material handwritten or drawn by the instructor and image captures of the blackboard contents.

The output returned 82 results for TCP/IP. This is really not helpful for a new user. It will be hard to decide out of the given 82 results which one he/she should follow and in which order.

### 2.1.3 CDEEP IIT Bombay

The Centre for Distance Engineering Education Programme has been established to make IIT's courses available to the Student community at large. There are courses available in fundamental subjects, advance subjects. Different activities of CDEEP include laboratory demonstrations, transmitting classroom lectures live to the destination, develop web-based course material, tutorials, assignments, studio recording of lectures etc [CDE].

Currently, there is no search facility in CDEEP which can display for a particular query that whether it exist in any of the courses which are there in CDEEP curriculum or not.

## 2.2 Learning Management Systems

A Learning Management System (LMS) is a set of software tools designed to facilitate user learning interventions. Most LMSs we surveyed are web-based to facilitate 'anytime, any

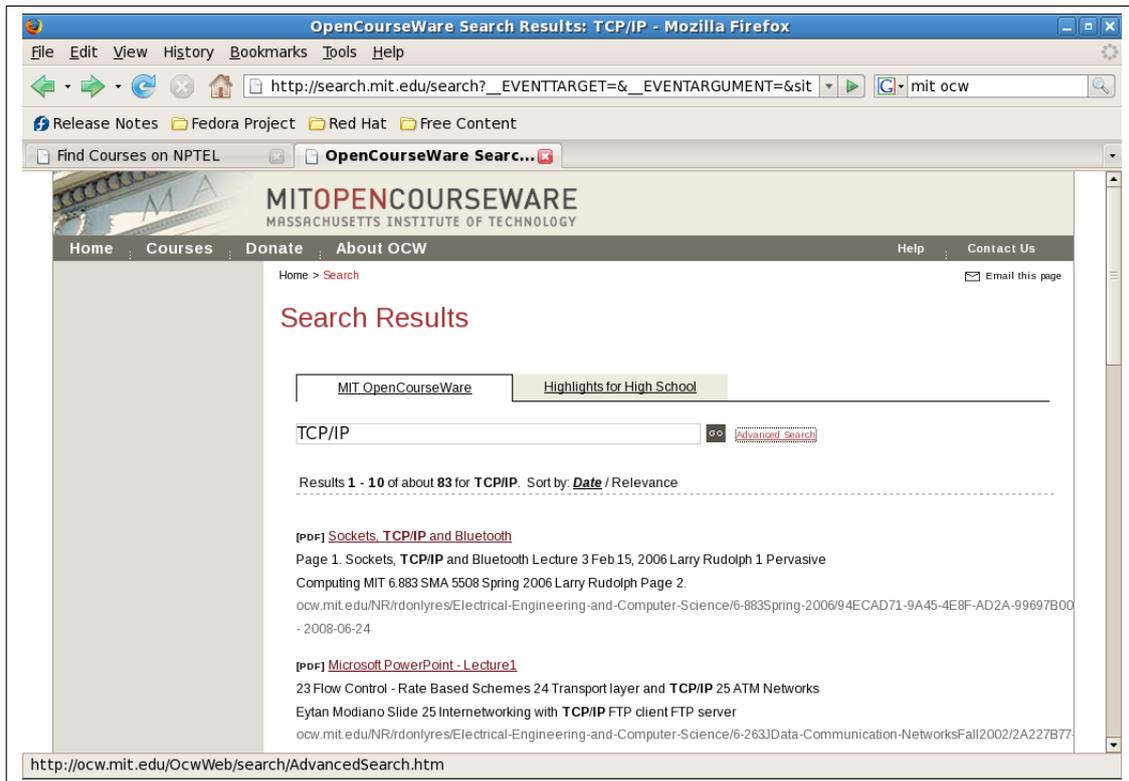


Figure 2.2: Screen-shot of MIT's OCW

place, any pace ' ' access to learning content and administration. LMSs are based on a variety of development platforms, from Java architectures to Microsoft.net and usually employ the use of a database back-end [Lea]. The learning environment used by universities and colleges allow instructors to manage their courses and exchange information with students for a course that in most cases will last several weeks and will meet several times during those weeks. While in case of the corporate world a course may be much shorter, completed in single instructor-led or online session.

### 2.2.1 ATutor

**ATutor** is an Open Source Web-based **Learning Management System** [ATu].

ATutor is used in various contexts, including online course management, continuing professional development for teachers, career development, and academic research. ATutor is used internationally and has been translated into over fifteen languages with support for over forty additional language modules currently under development.

- Two, of many, accessibility features in the system are text alternatives for all visual elements, and keyboard access to all elements of the program. With these features, a blind person can listen to the entire interface of the system with the help of a screen reader, and he or she can access the system without needing a mouse.
- ATutor is also designed for adaptability to any of several teaching and learning scenarios. There are four main areas that reflect this design principle: themes, privileges, tool

modules, and groups.

The ATutor theme system to allow administrators to easily customize the look and layout of the system to their particular needs. Themes are used to give ATutor a new look, to give categories of courses their own look, or to provide multiple versions of ATutor on a single system, from which users could choose one as a preference setting.

### 2.2.2 OLAT

OLAT is the acronym for Online Learning and Training. It is a web application - a so called Learning Management System which supports any kind of online learning, teaching, and tutoring [OLA]. OLAT is free open source software OLAT has a lot of features for e-learning platforms such as content managing, forums, file discussions, quizzes with different kinds of questions, surveys, chat, submission modules etc.

There are other open source LMSs also available such as Moodle, SCORM, eFront etc.

Every type of LMS supports the following features:

- Manage users, roles, courses, instructors, and facilities and generate reports.
- Course calendar
- Learner messaging and notifications
- Assessment/testing capable of handling student pre/post testing
- Display scores and transcripts

## 2.3 Choice of Search Engine

Each search engine has multiple characteristics that differentiates it from the other engines. Each of the search engines can be characterized by the features they implement as well as the performance they have in different scenarios. There are many search engines available based on a number of factors such as open source, information type, business, metasearch engines etc. [sea]. Of these, we have chosen to use Lucene [HG04], for the following reasons:

1. Lucene is not a standalone search application. Instead it is an API [Luc]. It can be integrated in an application as a search and indexing API. Nutch is one popular search engine that uses Lucene. Nutch does the functions of crawling and parsing and uses Lucene for indexing. However, in our case, we do not need all the features of Nutch. The API provided by Lucene are sufficient.
2. Lucene has been completely written in Java. Our system is also built in Java, hence it is easy to integrate.
3. Lucene does not have any crawling and HTML parsing functionality. We do not require these as we are targetting specific repositories. Also, we do not require any front-end functionalities.

# Chapter 3

## Overview of Solutions

As mentioned earlier, we have currently taken NPTEL as our target content repository. In the examples described in this chapter we will take the course Computer Networks from NPTEL as a running example. This course has 40 pdf files in it. For every course what we have to do is:

1. Parse the pdf files to get textual data, using PDFbox.
2. Index the text files, using Lucene.
3. Analyze the keywords and their frequencies in the index, using LUKE.
4. Find pre-requisites and follow-ups for every file, using heuristics developed in this project.
5. Generate a DAG for the whole course, using DOT.

The step-by-step workflow of the processing of each step is shown in Figure 3.1. Solutions for all the steps are described one by one in detail.

### 3.1 Parsing

Lucene requires the pdf files to be converted to text before indexing them. We made use of a Nutch utility called PDFbox to parse the pdf files. PDFBox is a Java library for extracting text contents from existing PDF documents. The implementation details of invoking PDFbox are described in the next chapter.

### 3.2 Indexing text data

We can embed Lucene easily into our applications and implement indexing and searching functionality. For specifically giving the right learning module we need to first index it. The basic code involving all the classes needed to index is given in the Appendix B. The main classes used for indexing are shown below:

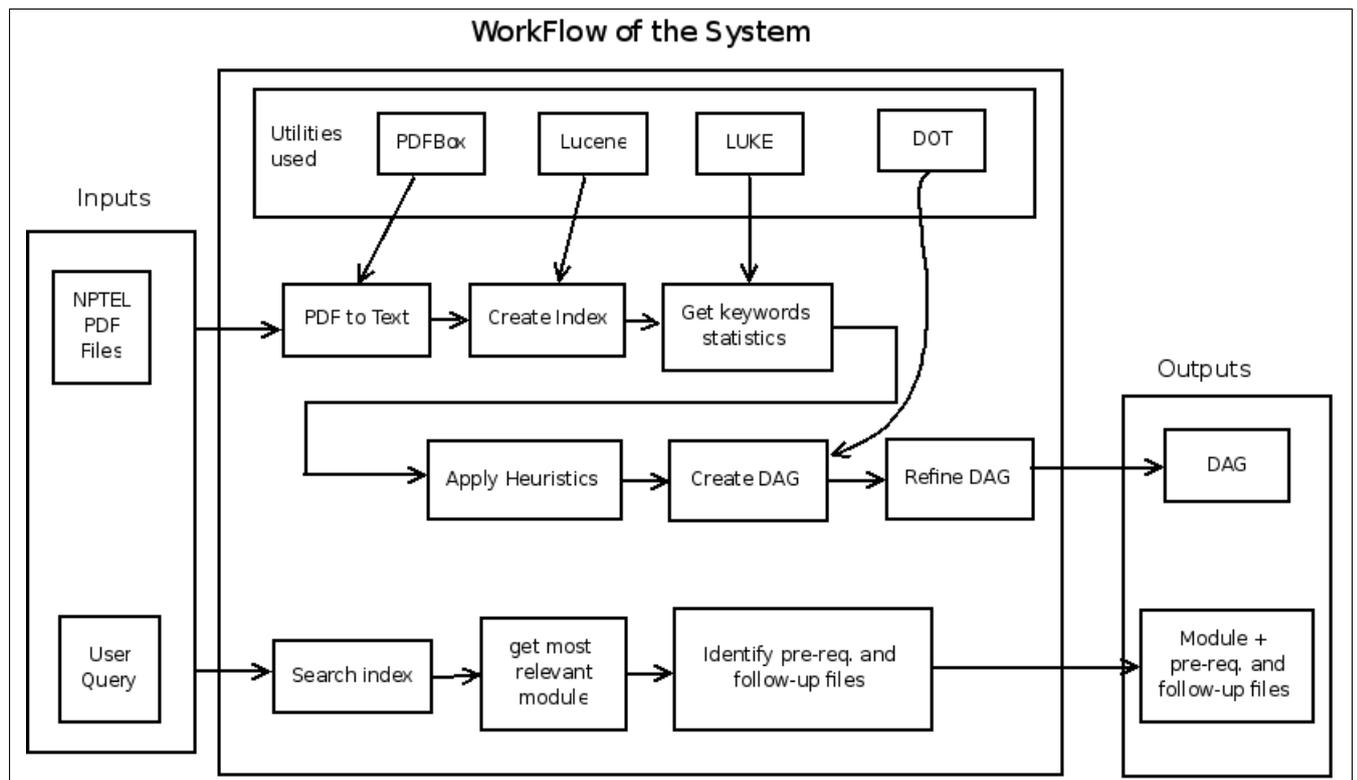


Figure 3.1: Screen-shot of the workflow

Listing 3.1: simple lucene index

```
Analyzer analyzer = new StopAnalyzer();
StringBuffer contents = new StringBuffer();
Directory directory = FSDirectory.getDirectory("/home/nidhi/Desktop/Computer_Networks/index");
IndexWriter iwriter = new IndexWriter(directory, analyzer, true);
iwriter.setMaxFieldLength(25000);
```

### 3.3 Analysing the Index

Once we have indexed our text data, we can print the index in different number of types. We can print the index according to the frequency of the terms, and even highlight the occurrence of the terms in the content. Lucene has its own default scoring mechanism on the basis of which it calculates the score of the documents and displays them in some order of preference. In our experiments we have taken the count of the index terms, i.e., the number of times each term occurs in each file. Then further processing is performed.

#### 3.3.1 Refining counts

We give the 40 lecture files (after converting to text) of the course Computer Networks course to Lucene. Lucene indexes the text files and allows us to view and print the index with the help

of LUKE (ref. Chapter 4 for more details). From there we get a collection of index terms (in the same way as it is in a book). We remove the non-technical English words, as Lucene indexes all the text data except some particular English words. The words which are removed from this course can also be removed from other courses so that we need not again and again remove them from other course's indexes.

A sample screenshot of LUKE output for the course Computer Networks is given below in Figure 3.2. Then we use the following strategies for refining the counts of the keywords.

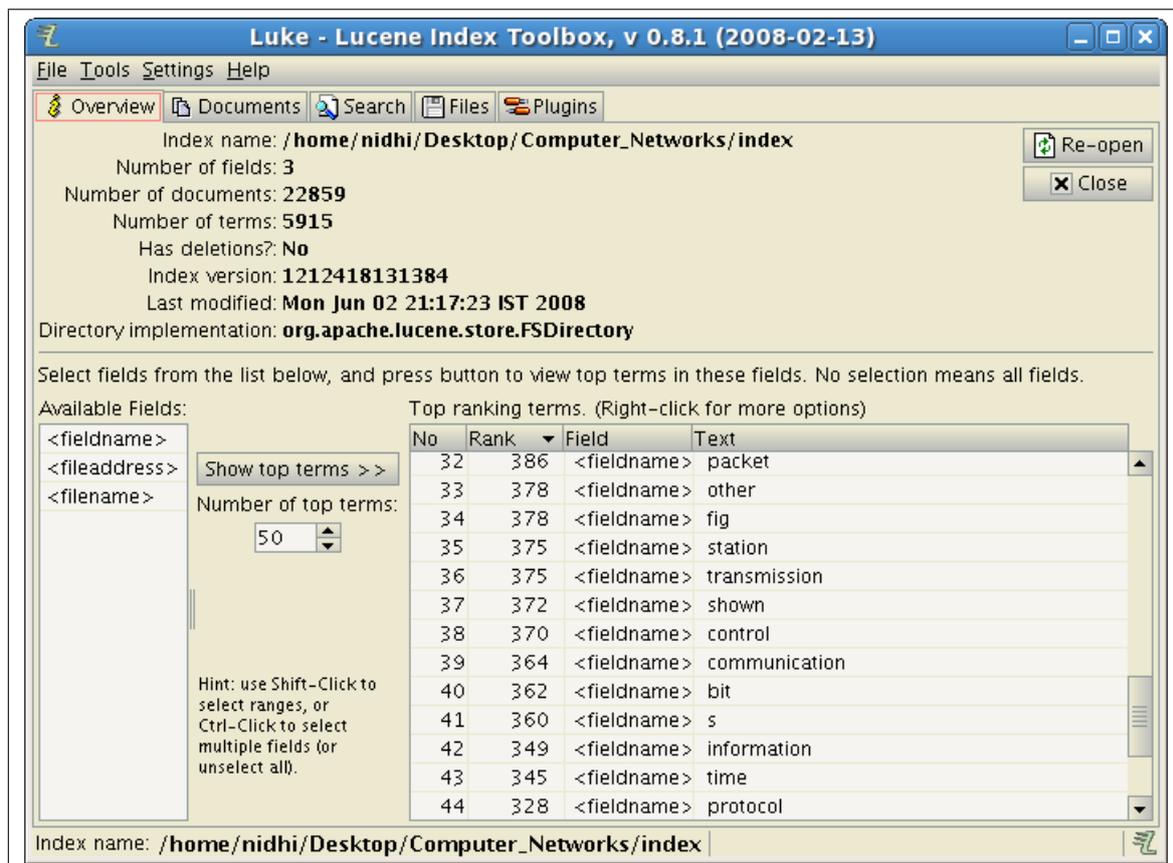


Figure 3.2: Screen-shot of Luke

### 3.3.2 Without any Threshold

In this strategy, we simply take the count of all keywords in each file. Note that we do not take the scores of the keywords in the document given by Lucene. Instead we have taken the counts of the keywords in the document as this gives more accurate results.

A sample screen shot for the table showing counts of all keywords is given in Figure 3.3.

It is not sufficient to simply use the absolute values of the counts, since these could be misleading. For example, in Figure 3.3, highest count of OSI is 58 and there are other smaller counts such as 2, 1, 2, 3 etc. These counts are not meaningful because there are other keywords in these files whose counts are much higher. So, we need not keep those value. So, we need to apply some kind of normalization in order to capture the relative importance of the keywords.

	EG	EH	EI	EJ	EK	EL	EM	EN	EO	EP	EQ	ER	ES	ET	EU	EV	EW	EX	EY
1	networking	node	nor	nrz	nyquist	optical	orbit	oriented	osi	ospf	packet	parity	payload	pcm	pdu	physical	piconet	polling	preamble
2	6	2	0	0	0	5	0	1	2	1	4	0	0	0	0	1	0	0	0
3	18	1	11	0	0	0	0	15	58	0	1	1	0	0	0	16	0	0	0
4	0	0	0	0	0	9	3	0	0	0	0	0	0	0	0	0	0	0	0
5	0	1	0	0	0	15	7	0	0	0	0	0	0	0	0	2	0	0	0
6	0	0	0	0	72	1	0	0	0	0	0	0	0	0	0	0	0	0	0
7	0	0	0	60	18	0	0	0	0	0	0	0	0	100	0	0	0	0	0
8	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
9	0	0	11	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0
10	0	0	0	0	0	3	0	0	0	0	0	0	0	0	0	1	0	0	0
11	0	0	0	5	0	0	0	24	0	0	0	3	0	0	11	2	0	0	18
12	0	0	0	0	0	1	0	0	1	0	0	86	0	0	0	0	0	0	0
13	0	1	0	0	0	0	0	0	0	0	1	0	0	0	29	0	0	0	0
14	0	0	0	0	0	0	0	1	1	0	0	0	0	0	0	0	0	10	0
15	0	6	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0
16	0	16	0	0	0	0	0	1	0	1	13	0	2	0	0	0	0	0	0
17	0	0	0	5	0	25	0	1	2	0	0	6	50	0	0	4	0	0	0
18	6	2	0	0	0	0	0	3	3	0	8	0	0	0	0	7	0	0	0
19	0	1	0	0	0	0	0	1	3	0	2	0	2	0	5	7	0	0	0
20	50	0	22	0	0	0	0	15	5	0	3	0	36	0	23	7	0	0	0
21	0	10	0	0	0	2	0	3	0	0	1	0	0	0	0	0	0	0	0
22	0	20	0	0	0	0	0	0	0	0	6	0	0	0	0	0	0	73	0
23	0	0	0	0	0	5	0	0	3	0	0	0	0	0	11	5	0	0	36
24	0	9	0	0	0	0	0	0	0	0	5	0	0	0	5	2	0	0	0
25	0	3	0	10	0	28	0	0	5	0	0	0	0	0	0	8	0	0	45
26	6	0	0	20	0	3	0	3	0	0	2	0	2	0	0	13	0	0	0
27	0	0	0	0	0	0	0	0	3	0	1	0	2	0	0	3	0	0	0
28	0	0	0	0	0	0	0	7	0	0	3	0	0	0	11	1	100	10	0
29	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
30	0	0	0	0	0	1	92	3	0	0	0	0	0	0	0	0	0	5	0
31	0	0	0	0	0	0	0	0	1	0	1	0	0	0	0	4	0	0	0

Figure 3.3: Screen-shot of the Keyword Count table

### 3.3.3 Mean Threshold

We initially tried a simple idea of taking the Mean of the counts for each keyword and ignoring values that are below the mean. Similarly, we can take the Mean of counts of the keywords in each file and ignore keywords that are below the mean.

For example, if we take a look at the counts of the term "csma", it has the maximum count of 48 and minimum count is 1. Other counts for the keyword Frame are 1, 2 and 7. So we take the mean of all the values and ignore the values which are less than the mean. These values are simply assigned zero.

However, this idea does not work when there are some keywords which are not occurring in more than 2-3 files. Their counts in those files are also less. Those values will be discarded due to the mean threshold. But we need to retain such values because there are no other modules for those keywords. Hence we do not use the Mean Threshold idea but modify it to the Percentage Threshold as described below.

### 3.3.4 Percentage Threshold

In this strategy, instead of taking the Mean of the counts for each keyword and ignoring values that are below the mean, we normalize the values. We take the sum of the counts of all the occurrences of that keyword. For each occurrence, we replace its absolute count with its percentage from the total. As a result of percentageThreshold, pre-requisites for the query "OSI" are "Internet Protocol", "Transport and Application Layer Protocols" and "Layered Network Architecture".

Subsequently, the values whose percentage count is less than 10 are discarded. For more explanation refer to chapter 5.

## 3.4 Find out Pre-requisites and Follow-ups

The lecture modules are given in a sequential order. We have not assumed any other information about the dependencies of the concepts in the course. In order to facilitate the learning of user, we now have to give him pre-requisites and follow-up modules also for his query. We now describe a few heuristics used to identify these pre-requisites and follow-ups.

### 3.4.1 H1: Take count of words

For a given file we take its topKwords. For those topKwords we take the topKfiles. For each file in those topKfiles, we simply check for how many keywords is the given file is appearing in the topKfiles. After that we sort all the topKfiles (without duplicates) and assign the count. Then we arrange the files in ascending order of the counts. Those files whose module numbers are less than the module number of the file for which we are finding pre-requisites, are assigned as pre-requisites. Those whose module numbers are greater are assigned as the follow-ups.

---

**Algorithm 1** Heuristic 1

---

- 1: Take count of each Keyword in each file.
  - 2: Store keywords as columns and files as rows.
  - 3: For each file get topKkeywords
  - 4: For each keyword
  - 5: Sort the file entries and get unique files
  - 6: Assign weight to each file based on sum of counts of all keywords appearing in it.
  - 7: Order the files according to their weights.
  - 8: For files whose index = 1 to  $i - 1$ ; get the topK files according to weight.
  - 9: For files whose index  $> i$ ; get the topK files according to weight.
- 

The top 3 files whose counts are less than the serial number of the given file will be considered as the pre-requisites. In the same way 3 top files whose counts are greater than the given file will be given as the follow ups.

The Figure 3.4 below shows a view of topkWords for each file:

### 3.4.2 H2: Take position of the file

With the above approach it was the possibility that the file which was more important was not appearing at the correct position and in the correct order. So what we decided that instead of just taking the count, we shall also consider the position of the file in the topKfiles. We will follow the simple formula  $c = k - p + 1$ ; where  $c$  will denote the final count of the file.  $K$  denotes the total top files (in our case  $k = 5$ ).  $p$  will denote the position of the file (varies from 1 to 5).

	A	B	C	D	E	F	G	H	I	J	K
1	M1L1.txt	man	wan	multimedia	remote	internetworking	broadcast	speed	voice	spanni	
2	M1L2.txt	osi	architecture	session	transport	adaptive	application	ftp	telnet	connec	
3	M2L1.txt	sine	domain	interval	amplitude	spectrum	analog	digital	broadband	signal	
4	M2L2.txt	broadband	antenna	cable	mhz	ghz	signaling	satellite	repeater	attenu	
5	M2L3.txt	nyquist	distortion	attenuation	amplifier	baud	channel	filter	mhz	delay	
6	M2L4.txt	pcm	delta	nrz	analog	decoder	digital	modulation	nyquist	synchr	
7	M2L5.txt	modulation	amplitude	antenna	carrier	analog	signal	amplifier	rf	recove	
8	M2L6.txt	constellation	psk	fsk	amplitude	modulation	baud	rf	nor	analog	
9	M2L7.txt	tdm	fdm	multiplexing	division	demultiplexing	slot	multiplexer	buffer	asynch	
10	M3L1.txt	stuffing	modem	synchronization	dce	baud	oriented	asynchronous	preamble	termin	
11	M3L2.txt	hamming	parity	crc	burst	cyclic	error	checksum	bits	bit	
12	M3L3.txt	arq	sliding	window	retransmission	buffer	sender	pdu	transmitter	receiv	
13	M3L4.txt	hdlc	asynchronous	ibm	retransmission	duplex	recovery	polling	control	symme	
14	M4L1.txt	switching	division	circuit	switch	mesh	tdm	bus	duplex	speed	
15	M4L2.txt	switching	datagram	circuit	message	node	packet	destination	route	vci	
16	M4L3.txt	vt	sonet	multiplexer	payload	byte	terminal	transport	optical	demult	
17	M4L4.txt	isdn	dce	pvc	segmentation	session	synchronize	recovery	circuit	termin	
18	M4L5.txt	relay	pvc	isdn	wan	dce	connection	frame	circuit	byte	
19	M4L6.txt	vpi	atm	vci	cell	multimedia	networking	payload	connectionless	bursty	
20	M5L1.txt	mesh	topologies	star	topology	repeater	bus	utp	reliability	directi	
21	M5L2.txt	polling	aloha	contention	csma	cd	load	node	mac	delay	
22	M5L3.txt	preamble	synchronize	cable	ethernet	mbps	mac	utp	cd	pdu	
23	M5L4.txt	ibm	token	ring	bus	cd	contention	csma	recovery	utp	
24	M5L5.txt	fddi	bypass	counter	preamble	ring	optical	token	segmentation	ism	
25	M5L6.txt	gmii	gigabit	ethernet	hub	utp	signaling	duplex	carrier	mbps	
26	M5L7.txt	fhss	wired	dsss	bss	infrared	fading	wireless	hopping	ism	
27	M5L8.txt	bluetooth	piconet	master	rf	hopping	segmentation	ism	ghz	slot	
28	M5L9.txt	cdma	handoff	fdma	cellular	tdma	mobility	mobile	fading	cell	
29	M5L10.txt	vsat	orbit	geo	satellite	aloha	slot	tdma	antenna	conten	
30	M6L1.txt	bridge	spanning	internetworking	repeater	hub	loop	segment	lan	switch	
31	M6L2.txt	segmentation	ism	ism	ism	ism	addressing	is	data	subne	

Figure 3.4: Screen-shot of the TopKWords for each file table

**Algorithm 2** Heuristic 2; Without any threshold, take keyword position;  $i$  is the index of the file for which we have to find requisites

- 1: Take count of each Keyword in each file.
- 2: Store keywords as columns and files as rows.
- 3: For each file get topKkeywords.
- 4: For each keyword get topKfiles.
- 5: Sort the file entries and get unique files.
- 6: For each file take position of the file for each keyword in topKfiles.
- 7: Assign weight as  $w = K - p + 1$ .
- 8: For files whose index = 1 to  $i - 1$ ; get the topK files.
- 9: For files whose index  $> i$ ; get the topK files.

After experiments we observed that it gave better ordering of the results with more relevant files on correct positions.

### 3.4.3 H3: Take the Average count

Another approach we tried for the correct ordering of the pre-requisites and follow-ups is to take the counts of the keywords appearing in that file. We took average counts of each file and then sorted according to the average count.

---

**Algorithm 3** Heuristic 3; With Percentage threshold,take average count;  $i$  is the index of the file for which we have to find requisites

---

- 1: Take count of each Keyword in each file.
  - 2: tore keywords as columns and files as rows.
  - 3: For each file get topKkeywords
  - 4: For each keyword get topKfiles
  - 5: Sort the file entries and get unique files
  - 6: Assign weight to each file based on the average of sum of counts of all keywords appearing in it.
  - 7: Order the files according to their weights.
  - 8: For files whose index = 1 to  $i - 1$ ; get the topK files according to weight.
  - 9: For files whose index  $> i$ ;get the topK files according to weight.
-

Another variation to this approach can be that we take the counts of the keywords appearing in that file and divide by  $K$  ( $K = 5$  in our case).

#### 3.4.4 H4: Find correlation

Another heuristic we applied orders the files based on their correlation with the file for which we are finding pre-requisites and follow-ups. Suppose  $i$  is the index of the file which we are currently processing. we have count of each keyword in this file. For all the other files in the array, we will correlate the corresponding entries with those of the  $i$ th file's entries. We multiply the corresponding entries and take summation of the resulting counts. In this way we get another approach to order the files. After this, we will sort the files according to the latest summation computed and separate out the pre-requisites and follow-ups.

---

**Algorithm 4** Heuristic 4; With Percentage threshold, find correlation;  $i$  is the index of the file for which we have to find requisites

---

- 1: Take count of each Keyword in each file.
  - 2: Store keywords as columns and files as rows.
  - 3: For each file get top  $K$  keywords
  - 4: For each keyword get top  $K$  files
  - 5: Sort the file entries and get unique files
  - 6: Multiply all keyword entries of the  $i$ th file to those of the others.
  - 7: Take sum of the resulting counts.
  - 8: For files whose index = 1 to  $i - 1$ ; get the top  $K$  files according to weight.
  - 9: For files whose index  $> i$ ; get the top  $K$  files according to weight.
- 

By applying this heuristic the counts are increased accordingly due to multiplication and when applied with the percentage threshold gives promising results

## 3.5 Generate DAG

Now, we have found out pre-requisites and follow-up files for all the files of a course. Our next task is to generate dependency graph for the whole course. The purpose of creating dependency graph is to let the user decide what should he know before learning any specific topic. By viewing the dependency graph the user can easily identify which modules need to be learnt before any specific topic and what can be learnt after that [ref. chapter 4 for more detail]. We have captured all the dependencies between the files and later we draw dependency graph with the help of those dependency.

### 3.5.1 Refining the Graph

Now we have set of pre-requisites for all the chapters of the Computer Networks course. We need to show them with the help of dependency graph. But while making the dependency graph

we observed that the graph is kind of messy and has asymmetric relations between the pre-requisites. So, we need to refine the dependencies for every file.

Let us say X and Y are two different files. When we make the dependency graph then we need to check out that we are showing only symmetric relations in our graph to make the graph more clean and readable. For every link between X and Y in the graph we need to ensure that there exists a link between X and Y iff X is a pre-requisite for Y and Y is a follow up of X. This way the graph will clearly depict the dependencies between the files in the course. The graph below in Figure 3.5 shows dependencies in “Computer Networks” course.

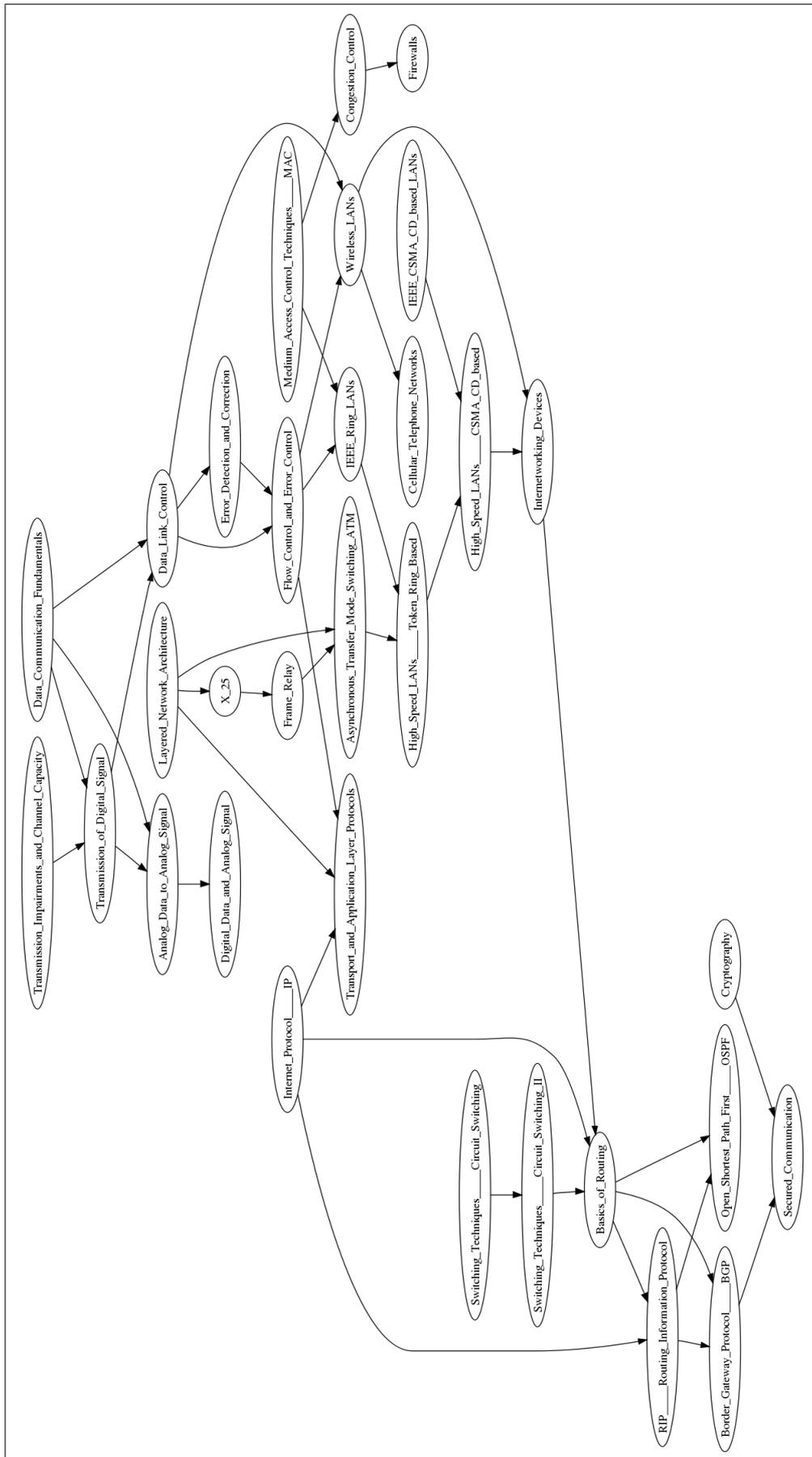


Figure 3.5: DAG for the Computer Networks course

# Chapter 4

## Implementation Details

In this chapter we will see how all the processing is being done and which are the components involved. We will one by one see the processing of each phase.

### 4.1 Parsing with PDFBox

As Lucene indexes only text information, we need to convert pdf files to text files. For that we used PDFBox which is an open source Java PDF library for working with PDF documents. It allows creation of new PDF documents, manipulation of existing documents and to extract content from documents. This functionality is encapsulated in the `org.pdfbox.util.PDFTextStripper`. PDFBox also includes several other command line utilities such as merger PDF documents, create images from pdf pages, print pdf etc. The code snippet is as shown below:

Listing 4.1: simple lucene index

```
File filename1 = new File("/home/nidhi/Desktop/SAD/pdf/M14L1" + ".pdf");
PDDocument doc1 = PDDocument.load(filename1);
PDFTextStripper stripper = new PDFTextStripper();
System.out.println(stripper.getText(doc1));
String str1=stripper.getText(doc1);
File outFile = new File("/home/nidhi/Desktop/SAD/TextFiles/M14L1" + ".txt");
FileWriter out = new FileWriter(outFile);
    out.write(str1);
    out.close();
```

#### 4.1.1 Using Lucene to Index and Search

Lucene is a free open source information retrieval library, originally created in java.[HG04]. It lets the user add indexing and searching capabilities to their applications. Lucene doesn't care about the source of the data, its format, or even its language, as long as we can convert it to text [Lucene] This means we can use Lucene to index and search data stored in files:

web pages on remote web servers, documents stored in local file systems, simple text files, Microsoft Word documents, HTML or PDF files, or any other format from which we can extract textual information. There is no efficient search facility provided in NPTEL. The search option provided just works for the course names and one can't get any information about a particular query if it does not appear in the course name.

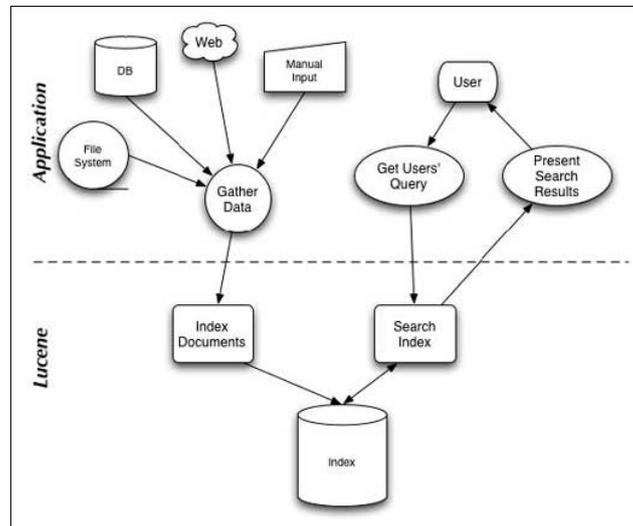


Figure 4.1: Architecture of Lucene [Lucene]

### 4.1.2 Indexing

Suppose we needed to search a large number of files, and we want to be able to find files that contained a certain word or a phrase. A naive approach to do this would be to sequentially scan each file for the given word or phrase. This approach has a number of flaws, the most obvious of which is that it does not scale to larger file sets or cases where files are very large. This is where we need indexing : To search large amounts of text quickly, we must first index that text and convert it into a format that will let us search it rapidly, eliminating the slow sequential scanning process. This conversion process is called indexing, and its output is called an index. Most of the content has been made available in the form of PDF documents. Some of these contain scans of material handwritten or drawn by the instructor and image captures of the blackboard contents.

#### Creating an index

In this section we will see how to use a single class called `Indexer` and its static methods together, they recursively traverse file system directories and index all files with a `.txt` extension. When `Indexer` completes execution it leaves behind a Lucene index for the `Searcher` to search upon. The core indexing classes and how we used them in our program is described as follows:

- **IndexWriter** is the central component of the indexing process. This class creates a new index and adds documents to an existing index. `Index-Writer` gives us write access to the

index but does not let us read or search it. We can also run Indexer from the command-line. It takes two arguments: A path to a directory where we store the Lucene index and path to a directory that contains the files we want to index.

- **Directory** class represents the location of a Lucene index. It is an abstract class that allows its subclasses (two of which are included in Lucene) to store the index. If we have to store our index on the disk then we need to use `FSDirectory`, a `Directory` subclass that maintains a list of real files in the file system.

The other implementation of `Directory` is a class called `RAMDirectory`. Although it exposes an interface identical to that of `FSDirectory`, `RAMDirectory` holds all its data in memory. This implementation is therefore useful for smaller indices that can be fully loaded in memory and can be destroyed upon the termination of an application.

- **Analyzer**

Before text is indexed, it is passed through an `Analyzer`. The `Analyzer`, specified in the `IndexWriter` constructor, is in charge of extracting tokens out of text to be indexed and eliminating the rest. If the content to be indexed is not plain text, it should first be converted to it. `Analyzer` is an abstract class, but Lucene has with several implementations of it. Some of them deal with skipping stop words (frequently used words that do not help distinguish one document from the other, such as a, an, the, in, and on); some deal with conversion of tokens to lowercase letters, so that searches are not case-sensitive; and so on. `Analyzers` are an important part of Lucene and can be used for much more than simple input filtering. For a developer integrating Lucene into an application, the choice of analyzer(s) is a critical element of application design.

- **Document**

A `Document` represents a collection of fields. It can be a web page, an email message, or a text file that we want to retrieve at a later time. Fields of a document represent the document or meta-data associated with that document. For each text file we find, we create a new instance of the `Document` class, populate it with `Fields` and add that `Document` to the index, effectively indexing the file.

- **Field**

Lucene offers four different types of fields from which you can choose. Each field corresponds to a piece of data that is either queried against or retrieved from the index during search.

- **Keyword**

It is not analyzed, but is indexed and stored. This type is suitable for fields whose original value should be preserved in its entirety, such as URLs, file system paths, dates, personal names, Social Security numbers, telephone numbers, and so on. For example, we used the file system path in `Indexer` as a `Keyword` field.

- Unindexed

Is neither analyzed nor indexed, but its value is stored in the index as is. This type is suitable for fields that we need to display with search results (such as a URL or database primary key), but whose values we will never search directly.

- UnStored

The opposite of UnIndexed. This field type is analyzed and indexed but is not stored in the index. It is suitable for indexing a large amount of text that does not need to be retrieved in its original form, such as bodies of web pages, or any other type of text document.

- Text

Is analyzed, and is indexed. This implies that fields of this type can be searched against, but we have to be cautious about the field size.

### 4.1.3 Searching the Index

The Searcher program complements Indexer and it also provides command-line searching capability. It will take two arguments: The path to the index created with Indexer and a query to use to search the index.

The core searching classes are

- IndexSearcher

It is a class that opens an index in a read-only mode. It offers a number of search methods, the simplest taking takes a single Query object as a parameter and returning a hits object.

- Term

A Term is the basic unit for searching. Similar to the Field object, it consists of a pair of string elements: the name of the field and the value of that field. Because the TermQuery object is derived from the abstract parent class Query, it can be used with the Query type on the left side of the statement.

- Query

Lucene comes with a number of concrete Query subclasses. The most basic Lucene Query is TermQuery. Other Query types are BooleanQuery, PhraseQuery, prefixQuery, PhrasePrefixQuery, RangeQuery, FilteredQuery, and SpanQuery.

- TermQuery

TermQuery is the most basic type of query supported by Lucene, and it is one of the primitive query types. It is used for matching documents that contain fields with specific values. A term query accepts a single term such as:

- Hits

This class simply points to the ranked search result documents that match a given query. The hits collection is ordered by score by default.

## 4.2 Using LUKE

The fundamental concepts in Lucene are index, document, field and term. An index contains a sequence of documents.

- A document is a sequence of fields.
- A field is a named sequence of terms.
- A term is a string.

Lucene's index falls into the family of indexes known as an inverted index. This is because it can list, for a term, the documents that contain it. This is the inverse of the natural relationship, in which documents list terms. When Lucene creates index it generates three files which are

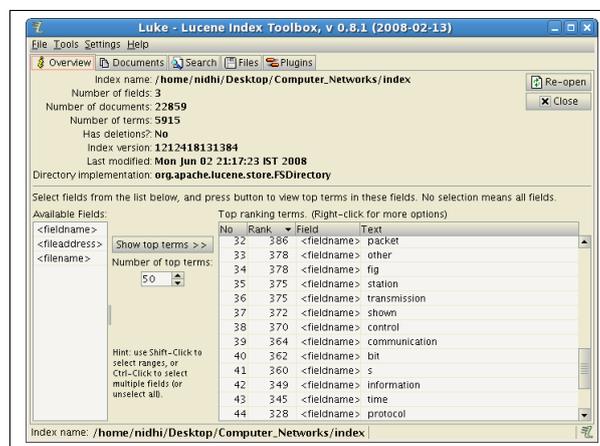


Figure 4.2: Screen-shot of Luke

not readable. For reading and printing the index we need to use LUKE. [Luk] Luke is a handy development and diagnostic tool, which accesses already existing Lucene indexes and allows us to display and modify their contents in several ways such as browse by document number, or by term, view documents and copy them, see the most frequent terms etc.

## 4.3 Generating graph with DOT

After we have identified the pre-requisites and follow ups for every chapter, we plan to prepare a dependency graph for the whole course. Having the dependency graph, the user can clearly make out which module he needs to learn in order to learn about a specific topic.

We make use of the DOT utility of the Graphviz package [DOT]. DOT is a plain-text graph description language. It is a simple way of describing graphs that both humans and computer programs can use. DOT graphs are files that end with the .dot extension.

We captured all the dependencies while finding out the pre-requisites and follow-up files for each chapter. Those dependencies are saved in a text file with the .dot extension. Various attributes can be applied to nodes and edges in DOT files. These attributes can control aspects such as color, shape, and line styles. Sample graph are shown in the next chapters.

Code snippet for generating DAG:

Listing 4.2: Generate DAG

```

public static void generateDAG(String filename)
{
    int requisites[] ;
    int [] topKWords ;
    int [] topKFiles ;
    int [] noOfWordsHavingAFile;

    //String to be written in the file
    String str = "";

    //For each file
    for(int i=0;i<Helper.arrFileNames.length;i++)
    {

        System.out.println("Processing File : " + Helper.extractFileNameFromAbsolutePath(Helper.arrFileNames[i]
            ));
        //System.out.println("Processing File : " + Helper.arrFileNames[i]);
        System.out.println
        System.out.println("Processing File : " + Helper.extractFileNameFromAbsolutePath(Helper.arrFileNames [i] +
            "" + Helper.arrFileTitles[i]);
        //Fetch top K words of the file.
        topKWords = Helper.getTopKWords(Helper.arrFileNames[i]);

        requisites = new int[Helper.arrFileNames.length];
        noOfWordsHavingAFile = new int[requisites.length];
        //*****
        for(int j=0;j<requisites.length;j++)
        {
            requisites[j]=0;
            noOfWordsHavingAFile[j] = 0;
        }
        //Process each word
        for(int j=0;j<topKWords.length;j++)
        {
// System.out.println("\n\nProcessing word no. : " + topKWords[j]);
            int currentWordIndex = topKWords[j];
            if(currentWordIndex== -1)
                break;
// System.out.print(Helper.arrTechnicalWords[currentWordIndex] + " : ");
            //Fetch top K files of the word being processed
            topKFiles = Helper.getTopKFiles(Helper.arrTechnicalWords[currentWordIndex]);
            for(int k=0;k<topKFiles.length;k++)
            {
// System.out.println(topKFiles[k] + " ");
                if(topKFiles[k]== -1)
                    break;
// System.out.print(Helper.extractFileNameFromAbsolutePath(Helper.arrFileNames[topKFiles[k]]) + " ");
                //requisites[topKFiles[k] += 1;

```

```
requisites[topKFiles[k]] += (Helper.K - k);
//requisites[topKFiles[k]] += Helper.result[topKFiles[k]][currentWordIndex];
//noOfWordsHavingAFile[topKFiles[k]]++;
// System.out.println(Helper.result[k][j]);

}}
```

Code snippet for refining DAG:

Listing 4.3: Refine DAG

```
private static void pruneDAG()
{
    int[] tempArray1,tempArray2,tempArray;
    Vector tempVector;
    for(int i=0;i<Helper.arrFileNames.length;i++)
    {
        tempArray1 = Helper.arrFoundPrerequisites[i];
        tempVector=new Vector();
        for(int j=0;j<tempArray1.length;j++)
        {
            if(tempArray1[j]==-1)
                break;
            if(doesArrayContainValue(Helper.arrFoundFollowups[tempArray1[j]], i, 2))
                tempVector.add(new Integer(tempArray1[j]));
        }
        for(int j=0;j<Helper.MaxNoOfFollowups;j++)
            Helper.arrFoundPrerequisites[i][j]=-1;
        for(int j=0;j<tempVector.size();j++)
            Helper.arrFoundPrerequisites[i][j] = ((Integer)tempVector.get(j)).intValue();

        tempArray1 = Helper.arrFoundFollowups[i];
        tempVector=new Vector();
        for(int j=0;j<tempArray1.length;j++)
        {
            if(tempArray1[j]==-1)
                break;
            if(doesArrayContainValue(Helper.arrFoundPrerequisites[tempArray1[j]], i, 2))
                tempVector.add(new Integer(tempArray1[j]));
        }
        for(int j=0;j<Helper.MaxNoOfFollowups;j++)
            Helper.arrFoundFollowups[i][j]=-1;
        for(int j=0;j<tempVector.size();j++)
            Helper.arrFoundFollowups[i][j] = ((Integer)tempVector.get(j)).intValue();
    }
}
```

# Chapter 5

## Experiments

We took NPTEL as the content repository. The examples given in the report are all in reference with the Computer Networks course. Experiments are done on other courses also. For every course, we have maintained specific files such as topkwords for each file, countofKeywords, fileTitles, file containing the pre-requisites and follow-ups for each file of the whole course etc. These files are kept for every course on which we did experiment. Finally a DAG is prepared for every course. The DAG will be presented to the user so that he/she can decide what to learn.

### 5.1 Testing

We have done experiments for 6 courses namely: Computer Networks, Artificial Intelligence, Software Engineering, embedded Systems, Operating System and Software Analysis and Design.

First of all let's take Computer Networks course. It has 40 pdf files. After parsing them with PDFBox, we got 40 text files. Those 40 text files are given to Lucene for which it will create index. At the time of indexing it creates segments file which are not readable by the user. We used Luke to print the index for the course. From that we got our index terms(keywords). For all those keywords, we took their count in each file in an excel file. We read all the counts in a string and through .csv format, directly wrote into an excel file. We maintained a separate file for topKwords of each file. We have maintained a dag.csv file which shows the pre-requisites and follow-ups for each file.

We have tried all the functions described in the third chapter with both the mean and percentage threshHolds. We observed that results after applying percentageThreshHold with the correlation function-Heuristic H4 are more promising.

Table 5.1: Requisites generated by our Program

File Name	Pre-requisite Files	Follow-up Files
Introduction and Course Outline (M1L1.txt)		<ul style="list-style-type: none"> <li>• Wireless LANs (M5L7.txt)</li> <li>• Layered Network Architecture (M1L2.txt)</li> <li>• Flow Control and Error Control (M3L3.txt)</li> </ul>
Layered Network Architecture (M1L2.txt)		<ul style="list-style-type: none"> <li>• X 25 (M4L4.txt)</li> <li>• Wireless LANs (M5L7.txt)</li> <li>• Asynchronous Transfer Mode Switching ATM (M4L6.txt)</li> </ul>
Data Communication Fundamentals (M2L1.txt)	<ul style="list-style-type: none"> <li>• Layered Network Architecture (M1L2.txt)</li> </ul>	<ul style="list-style-type: none"> <li>• Transmission of Digital Signal (M2L4.txt)</li> <li>• Analog Data to Analog Signal (M2L5.txt)</li> <li>• Data Link Control (M3L1.txt)</li> </ul>
Transmission Media (M2L2.txt)	<ul style="list-style-type: none"> <li>• Data Communication Fundamentals (M2L1.txt)</li> <li>• Layered Network Architecture (M1L2.txt)</li> </ul>	<ul style="list-style-type: none"> <li>• Data Link Control (M3L1.txt)</li> <li>• Wireless LANs (M5L7.txt)</li> <li>• Synchronous Optical Network SONET (M4L3.txt)</li> </ul>

Continued on next page...

<p>Transmission Impairments and Channel Capacity (M2L3.txt)</p>	<ul style="list-style-type: none"> <li>• Data Communication Fundamentals (M2L1.txt)</li> <li>• Transmission Media (M2L2.txt)</li> <li>• Layered Network Architecture (M1L2.txt)</li> </ul>	<ul style="list-style-type: none"> <li>• Transmission of Digital Signal (M2L4.txt)</li> <li>• Data Link Control (M3L1.txt)</li> <li>• Digital Data and Analog Signal (M2L6.txt)</li> </ul>
<p>Transmission of Digital Signal (M2L4.txt)</p>	<ul style="list-style-type: none"> <li>• Data Communication Fundamentals (M2L1.txt)</li> <li>• Transmission Impairments and Channel Capacity (M2L3.txt)</li> <li>• Layered Network Architecture (M1L2.txt)</li> </ul>	<ul style="list-style-type: none"> <li>• Data Link Control (M3L1.txt)</li> <li>• Synchronous Optical Network SONET (M4L3.txt)</li> <li>• Analog Data to Analog Signal (M2L5.txt)</li> </ul>
<p>Analog Data to Analog Signal (M2L5.txt)</p>	<ul style="list-style-type: none"> <li>• Data Communication Fundamentals (M2L1.txt)</li> <li>• Transmission of Digital Signal (M2L4.txt)</li> <li>• Transmission Media (M2L2.txt)</li> </ul>	<ul style="list-style-type: none"> <li>• Wireless LANs (M5L7.txt)</li> <li>• Digital Data and Analog Signal (M2L6.txt)</li> <li>• Data Link Control (M3L1.txt)</li> </ul>
<p>Digital Data and Analog Signal (M2L6.txt)</p>	<ul style="list-style-type: none"> <li>• Transmission of Digital Signal (M2L4.txt)</li> <li>• Analog Data to Analog Signal (M2L5.txt)</li> <li>• Data Communication Fundamentals (M2L1.txt)</li> </ul>	<ul style="list-style-type: none"> <li>• Data Link Control (M3L1.txt)</li> <li>• Wireless LANs (M5L7.txt)</li> <li>• Synchronous Optical Network SONET (M4L3.txt)</li> </ul>

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<p>Multiplexing of Digital Signals (M2L7.txt)</p>	<ul style="list-style-type: none"> <li>• Analog Data to Analog Signal (M2L5.txt)</li> <li>• Data Communication Fundamentals (M2L1.txt)</li> <li>• Transmission Impairments and Channel Capacity (M2L3.txt)</li> </ul>	<ul style="list-style-type: none"> <li>• Asynchronous Transfer Mode Switching ATM (M4L6.txt)</li> <li>• Data Link Control (M3L1.txt)</li> <li>• Synchronous Optical Network SONET (M4L3.txt)</li> </ul>
<p>Data Link Control (M3L1.txt)</p>	<ul style="list-style-type: none"> <li>• Transmission of Digital Signal (M2L4.txt)</li> <li>• Data Communication Fundamentals (M2L1.txt)</li> <li>• Layered Network Architecture (M1L2.txt)</li> </ul>	<ul style="list-style-type: none"> <li>• Wireless LANs (M5L7.txt)</li> <li>• Error Detection and Correction (M3L2.txt)</li> <li>• Asynchronous Transfer Mode Switching ATM (M4L6.txt)</li> </ul>
<p>Error Detection and Correction (M3L2.txt)</p>	<ul style="list-style-type: none"> <li>• Data Link Control (M3L1.txt)</li> <li>• Layered Network Architecture (M1L2.txt)</li> <li>• Transmission of Digital Signal (M2L4.txt)</li> </ul>	<ul style="list-style-type: none"> <li>• Wireless LANs (M5L7.txt)</li> <li>• Asynchronous Transfer Mode Switching ATM (M4L6.txt)</li> <li>• Flow Control and Error Control (M3L3.txt)</li> </ul>
<p>Flow Control and Error Control (M3L3.txt)</p>	<ul style="list-style-type: none"> <li>• Data Link Control (M3L1.txt)</li> <li>• Layered Network Architecture (M1L2.txt)</li> <li>• Error Detection and Correction (M3L2.txt)</li> </ul>	<ul style="list-style-type: none"> <li>• Wireless LANs (M5L7.txt)</li> <li>• Transport and Application Layer Protocols (M6L3.txt)</li> <li>• HDLC (M3L4.txt)</li> </ul>

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<p>HDLC (M3L4.txt)</p>	<ul style="list-style-type: none"> <li>• Data Link Control (M3L1.txt)</li> <li>• Flow Control and Error Control (M3L3.txt)</li> <li>• Error Detection and Correction (M3L2.txt)</li> </ul>	<ul style="list-style-type: none"> <li>• Wireless LANs (M5L7.txt)</li> <li>• X 25 (M4L4.txt)</li> <li>• Asynchronous Transfer Mode Switching ATM (M4L6.txt)</li> </ul>
<p>Switching Techniques Circuit Switching (M4L1.txt)</p>	<ul style="list-style-type: none"> <li>• Layered Network Architecture (M1L2.txt)</li> <li>• Data Communication Fundamentals (M2L1.txt)</li> <li>• Data Link Control (M3L1.txt)</li> </ul>	<ul style="list-style-type: none"> <li>• Asynchronous Transfer Mode Switching ATM (M4L6.txt)</li> <li>• Switching Techniques Circuit Switching II (M4L2.txt)</li> <li>• Frame Relay (M4L5.txt)</li> </ul>
<p>Switching Techniques Circuit Switching II (M4L2.txt)</p>	<ul style="list-style-type: none"> <li>• Switching Techniques Circuit Switching (M4L1.txt)</li> <li>• Layered Network Architecture (M1L2.txt)</li> <li>• Transmission Media (M2L2.txt)</li> </ul>	<ul style="list-style-type: none"> <li>• Basics of Routing (M7L1.txt)</li> <li>• RIP Routing Information Protocol (M7L2.txt)</li> <li>• Border Gateway Protocol BGP (M7L4.txt)</li> </ul>
<p>Synchronous Optical Network SONET (M4L3.txt)</p>	<ul style="list-style-type: none"> <li>• Data Communication Fundamentals (M2L1.txt)</li> <li>• Transmission of Digital Signal (M2L4.txt)</li> <li>• Data Link Control (M3L1.txt)</li> </ul>	<ul style="list-style-type: none"> <li>• Asynchronous Transfer Mode Switching ATM (M4L6.txt)</li> <li>• Frame Relay (M4L5.txt)</li> <li>• High Speed LANs Token Ring Based (M5L5.txt)</li> </ul>

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X 25 (M4L4.txt)	<ul style="list-style-type: none"> <li>• Layered Network Architecture (M1L2.txt)</li> <li>• Data Link Control (M3L1.txt)</li> <li>• Flow Control and Error Control (M3L3.txt)</li> </ul>	<ul style="list-style-type: none"> <li>• Frame Relay (M4L5.txt)</li> <li>• Asynchronous Transfer Mode Switching ATM (M4L6.txt)</li> <li>• Congestion Control (M7L5.txt)</li> </ul>
Frame Relay (M4L5.txt)	<ul style="list-style-type: none"> <li>• X 25 (M4L4.txt)</li> <li>• Layered Network Architecture (M1L2.txt)</li> <li>• Data Link Control (M3L1.txt)</li> </ul>	<ul style="list-style-type: none"> <li>• Asynchronous Transfer Mode Switching ATM (M4L6.txt)</li> <li>• IEEE Ring LANs (M5L4.txt)</li> <li>• High Speed LANs Token Ring Based (M5L5.txt)</li> </ul>
Asynchronous Transfer Mode Switching ATM (M4L6.txt)	<ul style="list-style-type: none"> <li>• Layered Network Architecture (M1L2.txt)</li> <li>• Frame Relay (M4L5.txt)</li> <li>• Data Link Control (M3L1.txt)</li> </ul>	<ul style="list-style-type: none"> <li>• High Speed LANs CSMA CD based (M5L6.txt)</li> <li>• Wireless LANs (M5L7.txt)</li> <li>• Internet Protocol IP (M6L2.txt)</li> </ul>
Network Topology (M5L1.txt)	<ul style="list-style-type: none"> <li>• Switching Techniques Circuit Switching (M4L1.txt)</li> <li>• Layered Network Architecture (M1L2.txt)</li> <li>• Asynchronous Transfer Mode Switching ATM (M4L6.txt)</li> </ul>	<ul style="list-style-type: none"> <li>• IEEE Ring LANs (M5L4.txt)</li> <li>• High Speed LANs Token Ring Based (M5L5.txt)</li> <li>• Satellite Networks (M5L10.txt)</li> </ul>

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<p>Medium Access Control Techniques MAC (M5L2.txt)</p>	<ul style="list-style-type: none"> <li>• Switching Techniques Circuit Switching II (M4L2.txt)</li> <li>• X 25 (M4L4.txt)</li> <li>• Asynchronous Transfer Mode Switching ATM (M4L6.txt)</li> </ul>	<ul style="list-style-type: none"> <li>• IEEE Ring LANs (M5L4.txt)</li> <li>• Wireless LANs (M5L7.txt)</li> <li>• Congestion Control (M7L5.txt)</li> </ul>
<p>IEEE CSMA CD based LANs (M5L3.txt)</p>	<ul style="list-style-type: none"> <li>• Data Link Control (M3L1.txt)</li> <li>• Asynchronous Transfer Mode Switching ATM (M4L6.txt)</li> <li>• Transmission Media (M2L2.txt)</li> </ul>	<ul style="list-style-type: none"> <li>• Wireless LANs (M5L7.txt)</li> <li>• High Speed LANs CSMA CD based (M5L6.txt)</li> <li>• High Speed LANs Token Ring Based (M5L5.txt)</li> </ul>
<p>IEEE Ring LANs (M5L4.txt)</p>	<ul style="list-style-type: none"> <li>• Medium Access Control Techniques MAC (M5L2.txt)</li> <li>• Frame Relay (M4L5.txt)</li> <li>• Network Topology (M5L1.txt)</li> </ul>	<ul style="list-style-type: none"> <li>• High Speed LANs Token Ring Based (M5L5.txt)</li> <li>• Congestion Control (M7L5.txt)</li> <li>• Wireless LANs (M5L7.txt)</li> </ul>
<p>High Speed LANs Token Ring Based (M5L5.txt)</p>	<ul style="list-style-type: none"> <li>• IEEE Ring LANs (M5L4.txt)</li> <li>• Asynchronous Transfer Mode Switching ATM (M4L6.txt)</li> <li>• Frame Relay (M4L5.txt)</li> </ul>	<ul style="list-style-type: none"> <li>• High Speed LANs CSMA CD based (M5L6.txt)</li> <li>• Wireless LANs (M5L7.txt)</li> <li>• Internetworking Devices (M6L1.txt)</li> </ul>

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<p>High Speed LANs CSMA CD based (M5L6.txt)</p>	<ul style="list-style-type: none"> <li>• High Speed LANs Token Ring Based (M5L5.txt)</li> <li>• IEEE CSMA CD based LANs (M5L3.txt)</li> <li>• Asynchronous Transfer Mode Switching ATM (M4L6.txt)</li> </ul>	<ul style="list-style-type: none"> <li>• Wireless LANs (M5L7.txt)</li> <li>• Internetworking Devices (M6L1.txt)</li> <li>• Satellite Networks (M5L10.txt)</li> </ul>
<p>Wireless LANs (M5L7.txt)</p>	<ul style="list-style-type: none"> <li>• Data Link Control (M3L1.txt)</li> <li>• Flow Control and Error Control (M3L3.txt)</li> <li>• Transmission Media (M2L2.txt)</li> </ul>	<ul style="list-style-type: none"> <li>• Bluetooth (M5L8.txt)</li> <li>• Cellular Telephone Networks (M5L9.txt)</li> <li>• Internetworking Devices (M6L1.txt)</li> </ul>
<p>Bluetooth (M5L8.txt)</p>	<ul style="list-style-type: none"> <li>• Wireless LANs (M5L7.txt)</li> <li>• Layered Network Architecture (M1L2.txt)</li> <li>• Asynchronous Transfer Mode Switching ATM (M4L6.txt)</li> </ul>	<ul style="list-style-type: none"> <li>• Satellite Networks (M5L10.txt)</li> <li>• Congestion Control (M7L5.txt)</li> <li>• Transport and Application Layer Protocols (M6L3.txt)</li> </ul>
<p>Cellular Telephone Networks (M5L9.txt)</p>	<ul style="list-style-type: none"> <li>• Wireless LANs (M5L7.txt)</li> <li>• Asynchronous Transfer Mode Switching ATM (M4L6.txt)</li> <li>• Bluetooth (M5L8.txt)</li> </ul>	<ul style="list-style-type: none"> <li>• Satellite Networks (M5L10.txt)</li> <li>• Congestion Control (M7L5.txt)</li> <li>• Transport and Application Layer Protocols (M6L3.txt)</li> </ul>

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<p>Satellite Networks (M5L10.txt)</p>	<ul style="list-style-type: none"> <li>• Transmission Media (M2L2.txt)</li> <li>• Asynchronous Transfer Mode Switching ATM (M4L6.txt)</li> <li>• Wireless LANs (M5L7.txt)</li> </ul>	<ul style="list-style-type: none"> <li>• RIP Routing Information Protocol (M7L2.txt)</li> <li>• Internet Protocol IP (M6L2.txt)</li> <li>• Transport and Application Layer Protocols (M6L3.txt)</li> </ul>
<p>Internetworking Devices (M6L1.txt)</p>	<ul style="list-style-type: none"> <li>• High Speed LANs CSMA CD based (M5L6.txt)</li> <li>• Wireless LANs (M5L7.txt)</li> <li>• Layered Network Architecture (M1L2.txt)</li> </ul>	<ul style="list-style-type: none"> <li>• Basics of Routing (M7L1.txt)</li> <li>• RIP Routing Information Protocol (M7L2.txt)</li> <li>• Open Shortest Path First OSPF (M7L3.txt)</li> </ul>
<p>Internet Protocol IP (M6L2.txt)</p>	<ul style="list-style-type: none"> <li>• Layered Network Architecture (M1L2.txt)</li> <li>• Switching Techniques Circuit Switching II (M4L2.txt)</li> <li>• X 25 (M4L4.txt)</li> </ul>	<ul style="list-style-type: none"> <li>• Transport and Application Layer Protocols (M6L3.txt)</li> <li>• Basics of Routing (M7L1.txt)</li> <li>• RIP Routing Information Protocol (M7L2.txt)</li> </ul>
<p>Transport and Application Layer Protocols (M6L3.txt)</p>	<ul style="list-style-type: none"> <li>• Layered Network Architecture (M1L2.txt)</li> <li>• Internet Protocol IP (M6L2.txt)</li> <li>• Flow Control and Error Control (M3L3.txt)</li> </ul>	<ul style="list-style-type: none"> <li>• Congestion Control (M7L5.txt)</li> <li>• Secured Communication (M8L2.txt)</li> <li>• Firewalls (M8L3.txt)</li> </ul>

Continued on next page...

Basics of Routing (M7L1.txt)	<ul style="list-style-type: none"> <li>• Internetworking Devices (M6L1.txt)</li> <li>• Switching Techniques Circuit Switching II (M4L2.txt)</li> <li>• Internet Protocol IP (M6L2.txt)</li> </ul>	<ul style="list-style-type: none"> <li>• RIP Routing Information Protocol (M7L2.txt)</li> <li>• Open Shortest Path First OSPF (M7L3.txt)</li> <li>• Border Gateway Protocol BGP (M7L4.txt)</li> </ul>
RIP Routing Information Protocol (M7L2.txt)	<ul style="list-style-type: none"> <li>• Basics of Routing (M7L1.txt)</li> <li>• Internetworking Devices (M6L1.txt)</li> <li>• Layered Network Architecture (M1L2.txt)</li> </ul>	<ul style="list-style-type: none"> <li>• Border Gateway Protocol BGP (M7L4.txt)</li> <li>• Open Shortest Path First OSPF (M7L3.txt)</li> <li>• Congestion Control (M7L5.txt)</li> </ul>
Open Shortest Path First OSPF (M7L3.txt)	<ul style="list-style-type: none"> <li>• RIP Routing Information Protocol (M7L2.txt)</li> <li>• Basics of Routing (M7L1.txt)</li> <li>• Layered Network Architecture (M1L2.txt)</li> </ul>	<ul style="list-style-type: none"> <li>• Border Gateway Protocol BGP (M7L4.txt)</li> <li>• Congestion Control (M7L5.txt)</li> <li>• Cryptography (M8L1.txt)</li> </ul>
Border Gateway Protocol BGP (M7L4.txt)	<ul style="list-style-type: none"> <li>• RIP Routing Information Protocol (M7L2.txt)</li> <li>• Basics of Routing (M7L1.txt)</li> <li>• Open Shortest Path First OSPF (M7L3.txt)</li> </ul>	<ul style="list-style-type: none"> <li>• Congestion Control (M7L5.txt)</li> <li>• Secured Communication (M8L2.txt)</li> <li>• Cryptography (M8L1.txt)</li> </ul>

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Congestion Control (M7L5.txt)	<ul style="list-style-type: none"> <li>• Basics of Routing (M7L1.txt)</li> <li>• RIP Routing Information Protocol (M7L2.txt)</li> <li>• Open Shortest Path First OSPF (M7L3.txt)</li> </ul>	<ul style="list-style-type: none"> <li>• Cryptography (M8L1.txt)</li> <li>• Firewalls (M8L3.txt)</li> <li>• Secured Communication (M8L2.txt)</li> </ul>
Cryptography (M8L1.txt)	<ul style="list-style-type: none"> <li>• Wireless LANs (M5L7.txt)</li> <li>• Error Detection and Correction (M3L2.txt)</li> <li>• Open Shortest Path First OSPF (M7L3.txt)</li> </ul>	<ul style="list-style-type: none"> <li>• Secured Communication (M8L2.txt)</li> <li>• Firewalls (M8L3.txt)</li> </ul>
Secured Communication (M8L2.txt)	<ul style="list-style-type: none"> <li>• Cryptography (M8L1.txt)</li> <li>• Border Gateway Protocol BGP (M7L4.txt)</li> <li>• RIP Routing Information Protocol (M7L2.txt)</li> </ul>	<ul style="list-style-type: none"> <li>• Firewalls (M8L3.txt)</li> </ul>
Firewalls (M8L3.txt)	<ul style="list-style-type: none"> <li>• Layered Network Architecture (M1L2.txt)</li> <li>• RIP Routing Information Protocol (M7L2.txt)</li> <li>• Congestion Control (M7L5.txt)</li> </ul>	

After generating the requisites for every file, we also draw the DAG as specified in the third chapter. In the DAG, we need to ensure that there exists a link between X and Y iff X is a pre-requisite for Y and Y is a follow up of X. This way the graph will clearly depict the dependencies between the files in the course.

The graph for the same course with percentage Threshold and counts - Heuristic H3 is as shown below in Figure 5.1.

The graph for the same course with percentage Threshold and counts - Heuristic H4 is as shown below in Figure 5.2.



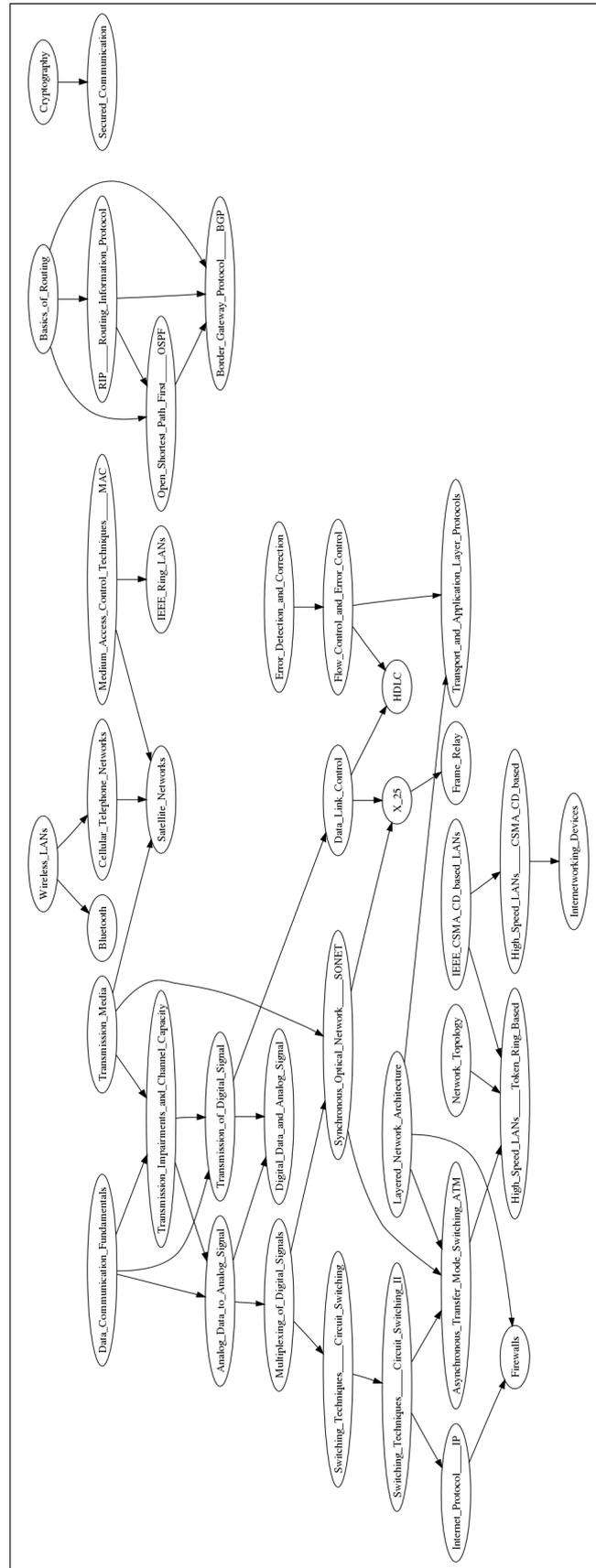


Figure 5.2: DAG for the Computer Networks course - Heuristic H4

## 5.2 Evaluation of the System

To know about the performance of the system, we have compared results generated by our program with those of the expert results. We created goodness metric for all of the 6 courses. We have created goodness metric separately for pre-requisites and follow-ups.

In the following description,  $P_i$  denotes the number of prerequisites suggested by expert for the  $i^{th}$  lecture.  $F_i$  denotes the number of followups suggested by expert for the  $i^{th}$  lecture.  $X_i$  denotes the number of correct prerequisites found by the program.  $Y_i$  denotes the number of correct followups found by the program.  $G_P$  denotes the goodness metric (performance of the program) for prerequisites.  $G_F$  denotes the goodness metric for followups.  $G$  denotes the overall goodness metric.

$$G_P = \sum_{i=1}^N \frac{P_i}{X_i} \quad (5.1)$$

$$G_F = \sum_{i=1}^N \frac{F_i}{Y_i} \quad (5.2)$$

$$G = \frac{G_P + G_F}{2} \quad (5.3)$$

Table 5.2: Evaluation results

Course	T0 - F0	T0 - F1	H1	H2	H3	H4
Networks	76.87	77.49	78.95	78.54	79.16	81.25
AI	60.56	69.91	73.57	72.76	73.17	80.89
SE	87.1	90.67	88.69	83.92	85.11	86.5
Embedded	81.15	77.97	85.11	76.38	78.96	81.74
OS	80.15	81.74	86	77.77	78.57	82.19
SAD	92.85	92.85	90.85	91.85	92	92.85

where T0 - F0 = Without any threshold

T0 - F1 = Without any threshold just take position

# Chapter 6

## Self-Assessment for the User

After we have found pre-requisites and follow-ups for each lecture of our course, we can concentrate on how to help the user to let him know how much he has learnt. In this section, we explain how the “Quiz” module proposed by the system can be used by the learner for self-evaluation. In addition, this module can also be used by the subject matter expert for getting feedback about the learning material.

### 6.1 Evaluation for the learner

After the learner finishes reading the recommended material for the topic, a quiz is offered related to that topic with the intention that the learner can judge whether the material was learnt well enough. When the learner takes the quiz, the scores of the learner are shown immediately along with the answers for those questions that the learner had got wrong. The learner is shown the scores of the quiz along with the average scores for that topic. The intention of this is that by looking at the absolute or average scores, the learner can get an idea of how much material was grasped. If the learner feels that the absolute score is not good enough, the learner can go through the pre-requisite material for the topic.

The quiz is objective in nature with the learner having to choose from multiple alternatives. The quiz questions, alternative answers, and the right choice are prepared by the subject matter expert per topic. These are stored in the “Quiz Question Bank” in a database.

### 6.2 Functioning of the System

The functioning of the system from the user perspective will be as follows:

1. User will enter the query or broadly speaking the area which he wants to learn.
2. User will get the most relevant link corresponding to the entered query.
3. User will be given the dependency graph for the course.
4. Quiz will be given to the student for self-evaluation.

5. Based on the outcomes of the results of quiz feedback will be provided to the user.

### **6.3 Feedback for the subject matter expert**

The quiz results can also serve as a feedback for the subject matter expert as explained in this section.

Periodically, the subject matter expert can view various statistics about the topics handled by him / her. These include the following:

- Number of learners that used the search along with the keyword
- Number of learners that used the pre-requisite material, the actual topic material, and those that viewed the follow-up material
- The quiz scores per topic per learner, and the average score per topic

Based on this information, the subject matter expert can decide if the material being offered by the search engine is adequate or not. The subject matter expert can then decide to use some other material for the topic that may be better than the one currently narrowed down by the search engine. If the subject matter expert finds some problem related to the relevance of the topics such that he feels some topic is not given much weightage and the other is not that important, then he can think about the default scoring mechanism of Lucene and decide the topics which are more relevant should be given more boost value.

# Chapter 7

## Conclusion and Future Work

We have tried out all the heuristics for 6 different courses. We can say that the heuristic with percentage Threshold and correlation function gives promising results as compared to the other heuristics. We have observed some conflicts in the system and the expert answers in the sense that for some files our program is generating requisites and in the expert views there are no requisites in that domain for the particular file. The things which need to be focused on for future work are: At this stage we had assumed the simple evaluation of the quiz given to the student. We can simply set some manual range to evaluate the quiz and inform the student about his/her progress. We have not taken into account the design and level of questions for the quiz. There can be many variations for the structure of the quiz such as fixing the number of questions of different difficulty levels and how to incorporate the level of difficulty with each question. We can observe the previous quizzes of the students. If a particular question is answered correctly by most of the students then that can be considered as easy and so on.



# Appendix A

## Lucene Scoring Formula

$$score(q, d) = coord(q, d) \cdot queryNorm(q) \cdot \sum_{t \text{ in } q} (tf(t \text{ in } d) \cdot idf(t)^2 \cdot t.getBoost() \cdot norm(t, d))$$

$tf(t \text{ in } d)$  defines the number of times the term  $t$  appears in the sorted document  $d$ . Documents with more occurrences of a given term have high score. The default value in Similarity class is:

$$tf(t \text{ in } d) = \text{frequency}^{\frac{1}{2}}$$

$idf(t)$  denotes the Inverse Document Frequency and correlates to the inverse of  $docFreq$ . The default computation is:

$$idf(t) = 1 + \log\left(\frac{numDocs}{docFreq + 1}\right)$$

$coord(q,d)$  calculates how many of the query terms are found in the specified document. A document having more query terms will receive a higher score other than the document with fewer query terms.

$queryNorm(q)$  is a normalizing factor which is used to make scores between queries comparable. This factor does not affect document ranking (since all ranked documents are multiplied by the same factor), but rather just attempts to make scores from different queries (or even different indexes) comparable. This is a search time factor computed by the Similarity in effect at search time. The default computation in DefaultSimilarity is:

$$queryNorm(q) = queryNorm(sumOfSquaredWeights) = \frac{1}{sumOfSquaredWeights^{\frac{1}{2}}}$$

The sum of squared weights (of the query terms) is computed by the query Weight object. For example, a boolean query computes this value as:

$t.getBoost()$  is a search time boost of term  $t$  in the query  $q$ .

$$sumOfSquaredWeights = q.getBoost()^2 \cdot \sum_{t \text{ in } q} (idf(t) \cdot t.getBoost())^2$$

$norm(t,d)$  takes a few (indexing time) boost and length factors:

- Document boost - set by calling `doc.setBoost()` before adding the document to the index.

- Field boost - set by calling `field.setBoost()` before adding the field to a document.
- `lengthNorm(field)` - computed when the document is added to the index in accordance with the number of tokens of this field in the document, so that shorter fields contribute more to the score. `LengthNorm` is computed by the `Similarity` class in effect at indexing.

When a document is added to the index, all the above factors are multiplied. If the document has multiple fields with the same name, all their boosts are multiplied together:

$$\text{norm}(t, d) = \text{doc.getBoost()} \cdot \text{lengthNorm}(\text{field}) \cdot \prod_{\text{field } f \text{ in } d \text{ named as } t} f.\text{getBoost}()$$

Sample code for LUCene Indexing is shown below:

---

## Listing A.1: Sample code Lucene

```
* This code was originally written for
* Erik's Lucene intro java.net article

public class Indexer {
    public static void main(String[] args) throws Exception {
        if (args.length != 2) {
            throw new Exception("Usage: java " + Indexer.class.getName()
                + " <index dir> <data dir>");
        }

        File indexDir = new File(args[0]);
        File dataDir = new File(args[1]);

        long start = new Date().getTime();
        int numIndexed = index(indexDir, dataDir);
        long end = new Date().getTime();
        System.out.println("Indexing " + numIndexed + " files took "
            + (end - start) + " milliseconds");
    }
    // open an index and start file directory traversal
    public static int index(File indexDir, File dataDir)
        throws IOException {

        if (!dataDir.exists() || !dataDir.isDirectory()) {
            throw new IOException(dataDir
                + " does not exist or is not a directory");
        }

        IndexWriter writer = new IndexWriter(indexDir,

            new StandardAnalyzer(), true);
        writer.setUseCompoundFile(false);
        indexDirectory(writer, dataDir);
        int numIndexed = writer.docCount();
        writer.optimize();
        writer.close();

        return numIndexed;
    }
    // recursive method that calls itself when it finds a directory
    private static void indexDirectory(IndexWriter writer, File dir)
        throws IOException {
        File[] files = dir.listFiles();
        for (int i = 0; i < files.length; i++) {
            File f = files[i];
            if (f.isDirectory()) {

                indexDirectory(writer, f);
            } else if (f.getName().endsWith(".txt")) {
```

```
        indexFile(writer, f);
    }
}
}
// method to actually index a file using Lucene
private static void indexFile(IndexWriter writer, File f)
    throws IOException {
    if (f.isHidden() || !f.exists() || !f.canRead()) {
        return;
    }
    System.out.println("Indexing " + f.getCanonicalPath());
    Document doc = new Document();

    doc.add(Field.Text("contents", new FileReader(f)));

    doc.add(Field.Keyword("filename", f.getCanonicalPath()));

    writer.addDocument(doc);
}
}
```

---

## Listing A.2: Sample code Lucene

```
* This code was originally written for
* Erik's Lucene intro java.net article
*/
public class Searcher {
    public static void main(String[] args) throws Exception {
        if (args.length != 2) {
            throw new Exception("Usage: java " + Searcher.class.getName()
                + " <index dir> <query>");
        }
        File indexDir = new File(args[0]);
        String q = args[1]; Query string
        if (!indexDir.exists() || !indexDir.isDirectory()) {
            throw new Exception(indexDir +
                " does not exist or is not a directory.");
        }
        search(indexDir, q);
    }
    public static void search(File indexDir, String q)
        throws Exception {
        Directory fsDir = FSDirectory.getDirectory(indexDir, false);

        IndexSearcher is = new IndexSearcher(fsDir);

        Query query = QueryParser.parse(q, "contents",
            new StandardAnalyzer());
        long start = new Date().getTime();

        Hits hits = is.search(query);
        long end = new Date().getTime();
        System.err.println("Found " + hits.length() +

            " document(s) (in " + (end - start) +
            " milliseconds) that matched query '" +
            q + "'");
        for (int i = 0; i < hits.length(); i++) {

            Document doc = hits.doc(i);
            System.out.println(doc.get("filename"));

        }
    }
}
```



# Appendix B

## More Experimental Results

In the Experiments chapter, we have shown the experiments for “Computer Networks” course only. The other courses for which we have also done experiments for are “Artificial Intelligence”, “Embedded Systems”, “Software Engineering”, “Operating System”, “System Analysis and Design”. The table for Embedded Systems is shown here:

Table B.1: Requisites generated by our Program

File Name	Pre-requisite Files	Follow-up Files
Introduction to Real Time Embedded Systems Part I (M1L1.txt)		<ul style="list-style-type: none"><li>• Wireless Communication (M5L27.txt)</li><li>• Testing Embedded Systems (M8L38.txt)</li><li>• Introduction to Real Time Systems (M6L28.txt)</li></ul>
Introduction to Real Time Embedded Systems Part II (M1L2.txt)		<ul style="list-style-type: none"><li>• Testing Embedded Systems (M8L38.txt)</li><li>• AD and DA Converters (M3L18.txt)</li><li>• Embedded Systems Components Part II (M1L4.txt)</li></ul>

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<p>Embedded Systems Components Part I (M1L3.txt)</p>	<ul style="list-style-type: none"> <li>• Introduction to Real Time Embedded Systems Part II (M1L2.txt)</li> </ul>	<ul style="list-style-type: none"> <li>• AD and DA Converters (M3L18.txt)</li> <li>• Embedded Systems Components Part II (M1L4.txt)</li> <li>• Embedded Processors and Memory I (M2L5.txt)</li> </ul>
<p>Embedded Systems Components Part II (M1L4.txt)</p>	<ul style="list-style-type: none"> <li>• Embedded Systems Components Part I (M1L3.txt)</li> <li>• Introduction to Real Time Embedded Systems Part II (M1L2.txt)</li> </ul>	<ul style="list-style-type: none"> <li>• AD and DA Converters (M3L18.txt)</li> <li>• Embedded Processors and Memory I (M2L5.txt)</li> <li>• Embedded Processors I (M2L10.txt)</li> </ul>
<p>Embedded Processors and Memory I (M2L5.txt)</p>	<ul style="list-style-type: none"> <li>• Embedded Systems Components Part II (M1L4.txt)</li> <li>• Embedded Systems Components Part I (M1L3.txt)</li> <li>• Introduction to Real Time Embedded Systems Part II (M1L2.txt)</li> </ul>	<ul style="list-style-type: none"> <li>• Embedded Processors and Memory II (M2L6.txt)</li> <li>• Field Programmable Gate Arrays and Applications (M4L20.txt)</li> <li>• General Purpose Processors I (M2L8.txt)</li> </ul>

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<p>Embedded Processors and Memory II (M2L6.txt)</p>	<ul style="list-style-type: none"> <li>• Embedded Processors and Memory I (M2L5.txt)</li> <li>• Embedded Systems Components Part II (M1L4.txt)</li> <li>• Embedded Systems Components Part I (M1L3.txt)</li> </ul>	<ul style="list-style-type: none"> <li>• General Purpose Processors I (M2L8.txt)</li> <li>• Interfacing bus Protocols ISA bus etc (M3L13.txt)</li> <li>• Testing Embedded Systems (M8L38.txt)</li> </ul>
<p>Digital Signal Processors (M2L7.txt)</p>	<ul style="list-style-type: none"> <li>• Embedded Systems Components Part II (M1L4.txt)</li> <li>• Introduction to Real Time Embedded Systems Part II (M1L2.txt)</li> <li>• Embedded Processors and Memory I (M2L5.txt)</li> </ul>	<ul style="list-style-type: none"> <li>• AD and DA Converters (M3L18.txt)</li> <li>• Introduction to Real Time Systems (M6L28.txt)</li> <li>• Analog Interfacing (M3L19.txt)</li> </ul>
<p>General Purpose Processors I (M2L8.txt)</p>	<ul style="list-style-type: none"> <li>• Embedded Processors and Memory II (M2L6.txt)</li> <li>• Embedded Processors and Memory I (M2L5.txt)</li> <li>• Embedded Systems Components Part II (M1L4.txt)</li> </ul>	<ul style="list-style-type: none"> <li>• Embedded Processors I (M2L10.txt)</li> <li>• Concepts in Real Time Operating Systems (M6L31.txt)</li> <li>• Introduction to Hardware Description Languages I (M4L21.txt)</li> </ul>

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<p>General Purpose Processors II (M2L9.txt)</p>	<ul style="list-style-type: none"> <li>• General Purpose Processors I (M2L8.txt)</li> <li>• Embedded Processors and Memory II (M2L6.txt)</li> <li>• Embedded Systems Components Part I (M1L3.txt)</li> </ul>	<ul style="list-style-type: none"> <li>• Interfacing bus Protocols ISA bus etc (M3L13.txt)</li> <li>• Concepts in Real Time Operating Systems (M6L31.txt)</li> <li>• Serial Data Communication (M5L25.txt)</li> </ul>
<p>Embedded Processors I (M2L10.txt)</p>	<ul style="list-style-type: none"> <li>• Embedded Systems Components Part II (M1L4.txt)</li> <li>• General Purpose Processors I (M2L8.txt)</li> <li>• Embedded Processors and Memory I (M2L5.txt)</li> </ul>	<ul style="list-style-type: none"> <li>• Embedded Processors II (M2L11.txt)</li> <li>• Serial Data Communication (M5L25.txt)</li> <li>• Concepts in Real Time Operating Systems (M6L31.txt)</li> </ul>
<p>Embedded Processors II (M2L11.txt)</p>	<ul style="list-style-type: none"> <li>• Embedded Processors I (M2L10.txt)</li> <li>• Embedded Systems Components Part II (M1L4.txt)</li> <li>• Embedded Processors and Memory I (M2L5.txt)</li> </ul>	<ul style="list-style-type: none"> <li>• Boundary Scan Methods and Standards (M8L41.txt)</li> <li>• Interfacing bus Protocols ISA bus etc (M3L13.txt)</li> <li>• Serial Data Communication (M5L25.txt)</li> </ul>

Continued on next page...

<p>Memory Interfacing (M2L12.txt)</p>	<ul style="list-style-type: none"> <li>• Embedded Systems Components Part II (M1L4.txt)</li> <li>• Embedded Processors I (M2L10.txt)</li> <li>• Embedded Processors and Memory I (M2L5.txt)</li> </ul>	<ul style="list-style-type: none"> <li>• Interfacing bus Protocols ISA bus etc (M3L13.txt)</li> <li>• Boundary Scan Methods and Standards (M8L41.txt)</li> <li>• AD and DA Converters (M3L18.txt)</li> </ul>
<p>Interfacing bus Protocols ISA bus etc (M3L13.txt)</p>	<ul style="list-style-type: none"> <li>• General Purpose Processors II (M2L9.txt)</li> <li>• Embedded Systems Components Part II (M1L4.txt)</li> <li>• Introduction to Real Time Embedded Systems Part II (M1L2.txt)</li> </ul>	<ul style="list-style-type: none"> <li>• Serial Data Communication (M5L25.txt)</li> <li>• DMA (M3L16.txt)</li> <li>• Interrupts (M3L15.txt)</li> </ul>
<p>Timers (M3L14.txt)</p>	<ul style="list-style-type: none"> <li>• Interfacing bus Protocols ISA bus etc (M3L13.txt)</li> <li>• Embedded Processors I (M2L10.txt)</li> <li>• Embedded Systems Components Part II (M1L4.txt)</li> </ul>	<ul style="list-style-type: none"> <li>• Interrupts (M3L15.txt)</li> <li>• Introduction to Hardware Description Languages II (M4L22.txt)</li> <li>• Introduction to Hardware Description Languages I (M4L21.txt)</li> </ul>
<p>Interrupts (M3L15.txt)</p>	<ul style="list-style-type: none"> <li>• Interfacing bus Protocols ISA bus etc (M3L13.txt)</li> <li>• Timers (M3L14.txt)</li> <li>• Embedded Processors I (M2L10.txt)</li> </ul>	<ul style="list-style-type: none"> <li>• DMA (M3L16.txt)</li> <li>• Concepts in Real Time Operating Systems (M6L31.txt)</li> <li>• Commercial Real Time Operating Systems (M6L32.txt)</li> </ul>

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<p>DMA (M3L16.txt)</p>	<ul style="list-style-type: none"> <li>• Interfacing bus Protocols ISA bus etc (M3L13.txt)</li> <li>• Interrupts (M3L15.txt)</li> <li>• General Purpose Processors I (M2L8.txt)</li> </ul>	<ul style="list-style-type: none"> <li>• Analog Interfacing (M3L19.txt)</li> <li>• Software Design Part 2 (M7L37.txt)</li> <li>• Serial Data Communication (M5L25.txt)</li> </ul>
<p>USB and IrDA (M3L17.txt)</p>	<ul style="list-style-type: none"> <li>• Interfacing bus Protocols ISA bus etc (M3L13.txt)</li> <li>• Interrupts (M3L15.txt)</li> <li>• Embedded Systems Components Part I (M1L3.txt)</li> </ul>	<ul style="list-style-type: none"> <li>• Wireless Communication (M5L27.txt)</li> <li>• Serial Data Communication (M5L25.txt)</li> <li>• Introduction to Real Time Systems (M6L28.txt)</li> </ul>
<p>AD and DA Converters (M3L18.txt)</p>	<ul style="list-style-type: none"> <li>• Digital Signal Processors (M2L7.txt)</li> <li>• Embedded Systems Components Part II (M1L4.txt)</li> <li>• Introduction to Real Time Embedded Systems Part II (M1L2.txt)</li> </ul>	<ul style="list-style-type: none"> <li>• Analog Interfacing (M3L19.txt)</li> <li>• Introduction to Real Time Systems (M6L28.txt)</li> <li>• Introduction to Hardware Description Languages I (M4L21.txt)</li> </ul>
<p>Analog Interfacing (M3L19.txt)</p>	<ul style="list-style-type: none"> <li>• AD and DA Converters (M3L18.txt)</li> <li>• Interfacing bus Protocols ISA bus etc (M3L13.txt)</li> <li>• DMA (M3L16.txt)</li> </ul>	<ul style="list-style-type: none"> <li>• Introduction to Real Time Systems (M6L28.txt)</li> <li>• Introduction to Hardware Description Languages I (M4L21.txt)</li> <li>• Design for Testability (M8L39.txt)</li> </ul>

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<p>Field Programmable Gate Arrays and Applications (M4L20.txt)</p>	<ul style="list-style-type: none"> <li>• Embedded Processors and Memory I (M2L5.txt)</li> <li>• Embedded Systems Components Part II (M1L4.txt)</li> <li>• General Purpose Processors I (M2L8.txt)</li> </ul>	<ul style="list-style-type: none"> <li>• Introduction to Hardware Description Languages I (M4L21.txt)</li> <li>• Design for Testability (M8L39.txt)</li> <li>• Introduction to Hardware Description Languages III (M4L23.txt)</li> </ul>
<p>Introduction to Hardware Description Languages I (M4L21.txt)</p>	<ul style="list-style-type: none"> <li>• Field Programmable Gate Arrays and Applications (M4L20.txt)</li> <li>• AD and DA Converters (M3L18.txt)</li> <li>• Timers (M3L14.txt)</li> </ul>	<ul style="list-style-type: none"> <li>• Introduction to Hardware Description Languages III (M4L23.txt)</li> <li>• Introduction to Hardware Description Languages II (M4L22.txt)</li> <li>• Modelling Timing Constraints (M7L35.txt)</li> </ul>
<p>Introduction to Hardware Description Languages II (M4L22.txt)</p>	<ul style="list-style-type: none"> <li>• Timers (M3L14.txt)</li> <li>• Introduction to Hardware Description Languages I (M4L21.txt)</li> <li>• Interfacing bus Protocols ISA bus etc (M3L13.txt)</li> </ul>	<ul style="list-style-type: none"> <li>• Introduction to Hardware Description Languages III (M4L23.txt)</li> <li>• Software Design Part 2 (M7L37.txt)</li> <li>• Design for Testability (M8L39.txt)</li> </ul>

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<p>Introduction to Hardware Description Languages III (M4L23.txt)</p>	<ul style="list-style-type: none"> <li>• Introduction to Hardware Description Languages I (M4L21.txt)</li> <li>• Field Programmable Gate Arrays and Applications (M4L20.txt)</li> <li>• Introduction to Hardware Description Languages II (M4L22.txt)</li> </ul>	<ul style="list-style-type: none"> <li>• Modelling Timing Constraints (M7L35.txt)</li> <li>• Built In Self Test BIST for Embedded Systems (M8L40.txt)</li> <li>• Introduction to Software Engineering (M7L33.txt)</li> </ul>
<p>Parallel Data Communication (M5L24.txt)</p>	<ul style="list-style-type: none"> <li>• Interfacing bus Protocols ISA bus etc (M3L13.txt)</li> <li>• DMA (M3L16.txt)</li> <li>• Interrupts (M3L15.txt)</li> </ul>	<ul style="list-style-type: none"> <li>• Introduction to Real Time Systems (M6L28.txt)</li> <li>• Serial Data Communication (M5L25.txt)</li> <li>• Commercial Real Time Operating Systems (M6L32.txt)</li> </ul>
<p>Serial Data Communication (M5L25.txt)</p>	<ul style="list-style-type: none"> <li>• Interfacing bus Protocols ISA bus etc (M3L13.txt)</li> <li>• USB and IrDA (M3L17.txt)</li> <li>• Embedded Processors I (M2L10.txt)</li> </ul>	<ul style="list-style-type: none"> <li>• Wireless Communication (M5L27.txt)</li> <li>• Introduction to Real Time Systems (M6L28.txt)</li> <li>• Network Communication (M5L26.txt)</li> </ul>

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<p>Network Communication (M5L26.txt)</p>	<ul style="list-style-type: none"> <li>• Serial Data Communication (M5L25.txt)</li> <li>• Interfacing bus Protocols ISA bus etc (M3L13.txt)</li> <li>• DMA (M3L16.txt)</li> </ul>	<ul style="list-style-type: none"> <li>• Requirements Analysis and Specification (M7L34.txt)</li> <li>• Introduction to Real Time Systems (M6L28.txt)</li> <li>• Modelling Timing Constraints (M7L35.txt)</li> </ul>
<p>Wireless Communication (M5L27.txt)</p>	<ul style="list-style-type: none"> <li>• Serial Data Communication (M5L25.txt)</li> <li>• USB and IrDA (M3L17.txt)</li> <li>• Interfacing bus Protocols ISA bus etc (M3L13.txt)</li> </ul>	<ul style="list-style-type: none"> <li>• Introduction to Real Time Systems (M6L28.txt)</li> <li>• Real Time Task Scheduling Part 2 (M6L30.txt)</li> <li>• Software Design Part 2 (M7L37.txt)</li> </ul>
<p>Introduction to Real Time Systems (M6L28.txt)</p>	<ul style="list-style-type: none"> <li>• AD and DA Converters (M3L18.txt)</li> <li>• Serial Data Communication (M5L25.txt)</li> <li>• Field Programmable Gate Arrays and Applications (M4L20.txt)</li> </ul>	<ul style="list-style-type: none"> <li>• Modelling Timing Constraints (M7L35.txt)</li> <li>• Testing Embedded Systems (M8L38.txt)</li> <li>• On line Testing of Embedded Systems (M8L42.txt)</li> </ul>

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<p>Real Time Task Scheduling Part 1 (M6L29.txt)</p>	<ul style="list-style-type: none"> <li>• Introduction to Real Time Systems (M6L28.txt)</li> <li>• Embedded Systems Components Part II (M1L4.txt)</li> <li>• Introduction to Real Time Embedded Systems Part II (M1L2.txt)</li> </ul>	<ul style="list-style-type: none"> <li>• Real Time Task Scheduling Part 2 (M6L30.txt)</li> <li>• Concepts in Real Time Operating Systems (M6L31.txt)</li> <li>• Testing Embedded Systems (M8L38.txt)</li> </ul>
<p>Real Time Task Scheduling Part 2 (M6L30.txt)</p>	<ul style="list-style-type: none"> <li>• Real Time Task Scheduling Part 1 (M6L29.txt)</li> <li>• Wireless Communication (M5L27.txt)</li> <li>• Embedded Systems Components Part II (M1L4.txt)</li> </ul>	<ul style="list-style-type: none"> <li>• Concepts in Real Time Operating Systems (M6L31.txt)</li> <li>• Commercial Real Time Operating Systems (M6L32.txt)</li> <li>• Built In Self Test BIST for Embedded Systems (M8L40.txt)</li> </ul>
<p>Concepts in Real Time Operating Systems (M6L31.txt)</p>	<ul style="list-style-type: none"> <li>• Real Time Task Scheduling Part 2 (M6L30.txt)</li> <li>• Real Time Task Scheduling Part 1 (M6L29.txt)</li> <li>• Introduction to Real Time Systems (M6L28.txt)</li> </ul>	<ul style="list-style-type: none"> <li>• Commercial Real Time Operating Systems (M6L32.txt)</li> <li>• Software Design Part 2 (M7L37.txt)</li> <li>• Built In Self Test BIST for Embedded Systems (M8L40.txt)</li> </ul>

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<p>Commercial Real Time Operating Systems (M6L32.txt)</p>	<ul style="list-style-type: none"> <li>• Concepts in Real Time Operating Systems (M6L31.txt)</li> <li>• Introduction to Real Time Systems (M6L28.txt)</li> <li>• Real Time Task Scheduling Part 2 (M6L30.txt)</li> </ul>	<ul style="list-style-type: none"> <li>• Software Design Part 2 (M7L37.txt)</li> <li>• Testing Embedded Systems (M8L38.txt)</li> <li>• Design for Testability (M8L39.txt)</li> </ul>
<p>Introduction to Software Engineering (M7L33.txt)</p>	<ul style="list-style-type: none"> <li>• Introduction to Real Time Systems (M6L28.txt)</li> <li>• Introduction to Hardware Description Languages I (M4L21.txt)</li> <li>• Introduction to Hardware Description Languages III (M4L23.txt)</li> </ul>	<ul style="list-style-type: none"> <li>• Software Design Part 1 (M7L36.txt)</li> <li>• Requirements Analysis and Specification (M7L34.txt)</li> <li>• Software Design Part 2 (M7L37.txt)</li> </ul>
<p>Requirements Analysis and Specification (M7L34.txt)</p>	<ul style="list-style-type: none"> <li>• Introduction to Software Engineering (M7L33.txt)</li> <li>• Introduction to Real Time Systems (M6L28.txt)</li> <li>• Introduction to Hardware Description Languages I (M4L21.txt)</li> </ul>	<ul style="list-style-type: none"> <li>• Software Design Part 1 (M7L36.txt)</li> <li>• Software Design Part 2 (M7L37.txt)</li> <li>• Testing Embedded Systems (M8L38.txt)</li> </ul>

Continued on next page...

<p>Modelling Timing Constraints (M7L35.txt)</p>	<ul style="list-style-type: none"> <li>• Introduction to Real Time Systems (M6L28.txt)</li> <li>• Introduction to Hardware Description Languages I (M4L21.txt)</li> <li>• Introduction to Hardware Description Languages III (M4L23.txt)</li> </ul>	<ul style="list-style-type: none"> <li>• Testing Embedded Systems (M8L38.txt)</li> <li>• Software Design Part 2 (M7L37.txt)</li> <li>• Design for Testability (M8L39.txt)</li> </ul>
<p>Software Design Part 1 (M7L36.txt)</p>	<ul style="list-style-type: none"> <li>• Introduction to Software Engineering (M7L33.txt)</li> <li>• Requirements Analysis and Specification (M7L34.txt)</li> <li>• Introduction to Real Time Systems (M6L28.txt)</li> </ul>	<ul style="list-style-type: none"> <li>• Software Design Part 2 (M7L37.txt)</li> <li>• Testing Embedded Systems (M8L38.txt)</li> <li>• Design for Testability (M8L39.txt)</li> </ul>
<p>Software Design Part 2 (M7L37.txt)</p>	<ul style="list-style-type: none"> <li>• Software Design Part 1 (M7L36.txt)</li> <li>• Introduction to Software Engineering (M7L33.txt)</li> <li>• Concepts in Real Time Operating Systems (M6L31.txt)</li> </ul>	<ul style="list-style-type: none"> <li>• Boundary Scan Methods and Standards (M8L41.txt)</li> <li>• Testing Embedded Systems (M8L38.txt)</li> <li>• Design for Testability (M8L39.txt)</li> </ul>

Continued on next page...

<p>Testing Embedded Systems (M8L38.txt)</p>	<ul style="list-style-type: none"> <li>• Introduction to Real Time Systems (M6L28.txt)</li> <li>• Introduction to Software Engineering (M7L33.txt)</li> <li>• Introduction to Real Time Embedded Systems Part II (M1L2.txt)</li> </ul>	<ul style="list-style-type: none"> <li>• Built In Self Test BIST for Embedded Systems (M8L40.txt)</li> <li>• On line Testing of Embedded Systems (M8L42.txt)</li> <li>• Design for Testability (M8L39.txt)</li> </ul>
<p>Design for Testability (M8L39.txt)</p>	<ul style="list-style-type: none"> <li>• Field Programmable Gate Arrays and Applications (M4L20.txt)</li> <li>• Introduction to Hardware Description Languages I (M4L21.txt)</li> <li>• Testing Embedded Systems (M8L38.txt)</li> </ul>	<ul style="list-style-type: none"> <li>• Boundary Scan Methods and Standards (M8L41.txt)</li> <li>• Built In Self Test BIST for Embedded Systems (M8L40.txt)</li> <li>• On line Testing of Embedded Systems (M8L42.txt)</li> </ul>
<p>Built In Self Test BIST for Embedded Systems (M8L40.txt)</p>	<ul style="list-style-type: none"> <li>• Testing Embedded Systems (M8L38.txt)</li> <li>• Design for Testability (M8L39.txt)</li> <li>• Embedded Systems Components Part II (M1L4.txt)</li> </ul>	<ul style="list-style-type: none"> <li>• On line Testing of Embedded Systems (M8L42.txt)</li> <li>• Boundary Scan Methods and Standards (M8L41.txt)</li> </ul>

Continued on next page...

<p>Boundary Scan Methods and Standards (M8L41.txt)</p>	<ul style="list-style-type: none"> <li>• Software Design Part 2 (M7L37.txt)</li> <li>• Design for Testability (M8L39.txt)</li> <li>• Built In Self Test BIST for Embedded Systems (M8L40.txt)</li> </ul>	<ul style="list-style-type: none"> <li>• On line Testing of Embedded Systems (M8L42.txt)</li> </ul>
<p>On line Testing of Embedded Systems (M8L42.txt)</p>	<ul style="list-style-type: none"> <li>• Built In Self Test BIST for Embedded Systems (M8L40.txt)</li> <li>• Introduction to Real Time Systems (M6L28.txt)</li> <li>• Testing Embedded Systems (M8L38.txt)</li> </ul>	

DAG for the course with Heuristic percentage Threshold and correlation H4 is as shown below in Figure B.1.

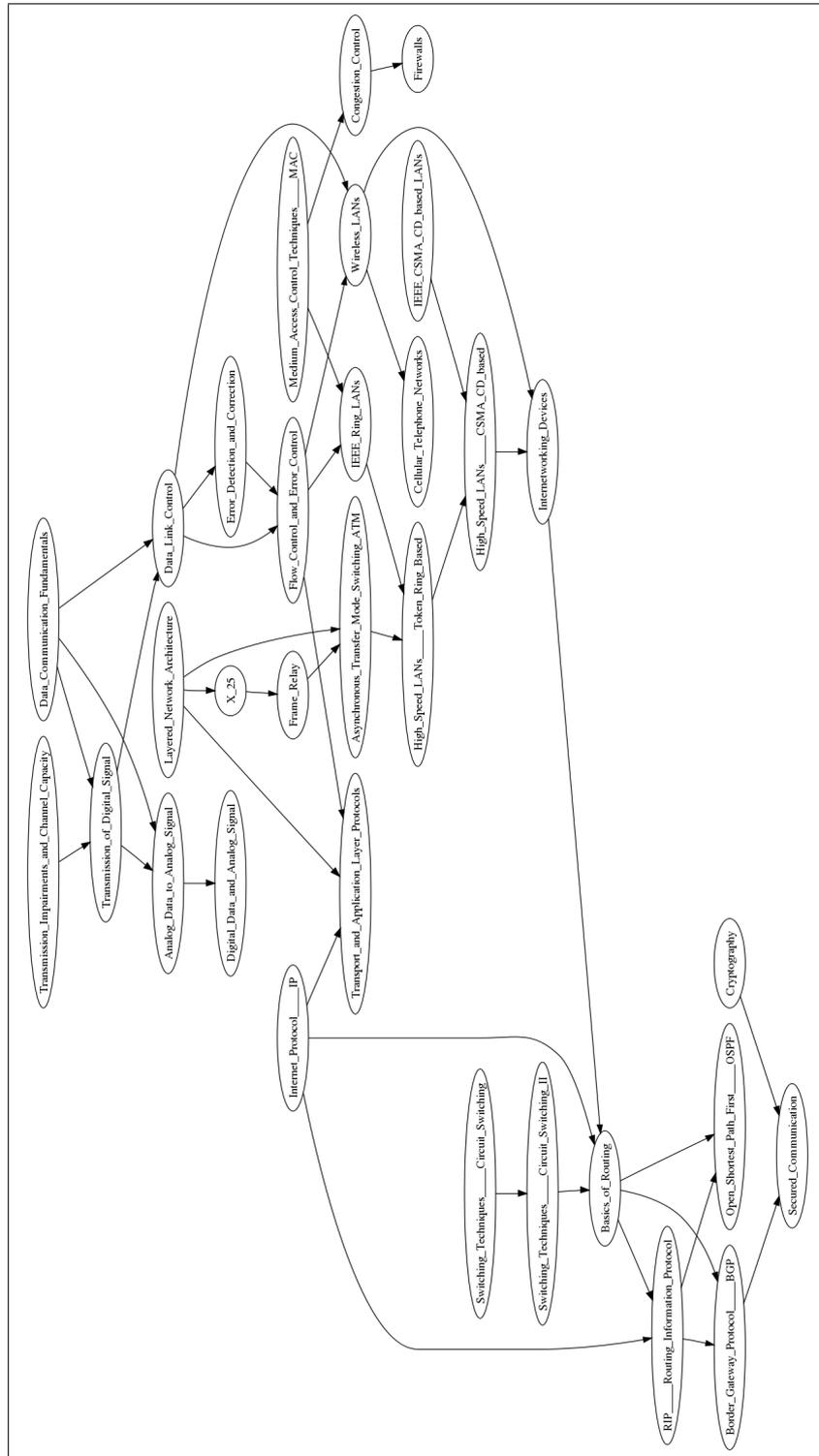


Figure B.1: DAG for the Embedded Systems course - Heuristic H4

## B. MORE EXPERIMENTAL RESULTS

DAG for the course Artificial Intelligence with Heuristic percentage Threshold and correlation H4 is as shown below in Figure B.2

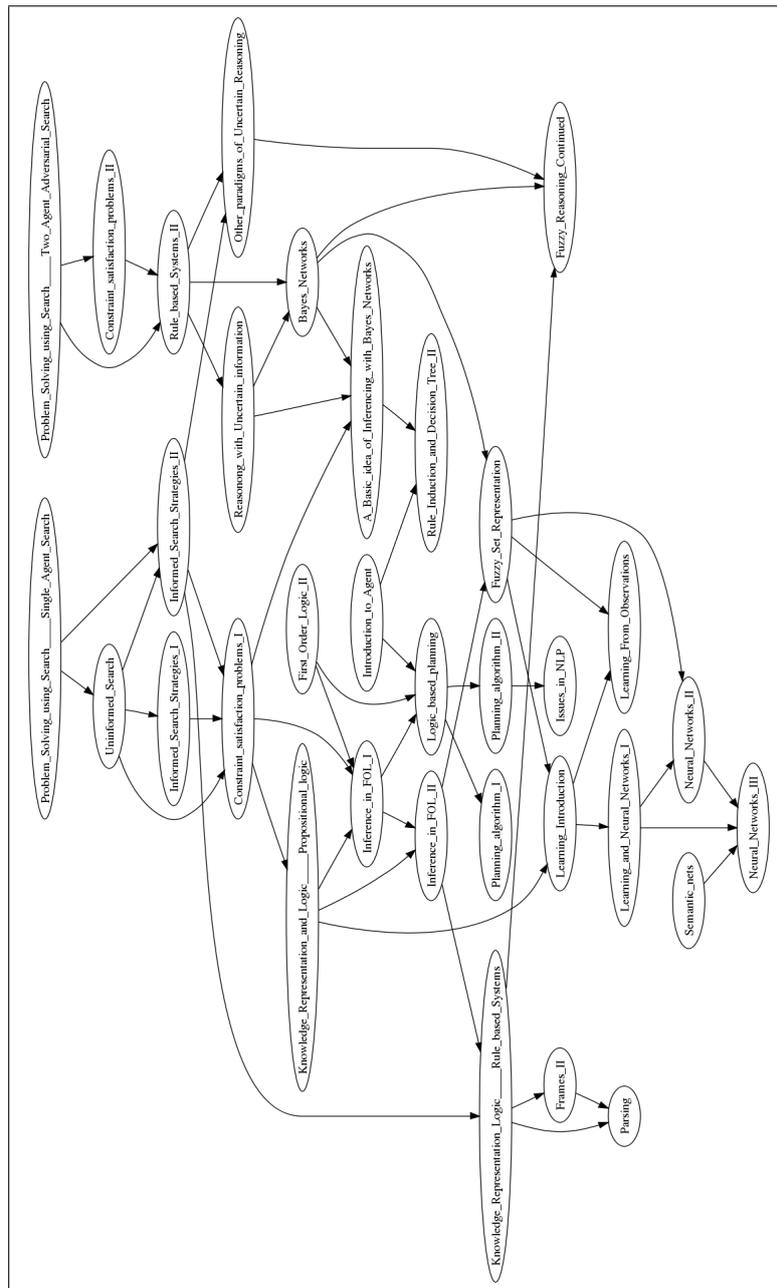


Figure B.2: DAG for the Artificial Intelligence course - Heuristic H4



## B. MORE EXPERIMENTAL RESULTS

DAG for the course Operating System with Heuristic percentage Threshold and correlation H4 is as shown below in FigureB.4

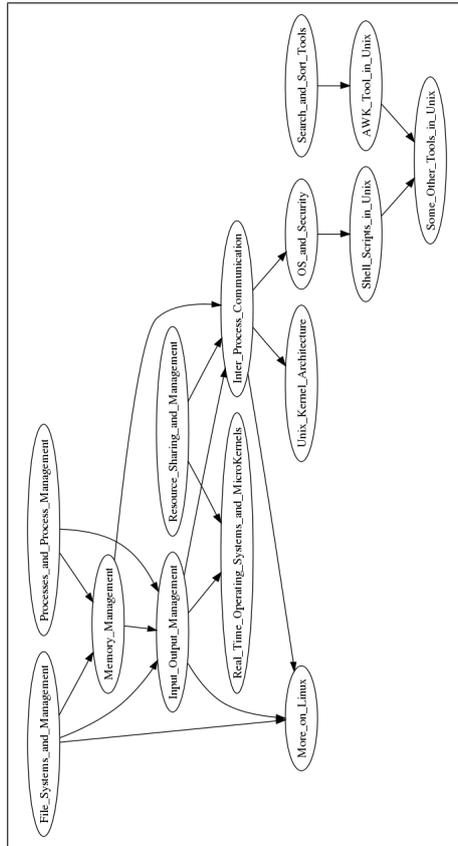


Figure B.4: DAG for the Operating Systems course - Heuristic H4

DAG for the course Systems Analysis and Design with Heuristic percentage Threshold and correlation H4 is as shown below in FigureB.5

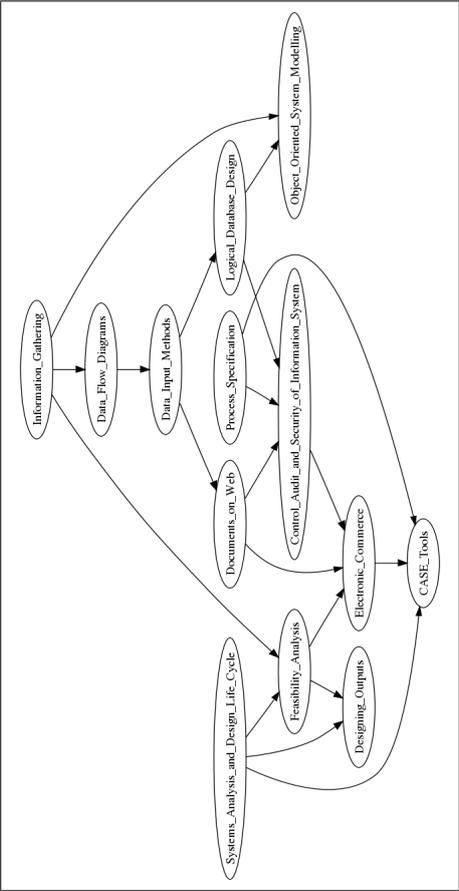


Figure B.5: DAG for the Systems Analysis and Design course - Heuristic H4



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