

General Data Flow Frameworks

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Part 1

About These Slides

Copyright

These slides constitute the lecture notes for CS618 Program Analysis course at IIT Bombay and have been made available as teaching material accompanying the book:

- Uday Khedker, Amitabha Sanyal, and Bageshri Karkare. *Data Flow Analysis: Theory and Practice*. CRC Press (Taylor and Francis Group). 2009.

(Indian edition published by Ane Books in 2013)

Apart from the above book, some slides are based on the material from the following book

- M. S. Hecht. *Flow Analysis of Computer Programs*. Elsevier North-Holland Inc. 1977.

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Outline

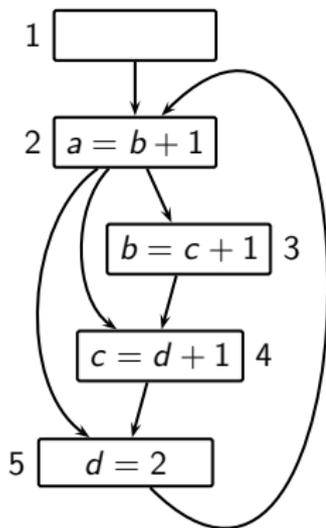
- Modelling General Flows
- Constant Propagation
- Strongly Live Variables Analysis (after mid-sem)
- Pointer Analyses (after mid-sem)
- Heap Reference Analysis (after mid-sem)



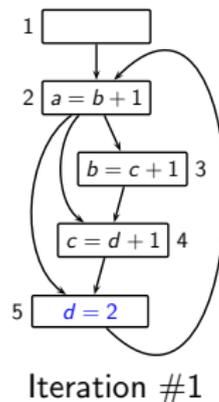
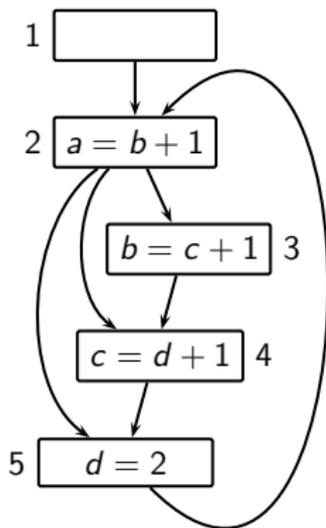
Part 2

Precise Modelling of General Flows

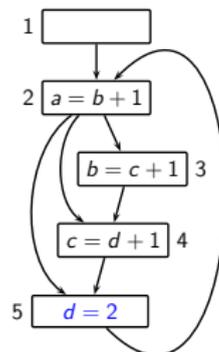
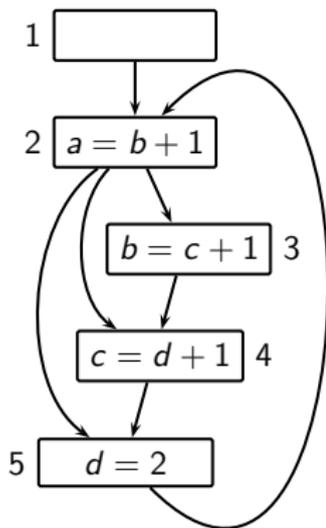
Complexity of Constant Propagation?



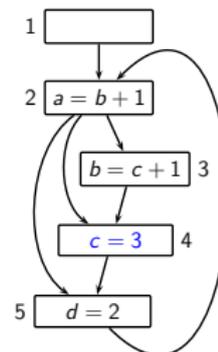
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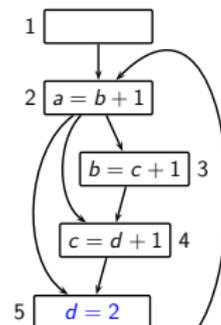
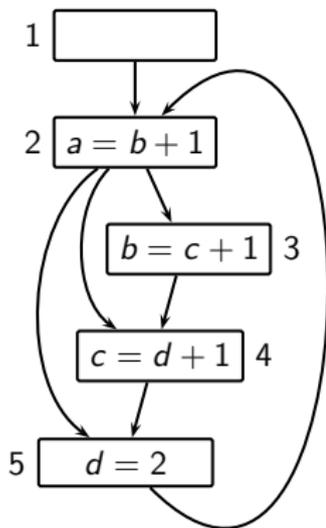
Iteration #1



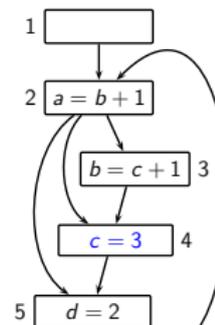
Iteration #2



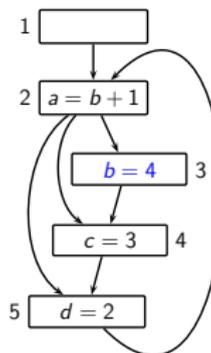
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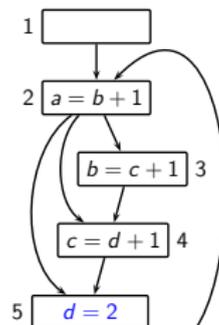
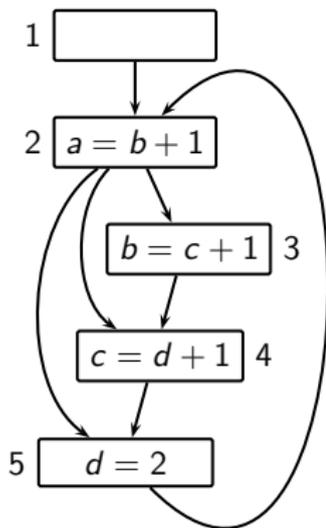
Iteration #2



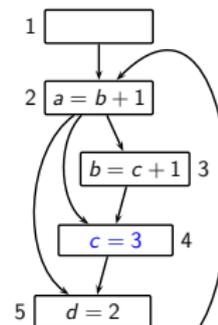
Iteration #3



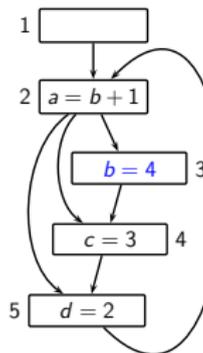
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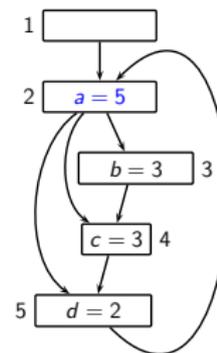
Iteration #1



Iteration #2



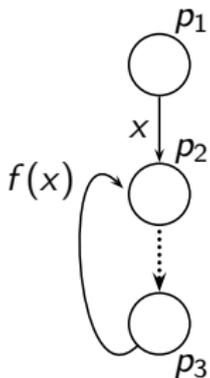
Iteration #3



Iteration #4



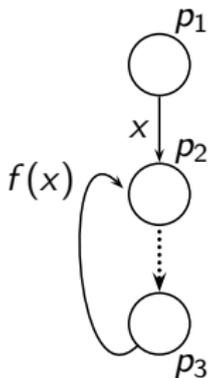
Loop Closures of Flow Functions



Paths Terminating at p_2	Data Flow Value
p_1, p_2	x
p_1, p_2, p_3, p_2	$f(x)$
$p_1, p_2, p_3, p_2, p_3, p_2$	$f(f(x)) = f^2(x)$
$p_1, p_2, p_3, p_2, p_3, p_2, p_3, p_2$	$f(f(f(x))) = f^3(x)$
...	...



Loop Closures of Flow Functions



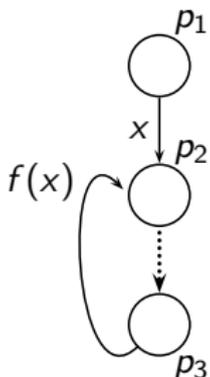
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...	...

- For static analysis we need to summarize the value at p_2 by a value which is safe after **any** iteration.

$$f^*(x) = x \sqcap f(x) \sqcap f^2(x) \sqcap f^3(x) \sqcap f^4(x) \sqcap \dots$$



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$$f^*(x) = x \sqcap f(x) \sqcap f^2(x) \sqcap f^3(x) \sqcap f^4(x) \sqcap \dots$$

- f^* is called the **loop closure** of f .



Loop Closure Boundedness

- Boundedness of f requires the existence of some k such that

$$f^*(x) = x \sqcap f(x) \sqcap f^2(x) \sqcap \dots \sqcap f^{k-1}(x)$$

- This follows from the descending chain condition
- For efficiency, we need a constant k that is independent of the size of the lattice



Loop Closures in Bit Vector Frameworks

- Flow functions in bit vector frameworks have constant Gen and Kill

$$\begin{aligned}f^*(x) &= x \sqcap f(x) \sqcap f^2(x) \sqcap f^3(x) \sqcap \dots \\f^2(x) &= f(\text{Gen} \cup (x - \text{Kill})) \\&= \text{Gen} \cup ((\text{Gen} \cup (x - \text{Kill})) - \text{Kill}) \\&= \text{Gen} \cup ((\text{Gen} - \text{Kill}) \cup (x - \text{Kill})) \\&= \text{Gen} \cup (\text{Gen} - \text{Kill}) \cup (x - \text{Kill}) \\&= \text{Gen} \cup (x - \text{Kill}) = f(x) \\f^*(x) &= x \sqcap f(x)\end{aligned}$$



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- Loop Closures of Bit Vector Frameworks are 2-bounded.*



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 \end{aligned}$$

- Loop Closures of Bit Vector Frameworks are 2-bounded.*
- Intuition: Since Gen and Kill are constant, same things are generated or killed in every application of f .
Multiple applications of f are not required unless the input value changes.



Larger Values of Loop Closure Bounds

- Fast Frameworks \equiv 2-bounded frameworks (eg. bit vector frameworks)

Both these conditions must be satisfied

- ▶ *Separability*

Data flow values of different entities are independent

- ▶ *Constant or Identity Flow Functions*

Flow functions for an entity are either constant or identity

- Non-fast frameworks

At least one of the above conditions is violated



Separability

$f : L \rightarrow L$ is $\langle \hat{h}_1, \hat{h}_2, \dots, \hat{h}_m \rangle$ where \hat{h}_i computes the value of \hat{x}_i



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Separable

Non-Separable

Example: All bit vector frameworks

Example: Constant Propagation



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Separable

$\langle \hat{x}_1, \hat{x}_2, \dots, \hat{x}_m \rangle$



$\langle \hat{y}_1, \hat{y}_2, \dots, \hat{y}_m \rangle$

Non-Separable

$\langle \hat{x}_1, \hat{x}_2, \dots, \hat{x}_m \rangle$



$\langle \hat{y}_1, \hat{y}_2, \dots, \hat{y}_m \rangle$

Example: All bit vector frameworks

Example: Constant Propagation

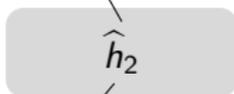


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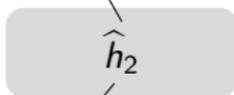


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$\hat{h} : \hat{L} \rightarrow \hat{L}$

Non-Separable

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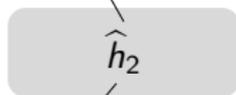


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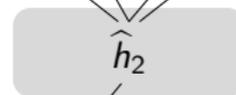


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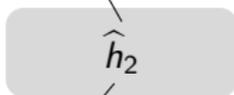


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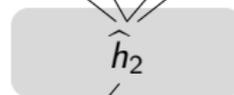
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Example: All bit vector frameworks

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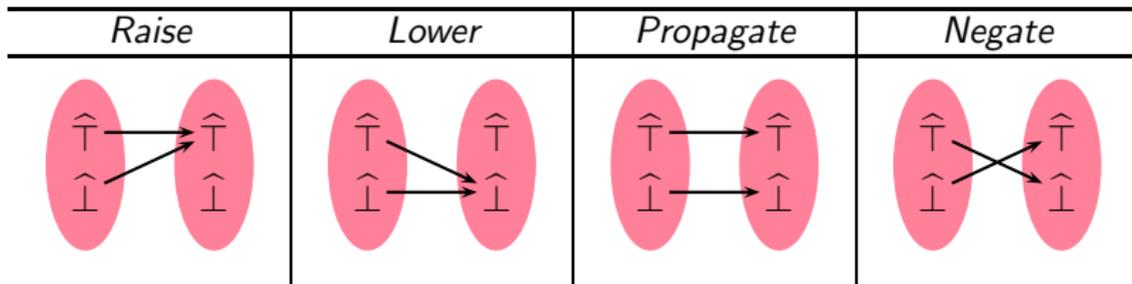
$\hat{h} : L \rightarrow \hat{L}$

Example: Constant Propagation



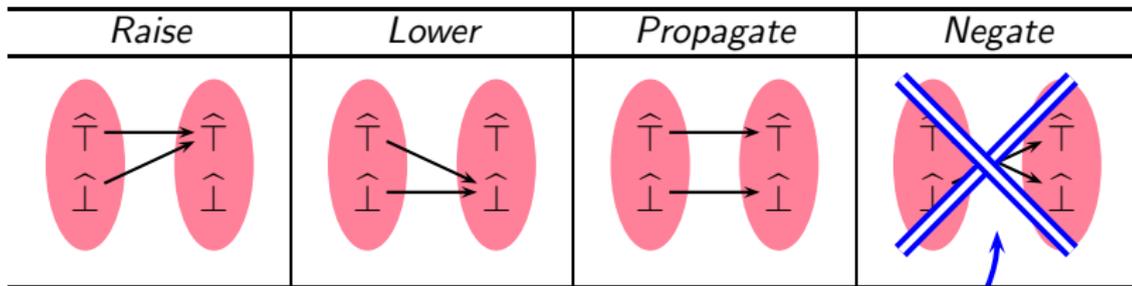
Separability of Bit Vector Frameworks

- \hat{L} is $\{0, 1\}$, L is $\{0, 1\}^m$
- $\hat{\Pi}$ is either boolean AND or boolean OR
- $\hat{\top}$ and $\hat{\perp}$ are 0 or 1 depending on $\hat{\Pi}$.
- \hat{h} is a *bit function* and could be one of the following:

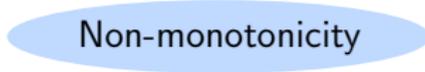


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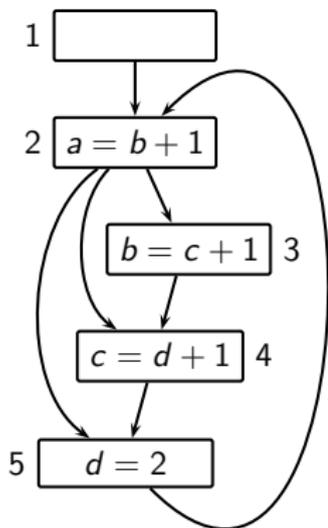
Non-monotonicity




Larger Values of Loop Closure Bounds

Composite flow function for the loop is

$$f(\langle v_a, v_b, v_c, v_d \rangle) = \langle v_b + 1, v_c + 1, v_d + 1, 2 \rangle$$

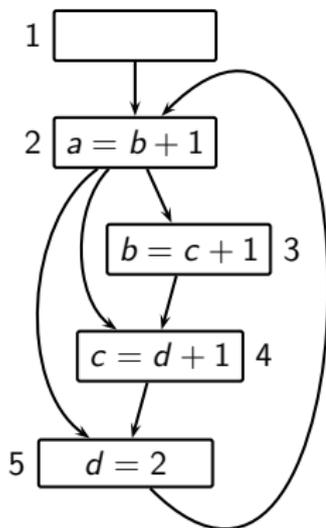


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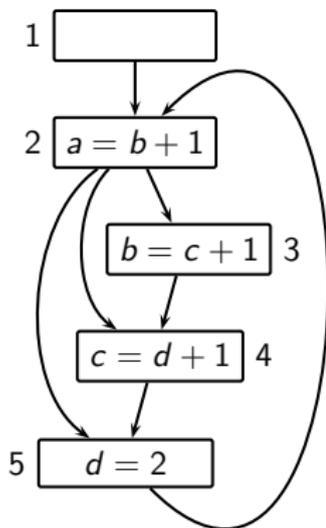
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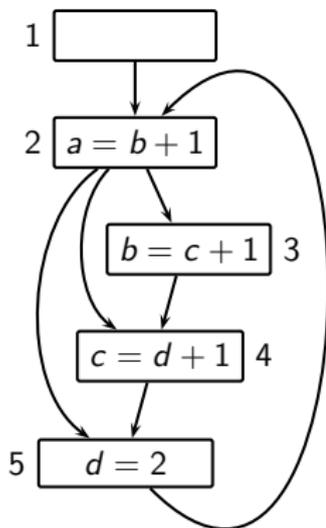
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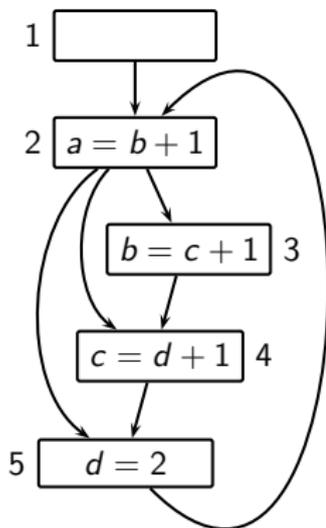
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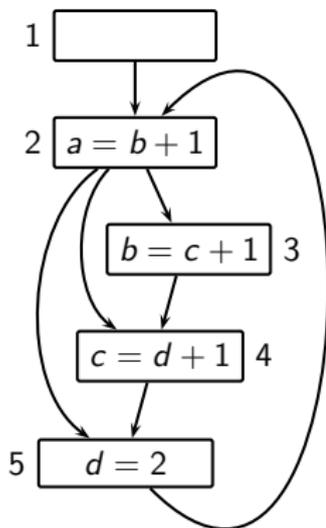
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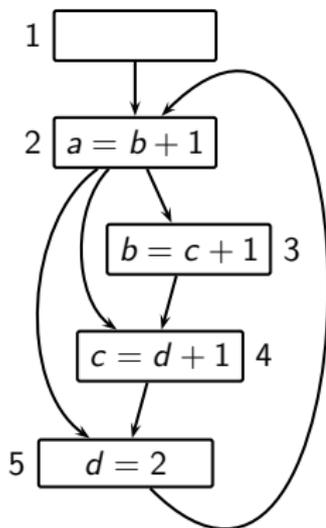


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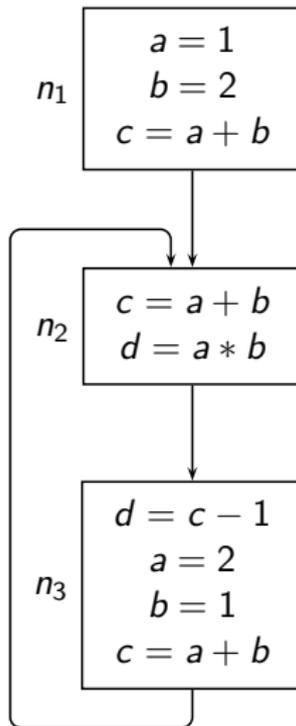
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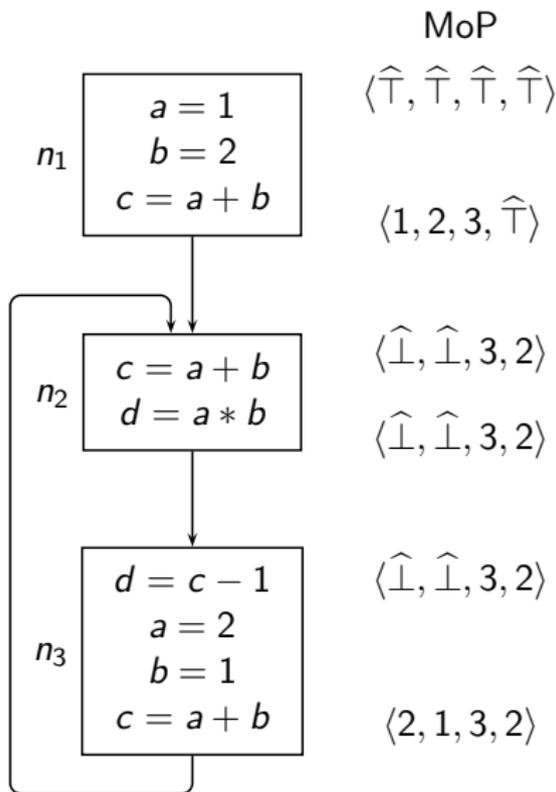
Part 3

Constant Propagation

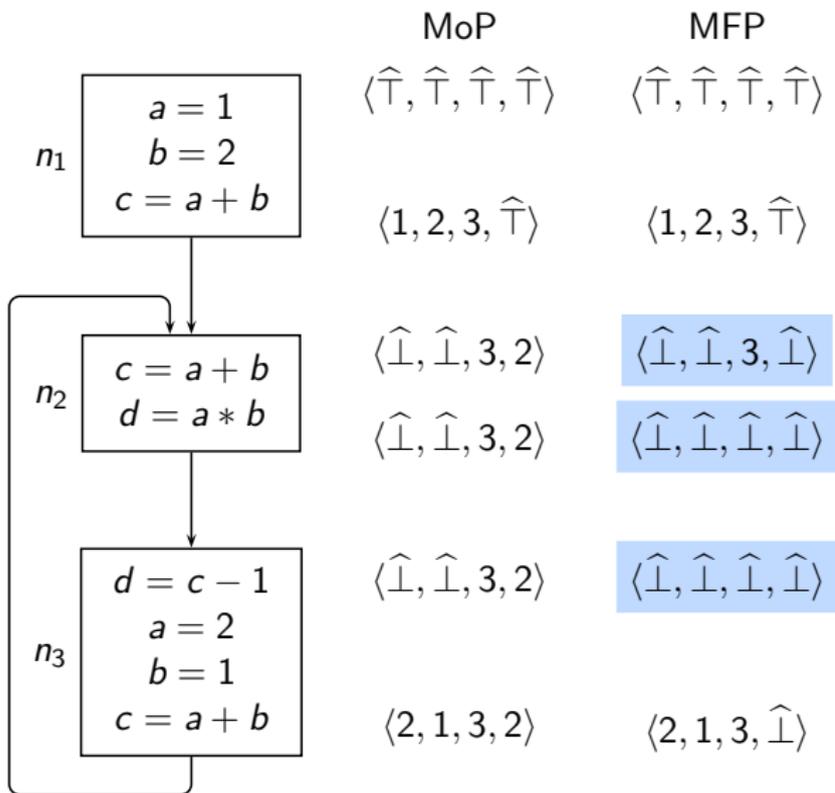
Example of Constant Propagation



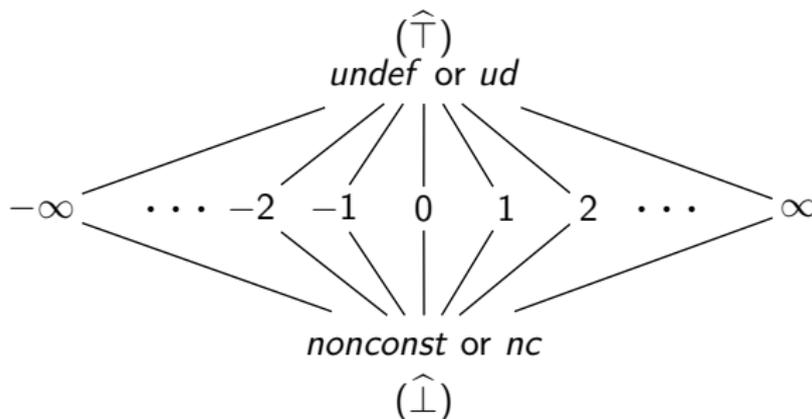
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Example of Constant Propagation



Component Lattice for Integer Constant Propagation



$\hat{\pi}$	$\langle v, ud \rangle$	$\langle v, nc \rangle$	$\langle v, c_1 \rangle$
$\langle v, ud \rangle$	$\langle v, ud \rangle$	$\langle v, nc \rangle$	$\langle v, c_1 \rangle$
$\langle v, nc \rangle$	$\langle v, nc \rangle$	$\langle v, nc \rangle$	$\langle v, nc \rangle$
$\langle v, c_2 \rangle$	$\langle v, c_2 \rangle$	$\langle v, nc \rangle$	If $c_1 = c_2$ then $\langle v, c_1 \rangle$ else $\langle v, nc \rangle$



Overall Lattice for Integer Constant Propagation

- In_n/Out_n values are mappings $\mathbb{V}ar \rightarrow \hat{L}: In_n, Out_n \in \mathbb{V}ar \rightarrow \hat{L}$
- Overall lattice L is a set of mappings $\mathbb{V}ar \rightarrow \hat{L}: L = \mathbb{V}ar \rightarrow \hat{L}$
- \sqcap and $\hat{\sqcap}$ get defined by \sqsubseteq and $\hat{\sqsubseteq}$
 - ▶ Partial order is restricted to data flow values of the same variable
Data flow values of different variables are incomparable

$$(x, v_1) \sqsubseteq (y, v_2) \Leftrightarrow x = y \wedge v_1 \hat{\sqsubseteq} v_2$$

OR

$$x \mapsto v_1 \sqsubseteq y \mapsto v_2 \Leftrightarrow x = y \wedge v_1 \hat{\sqsubseteq} v_2$$

- ▶ For meet operation, we assume that X is a total function
Partial functions are made total by using $\hat{\top}$ value

$$X \sqcap Y = \{(x, v_1 \hat{\sqcap} v_2) \mid (x, v_1) \in X, (x, v_2) \in Y\}$$

$$\text{OR} \quad X \sqcap Y = \{x \mapsto v_1 \hat{\sqcap} v_2 \mid x \mapsto v_1 \in X, x \mapsto v_2 \in Y\}$$



Notations for Mappings as Data Flow Values

Accessing and manipulating a mapping $X \subseteq A \rightarrow B$

- $X(a)$ denotes the image of $a \in A$
 $X(a) \in B$
- $X[a \mapsto v]$ changes the image of a in X to v

$$X[a \mapsto v] = (X - \{(a, u) \mid u \in B\}) \cup \{(a, v)\}$$



Defining Data Flow Equations for Constant Propagation

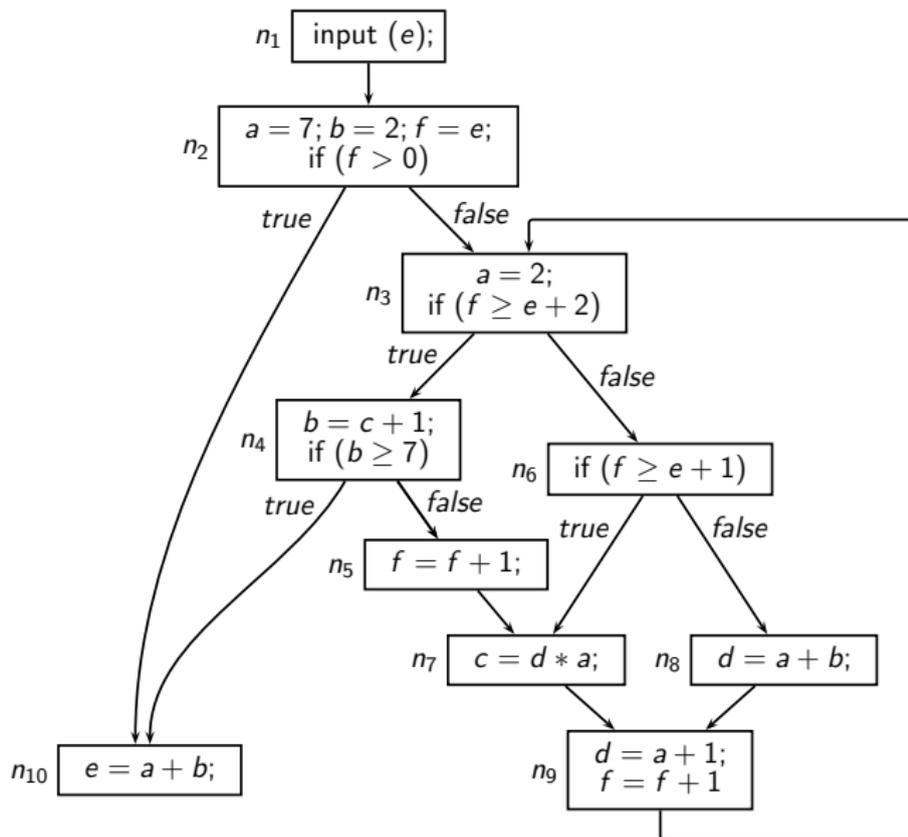
$$\begin{aligned}
 In_n &= \begin{cases} Bl = \{\langle y, ud \rangle \mid y \in \mathbb{V}ar\} & n = Start \\ \prod_{p \in pred(n)} Out_p & \text{otherwise} \end{cases} \\
 Out_n &= f_n(In_n)
 \end{aligned}$$

$$f_n(X) = \begin{cases} X[y \mapsto c] & n \text{ is } y = c, y \in \mathbb{V}ar, c \in \mathbb{C}onst \\ X[y \mapsto nc] & n \text{ is } input(y), y \in var \\ X[y \mapsto X(z)] & n \text{ is } y = z, y \in \mathbb{V}ar, z \in \mathbb{V}ar \\ X[y \mapsto eval(e, X)] & n \text{ is } y = e, y \in \mathbb{V}ar, e \in \mathbb{E}xpr \\ X & \text{otherwise} \end{cases}$$

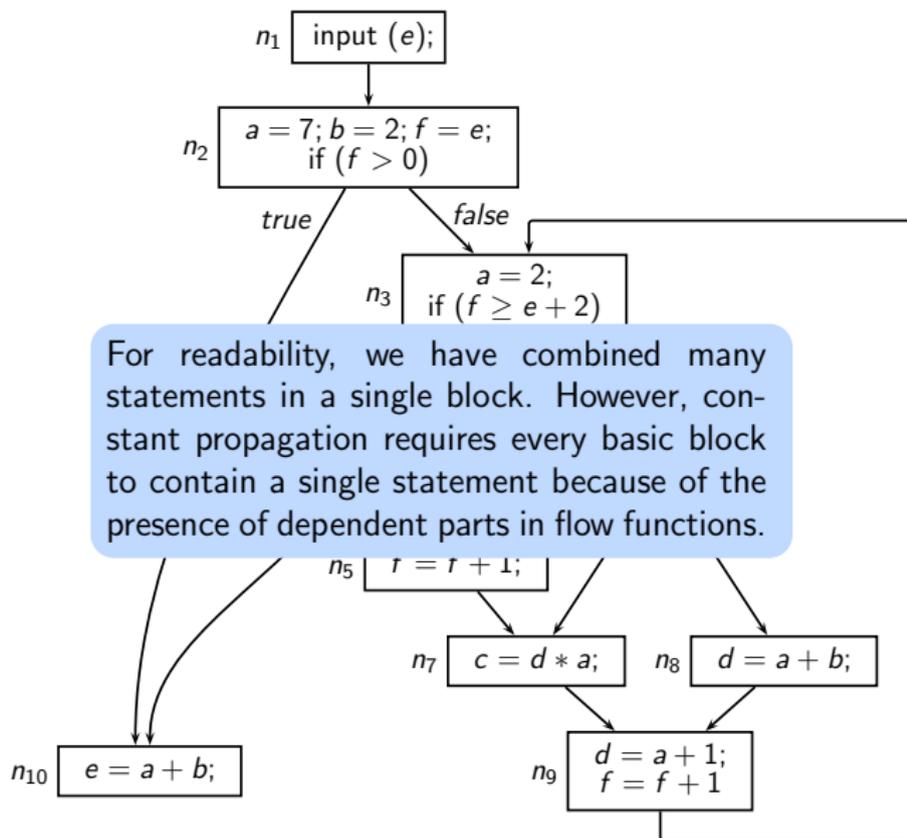
$$eval(e, X) = \begin{cases} nc & a \in Opd(e) \cap \mathbb{V}ar, X(a) = nc \\ ud & a \in Opd(e) \cap \mathbb{V}ar, X(a) = ud \\ -X(a) & e \text{ is } -a \\ X(a) \oplus X(b) & e \text{ is } a \oplus b \end{cases}$$



Example Program for Constant Propagation



Example Program for Constant Propagation

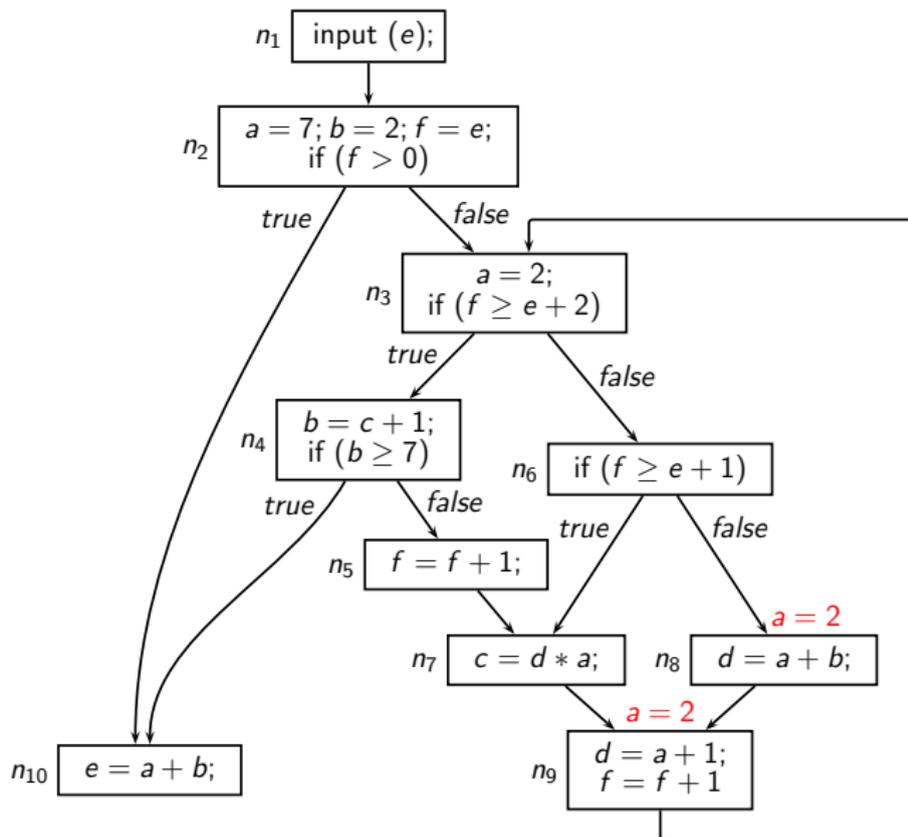


Result of Constant Propagation

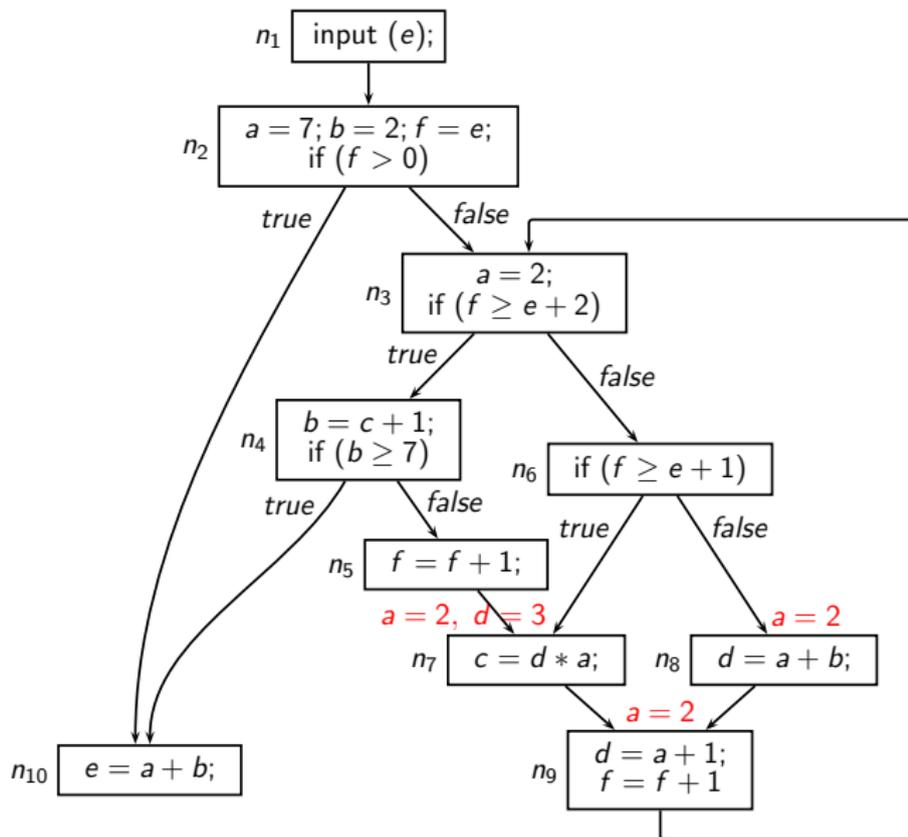
	Iteration #1	Changes in iteration #2	Changes in iteration #3	Changes in iteration #4
In_{n_1}	$\hat{\top}, \hat{\top}, \hat{\top}, \hat{\top}, \hat{\top}, \hat{\top}$			
Out_{n_1}	$\hat{\top}, \hat{\top}, \hat{\top}, \hat{\top}, \hat{\perp}, \hat{\top}$			
In_{n_2}	$\hat{\top}, \hat{\top}, \hat{\top}, \hat{\top}, \hat{\perp}, \hat{\top}$			
Out_{n_2}	$7, 2, \hat{\top}, \hat{\top}, \hat{\perp}, \hat{\perp}$			
In_{n_3}	$7, 2, \hat{\top}, \hat{\top}, \hat{\perp}, \hat{\perp}$	$\hat{\perp}, 2, \hat{\top}, 3, \hat{\perp}, \hat{\perp}$	$\hat{\perp}, 2, 6, 3, \hat{\perp}, \hat{\perp}$	$\hat{\perp}, \hat{\perp}, 6, 3, \hat{\perp}, \hat{\perp}$
Out_{n_3}	$2, 2, \hat{\top}, \hat{\top}, \hat{\perp}, \hat{\perp}$	$2, 2, \hat{\top}, 3, \hat{\perp}, \hat{\perp}$	$2, 2, 6, 3, \hat{\perp}, \hat{\perp}$	$2, \hat{\perp}, 6, 3, \hat{\perp}, \hat{\perp}$
In_{n_4}	$2, 2, \hat{\top}, \hat{\top}, \hat{\perp}, \hat{\perp}$	$2, 2, \hat{\top}, 3, \hat{\perp}, \hat{\perp}$	$2, 2, 6, 3, \hat{\perp}, \hat{\perp}$	$2, \hat{\perp}, 6, 3, \hat{\perp}, \hat{\perp}$
Out_{n_4}	$2, \hat{\top}, \hat{\top}, \hat{\top}, \hat{\perp}, \hat{\perp}$	$2, \hat{\top}, \hat{\top}, 3, \hat{\perp}, \hat{\perp}$	$2, 7, 6, 3, \hat{\perp}, \hat{\perp}$	
In_{n_5}	$2, \hat{\top}, \hat{\top}, \hat{\top}, \hat{\perp}, \hat{\perp}$	$2, \hat{\top}, \hat{\top}, 3, \hat{\perp}, \hat{\perp}$	$2, 7, 6, 3, \hat{\perp}, \hat{\perp}$	
Out_{n_5}	$2, \hat{\top}, \hat{\top}, \hat{\top}, \hat{\perp}, \hat{\perp}$	$2, \hat{\top}, \hat{\top}, 3, \hat{\perp}, \hat{\perp}$	$2, 7, 6, 3, \hat{\perp}, \hat{\perp}$	
In_{n_6}	$2, 2, \hat{\top}, \hat{\top}, \hat{\perp}, \hat{\perp}$	$2, 2, \hat{\top}, 3, \hat{\perp}, \hat{\perp}$	$2, 2, 6, 3, \hat{\perp}, \hat{\perp}$	$2, \hat{\perp}, 6, 3, \hat{\perp}, \hat{\perp}$
Out_{n_6}	$2, 2, \hat{\top}, \hat{\top}, \hat{\perp}, \hat{\perp}$	$2, 2, \hat{\top}, 3, \hat{\perp}, \hat{\perp}$	$2, 2, 6, 3, \hat{\perp}, \hat{\perp}$	$2, \hat{\perp}, 6, 3, \hat{\perp}, \hat{\perp}$
In_{n_7}	$2, 2, \hat{\top}, \hat{\top}, \hat{\perp}, \hat{\perp}$	$2, 2, \hat{\top}, 3, \hat{\perp}, \hat{\perp}$	$2, \hat{\perp}, 6, 3, \hat{\perp}, \hat{\perp}$	
Out_{n_7}	$2, 2, \hat{\top}, \hat{\top}, \hat{\perp}, \hat{\perp}$	$2, 2, 6, 3, \hat{\perp}, \hat{\perp}$	$2, \hat{\perp}, 6, 3, \hat{\perp}, \hat{\perp}$	
In_{n_8}	$2, 2, \hat{\top}, \hat{\top}, \hat{\perp}, \hat{\perp}$	$2, 2, \hat{\top}, 3, \hat{\perp}, \hat{\perp}$	$2, 2, 6, 3, \hat{\perp}, \hat{\perp}$	$2, \hat{\perp}, 6, 3, \hat{\perp}, \hat{\perp}$
Out_{n_8}	$2, 2, \hat{\top}, 4, \hat{\perp}, \hat{\perp}$	$2, 2, \hat{\top}, 4, \hat{\perp}, \hat{\perp}$	$2, 2, 6, 4, \hat{\perp}, \hat{\perp}$	$2, \hat{\perp}, 6, \hat{\perp}, \hat{\perp}, \hat{\perp}$
In_{n_9}	$2, 2, \hat{\top}, 4, \hat{\perp}, \hat{\perp}$	$2, 2, 6, \hat{\perp}, \hat{\perp}, \hat{\perp}$	$2, \hat{\perp}, 6, \hat{\perp}, \hat{\perp}, \hat{\perp}$	
Out_{n_9}	$2, 2, \hat{\top}, 3, \hat{\perp}, \hat{\perp}$	$2, 2, 6, 3, \hat{\perp}, \hat{\perp}$	$2, \hat{\perp}, 6, 3, \hat{\perp}, \hat{\perp}$	
$In_{n_{10}}$	$\hat{\perp}, 2, \hat{\top}, \hat{\top}, \hat{\perp}, \hat{\perp}$	$\hat{\perp}, 2, \hat{\top}, 3, \hat{\perp}, \hat{\perp}$	$\hat{\perp}, \hat{\perp}, 6, 3, \hat{\perp}, \hat{\perp}$	
$Out_{n_{10}}$	$\hat{\perp}, 2, \hat{\top}, \hat{\top}, \hat{\perp}, \hat{\perp}$	$\hat{\perp}, 2, \hat{\top}, 3, \hat{\perp}, \hat{\perp}$	$\hat{\perp}, \hat{\perp}, 6, 3, \hat{\perp}, \hat{\perp}$	



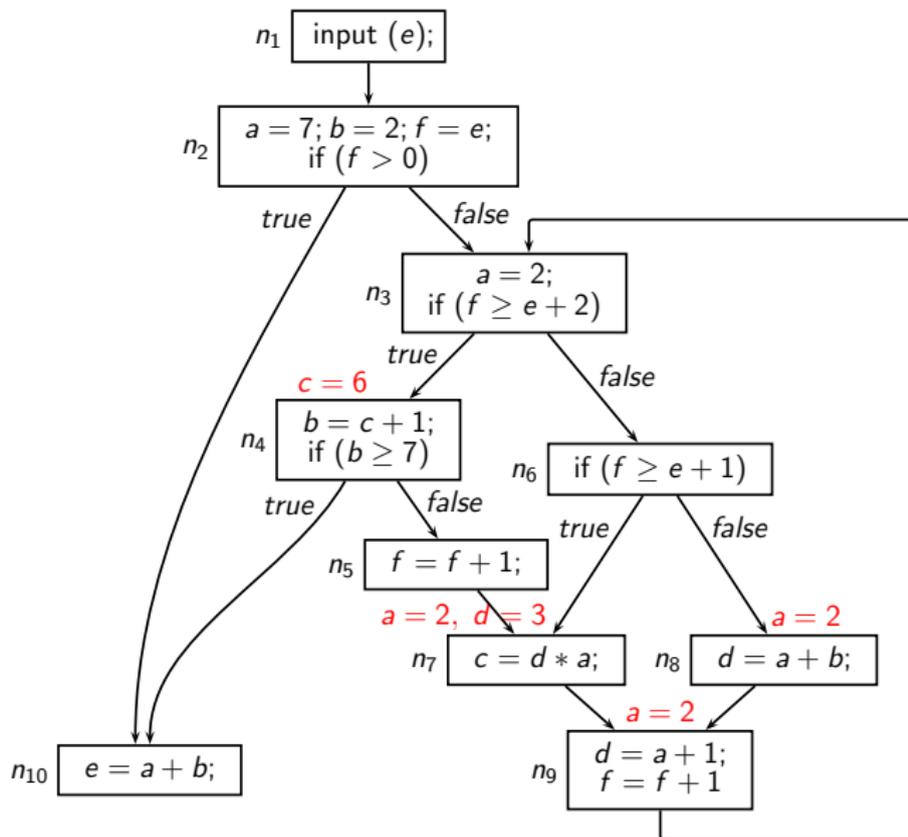
Result of Constant Propagation



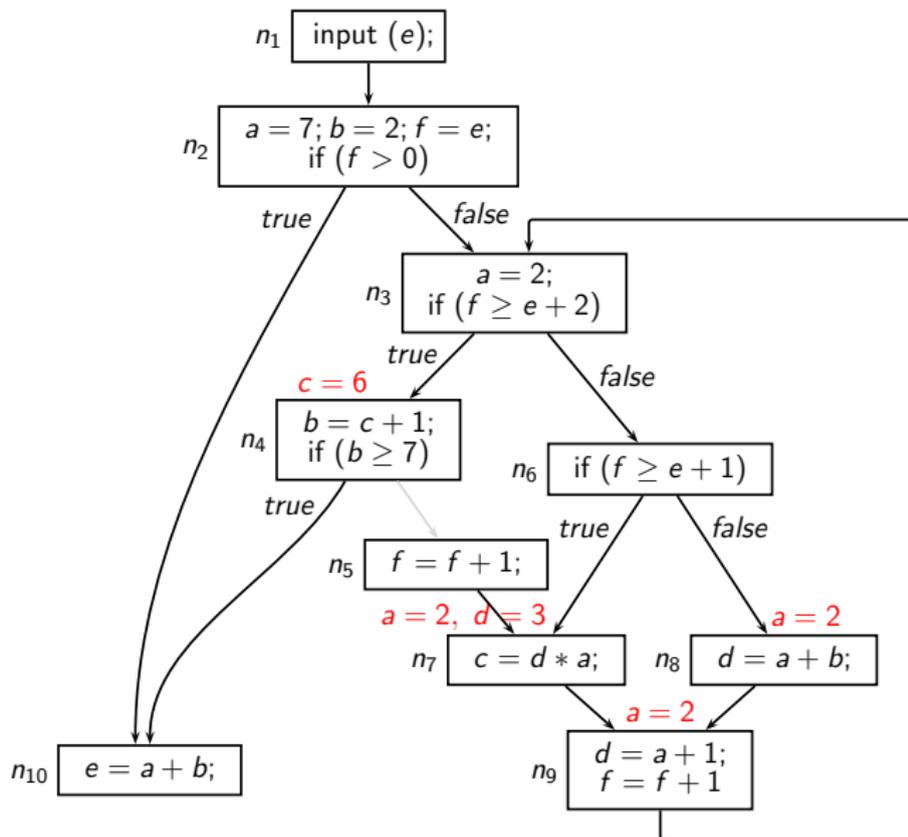
Result of Constant Propagation



Result of Constant Propagation



Result of Constant Propagation



Monotonicity of Constant Propagation

Proof obligation: $X_1 \sqsubseteq X_2 \Rightarrow f_n(X_1) \sqsubseteq f_n(X_2)$

where,

$$f_n(X) = \begin{cases} X [y \mapsto c] & n \text{ is } y = c, y \in \mathbb{V}\text{ar}, c \in \mathbb{C}\text{onst} & (C1) \\ X [y \mapsto nc] & n \text{ is } \textit{input}(y), y \in \textit{var} & (C2) \\ X [y \mapsto X(z)] & n \text{ is } y = z, y \in \mathbb{V}\text{ar}, z \in \mathbb{V}\text{ar} & (C3) \\ X [y \mapsto \textit{eval}(e, X)] & n \text{ is } y = e, y \in \mathbb{V}\text{ar}, e \in \mathbb{E}\text{xpr} & (C4) \\ X & \text{otherwise} & (C5) \end{cases}$$

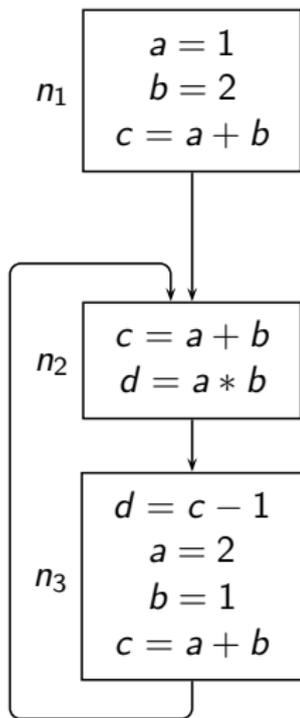
- The proof obligation trivially follows for cases C1, C2, C3, and C5
- For case C4, it requires showing

$$X_1 \sqsubseteq X_2 \Rightarrow \textit{eval}(e, X_1) \sqsubseteq \textit{eval}(e, X_2)$$

which follows from the definition of $\textit{eval}(e, X)$

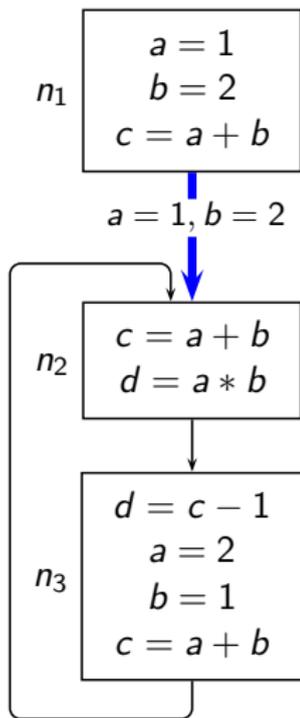


Non-Distributivity of Constant Propagation

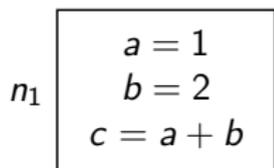


Non-Distributivity of Constant Propagation

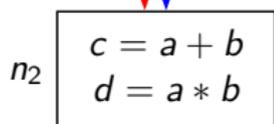
- $x = \langle 1, 2, 3, ? \rangle$ (Along $Out_{n_1} \rightarrow In_{n_2}$)



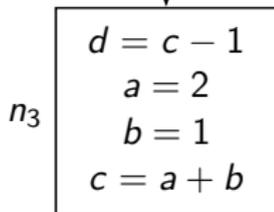
Non-Distributivity of Constant Propagation



$a = 1, b = 2$



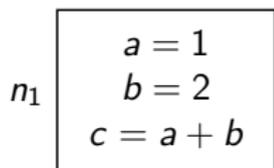
$a = 2, b = 1$



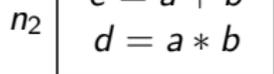
- $x = \langle 1, 2, 3, ? \rangle$ (Along $Out_{n_1} \rightarrow In_{n_2}$)
- $y = \langle 2, 1, 3, 2 \rangle$ (Along $Out_{n_3} \rightarrow In_{n_2}$)



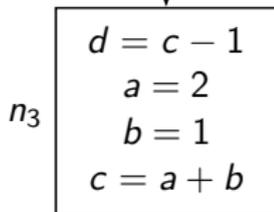
Non-Distributivity of Constant Propagation



$a = 1, b = 2$



$a = 2, b = 1$

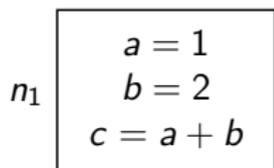


- $x = \langle 1, 2, 3, ? \rangle$ (Along $Out_{n_1} \rightarrow In_{n_2}$)
- $y = \langle 2, 1, 3, 2 \rangle$ (Along $Out_{n_3} \rightarrow In_{n_2}$)
- Function application before merging

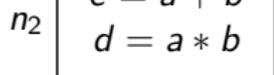
$$\begin{aligned} f(x) \sqcap f(y) &= f(\langle 1, 2, 3, ? \rangle) \sqcap f(\langle 2, 1, 3, 2 \rangle) \\ &= \langle 1, 2, 3, 2 \rangle \sqcap \langle 2, 1, 3, 2 \rangle \\ &= \langle \hat{\perp}, \hat{\perp}, 3, 2 \rangle \end{aligned}$$



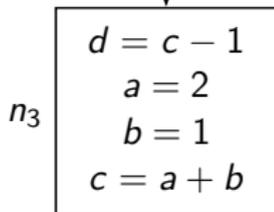
Non-Distributivity of Constant Propagation



$a = 1, b = 2$



$a = 2, b = 1$



- $x = \langle 1, 2, 3, ? \rangle$ (Along $Out_{n_1} \rightarrow In_{n_2}$)
- $y = \langle 2, 1, 3, 2 \rangle$ (Along $Out_{n_3} \rightarrow In_{n_2}$)
- Function application before merging

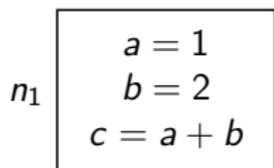
$$\begin{aligned} f(x) \sqcap f(y) &= f(\langle 1, 2, 3, ? \rangle) \sqcap f(\langle 2, 1, 3, 2 \rangle) \\ &= \langle 1, 2, 3, 2 \rangle \sqcap \langle 2, 1, 3, 2 \rangle \\ &= \langle \hat{\perp}, \hat{\perp}, 3, 2 \rangle \end{aligned}$$

- Function application after merging

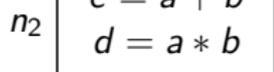
$$\begin{aligned} f(x \sqcap y) &= f(\langle 1, 2, 3, ? \rangle \sqcap \langle 2, 1, 3, 2 \rangle) \\ &= f(\langle \hat{\perp}, \hat{\perp}, 3, 2 \rangle) \\ &= \langle \hat{\perp}, \hat{\perp}, \hat{\perp}, \hat{\perp} \rangle \end{aligned}$$



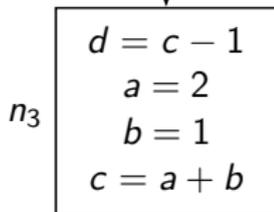
Non-Distributivity of Constant Propagation



$a = 1, b = 2$



$a = 2, b = 1$



- $x = \langle 1, 2, 3, ? \rangle$ (Along $Out_{n_1} \rightarrow In_{n_2}$)
- $y = \langle 2, 1, 3, 2 \rangle$ (Along $Out_{n_3} \rightarrow In_{n_2}$)
- Function application before merging

$$\begin{aligned} f(x) \sqcap f(y) &= f(\langle 1, 2, 3, ? \rangle) \sqcap f(\langle 2, 1, 3, 2 \rangle) \\ &= \langle 1, 2, 3, 2 \rangle \sqcap \langle 2, 1, 3, 2 \rangle \\ &= \langle \hat{1}, \hat{1}, 3, 2 \rangle \end{aligned}$$

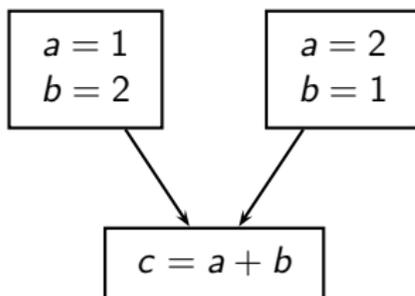
- Function application after merging

$$\begin{aligned} f(x \sqcap y) &= f(\langle 1, 2, 3, ? \rangle \sqcap \langle 2, 1, 3, 2 \rangle) \\ &= f(\langle \hat{1}, \hat{1}, 3, 2 \rangle) \\ &= \langle \hat{1}, \hat{1}, \hat{1}, \hat{1} \rangle \end{aligned}$$

- $f(x \sqcap y) \sqsubset f(x) \sqcap f(y)$

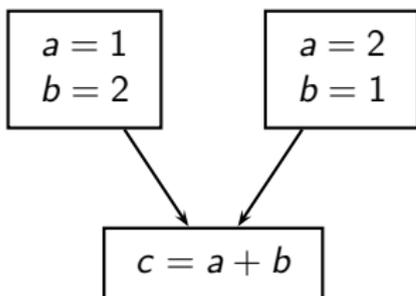


Why is Constant Propagation Non-Distributive?



Why is Constant Propagation Non-Distributive?

Possible combinations due to merging



$a = 1$

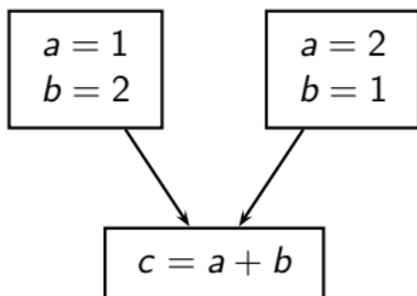
$a = 2$

$b = 1$

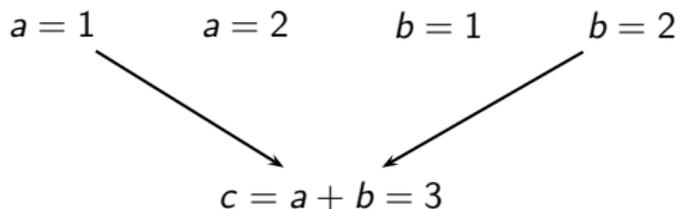
$b = 2$



Why is Constant Propagation Non-Distributive?



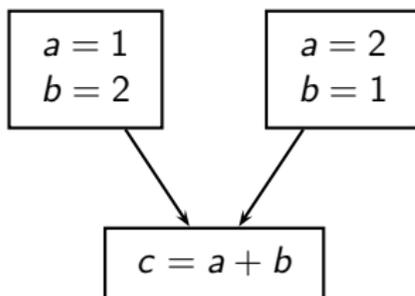
Possible combinations due to merging



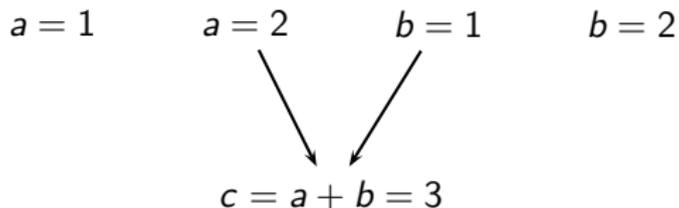
- Correct combination.



Why is Constant Propagation Non-Distributive?



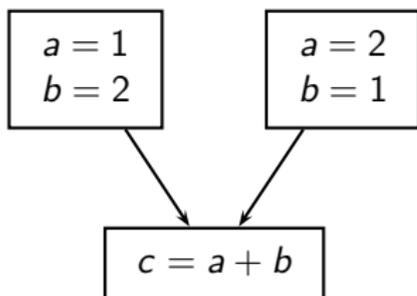
Possible combinations due to merging



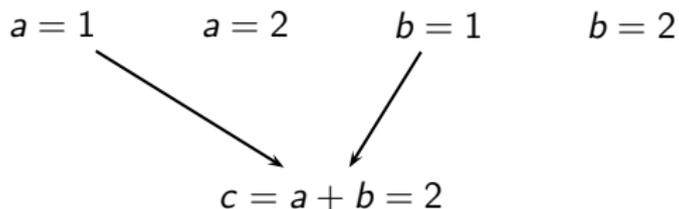
- Correct combination.



Why is Constant Propagation Non-Distributive?



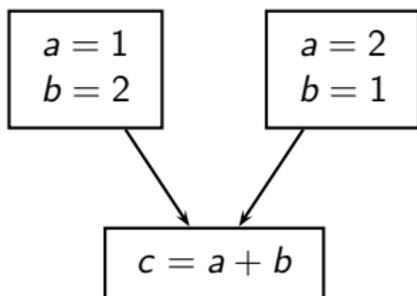
Possible combinations due to merging



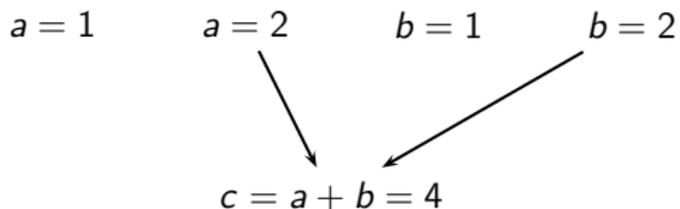
- Wrong combination.
- Mutually exclusive information.
- No execution path along which this information holds.



Why is Constant Propagation Non-Distributive?



Possible combinations due to merging

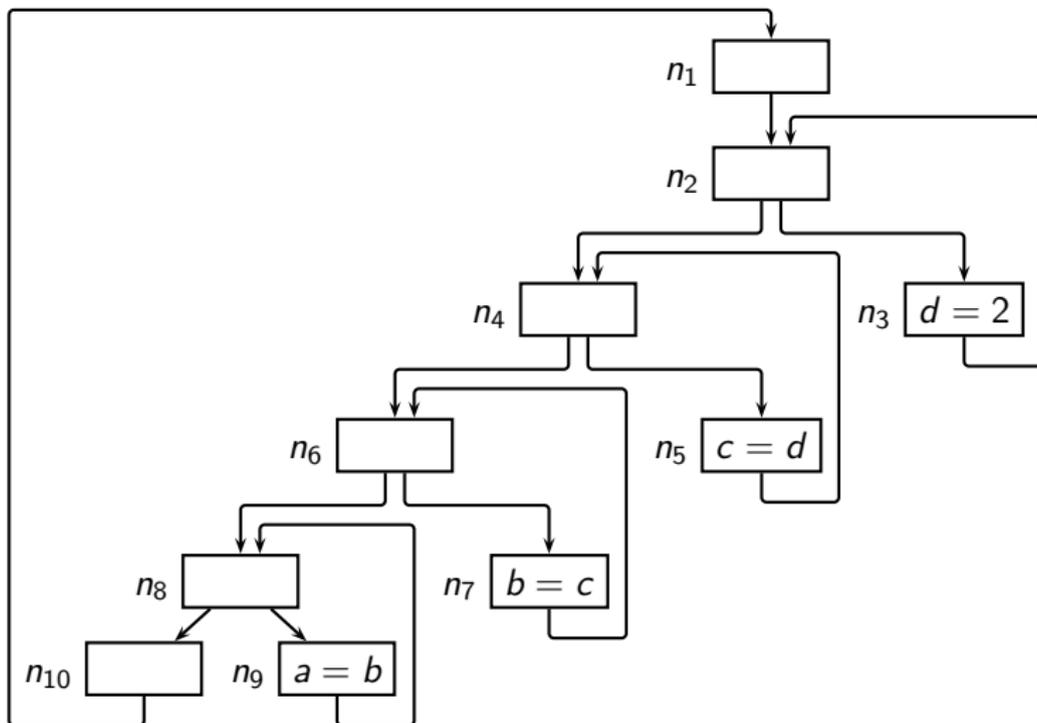


- Wrong combination.
- Mutually exclusive information.
- No execution path along which this information holds.



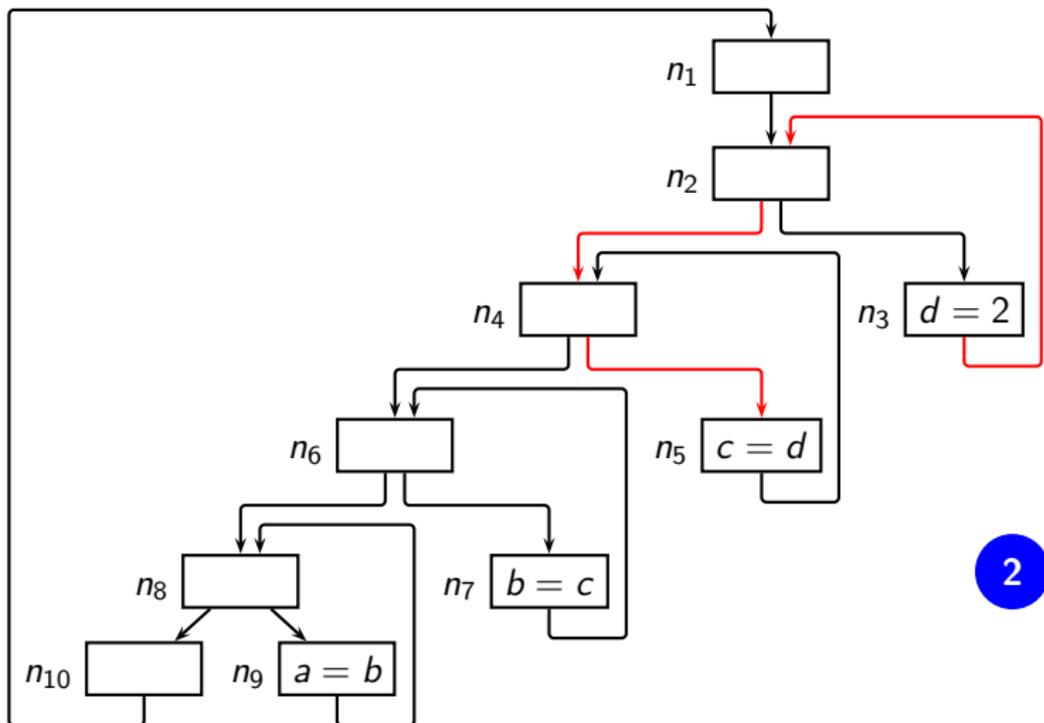
Tutorial Problem on Constant Propagation

How many iterations do we need?



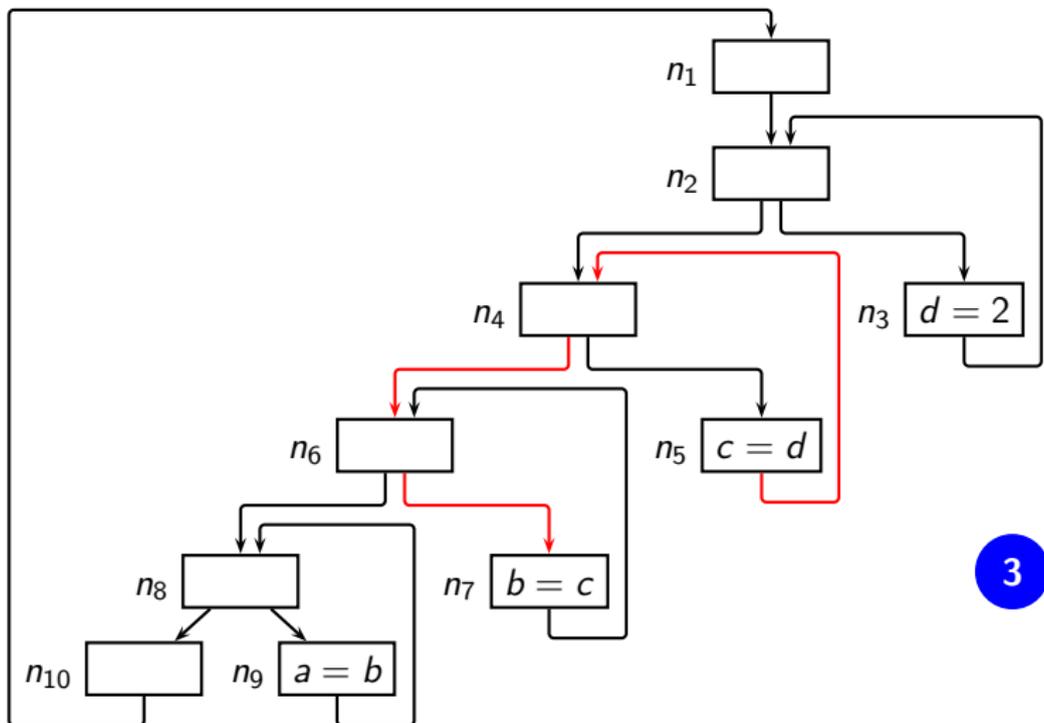
Tutorial Problem on Constant Propagation

How many iterations do we need?



Tutorial Problem on Constant Propagation

How many iterations do we need?

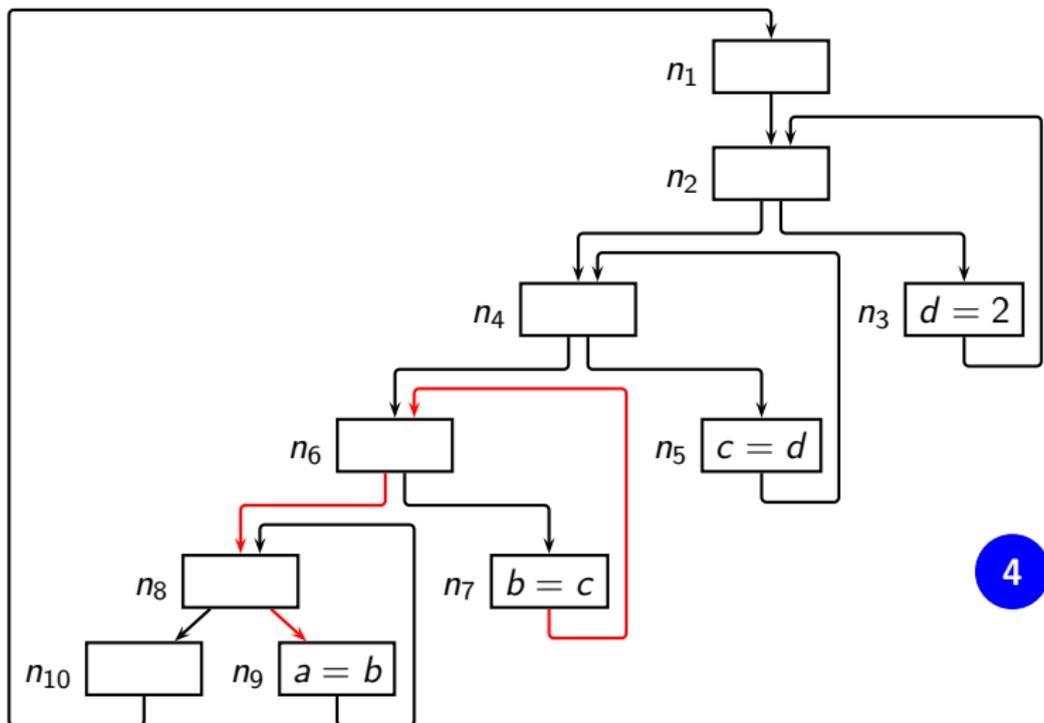


3



Tutorial Problem on Constant Propagation

How many iterations do we need?

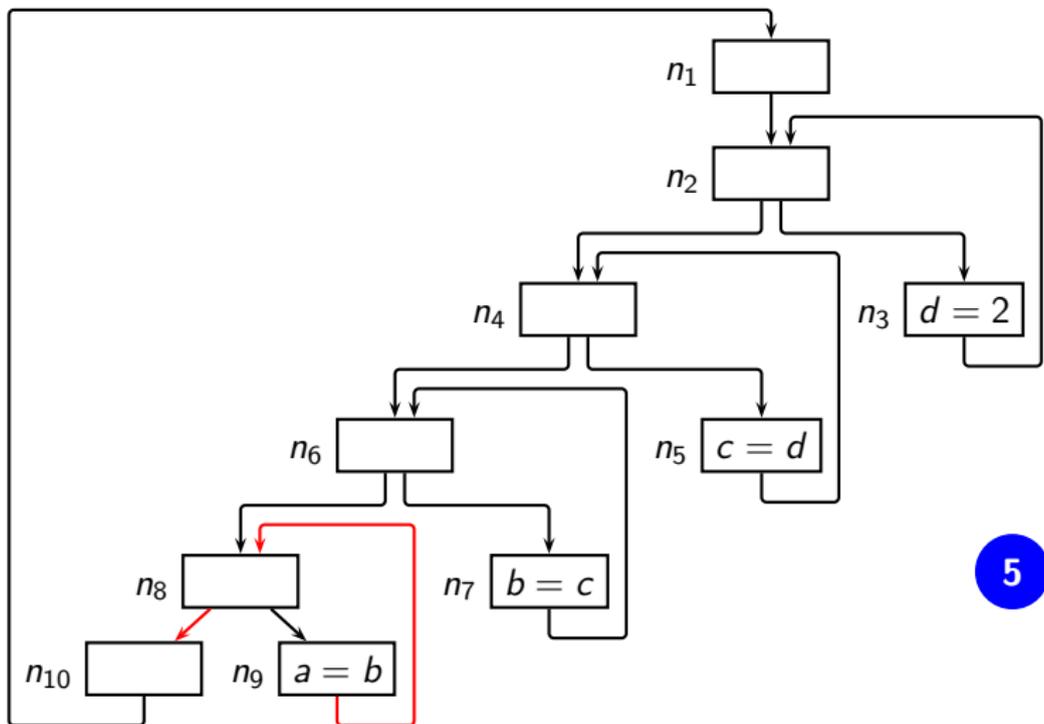


4



Tutorial Problem on Constant Propagation

How many iterations do we need?

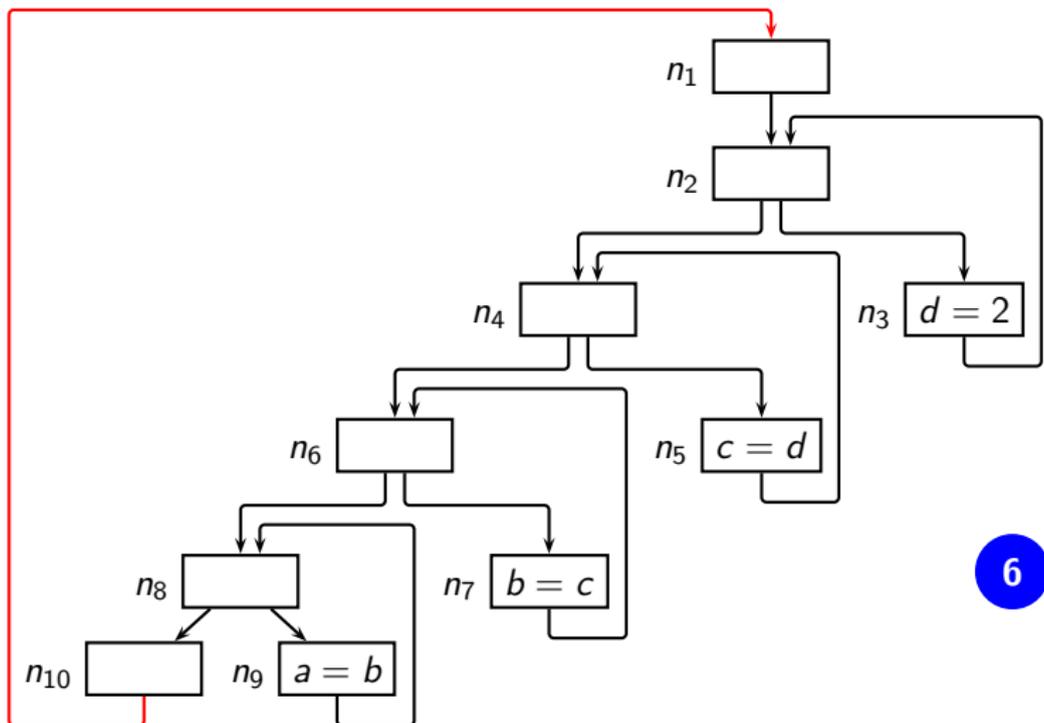


5



Tutorial Problem on Constant Propagation

How many iterations do we need?



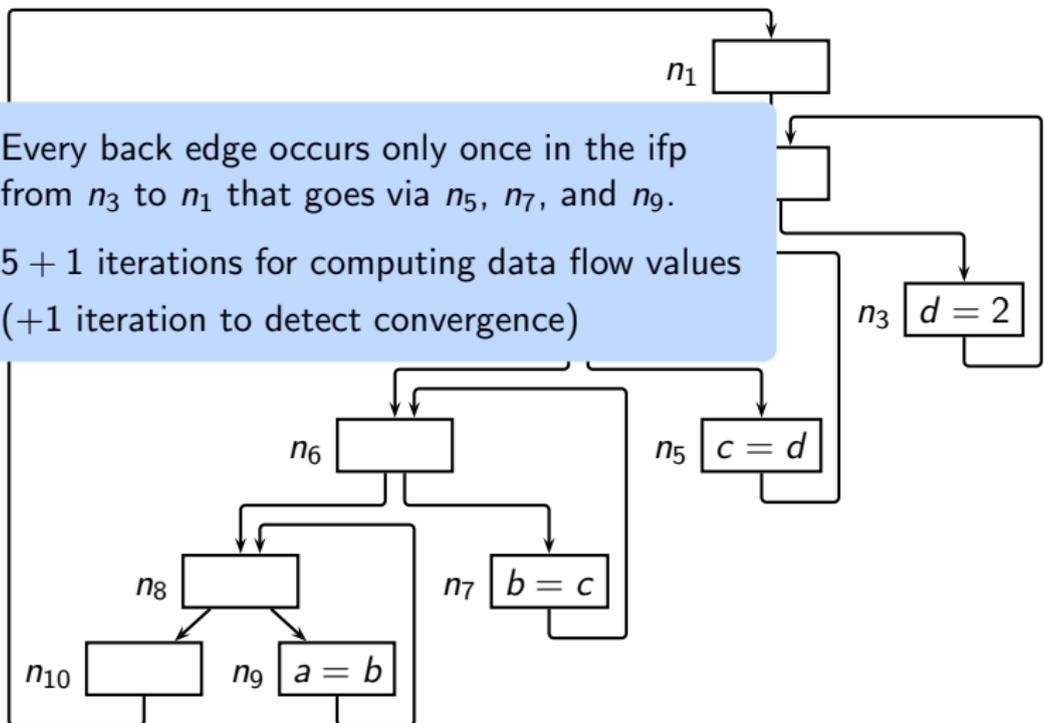
6



Tutorial Problem on Constant Propagation

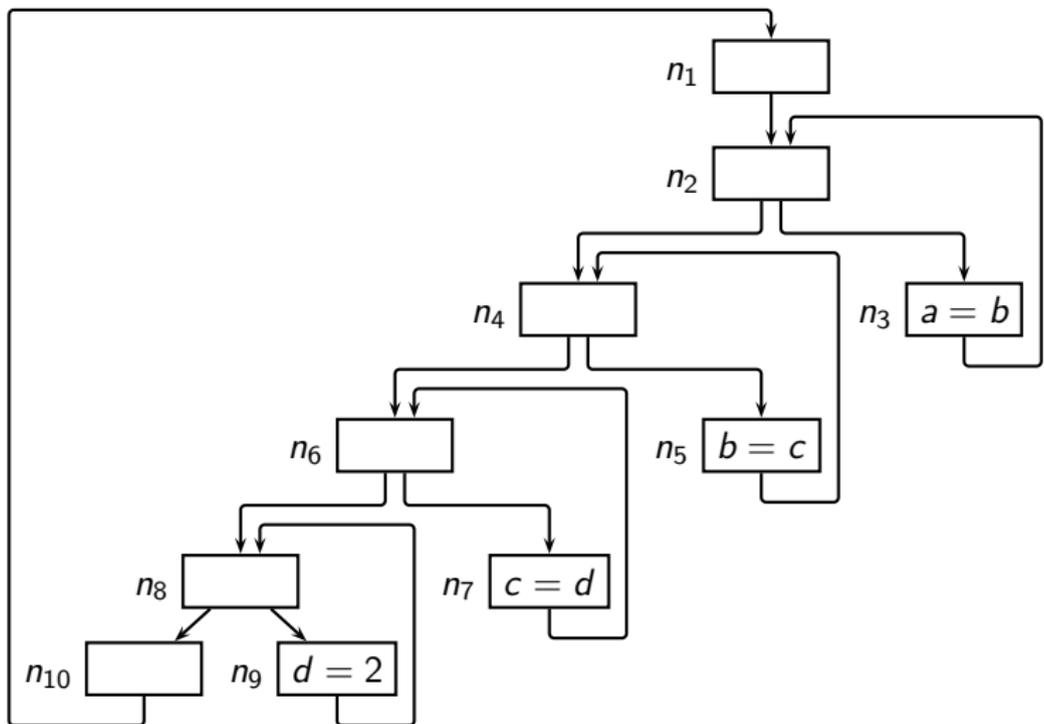
How many iterations do we need?

- Every back edge occurs only once in the ifp from n_3 to n_1 that goes via n_5 , n_7 , and n_9 .
- 5 + 1 iterations for computing data flow values (+1 iteration to detect convergence)



Tutorial Problem on Constant Propagation

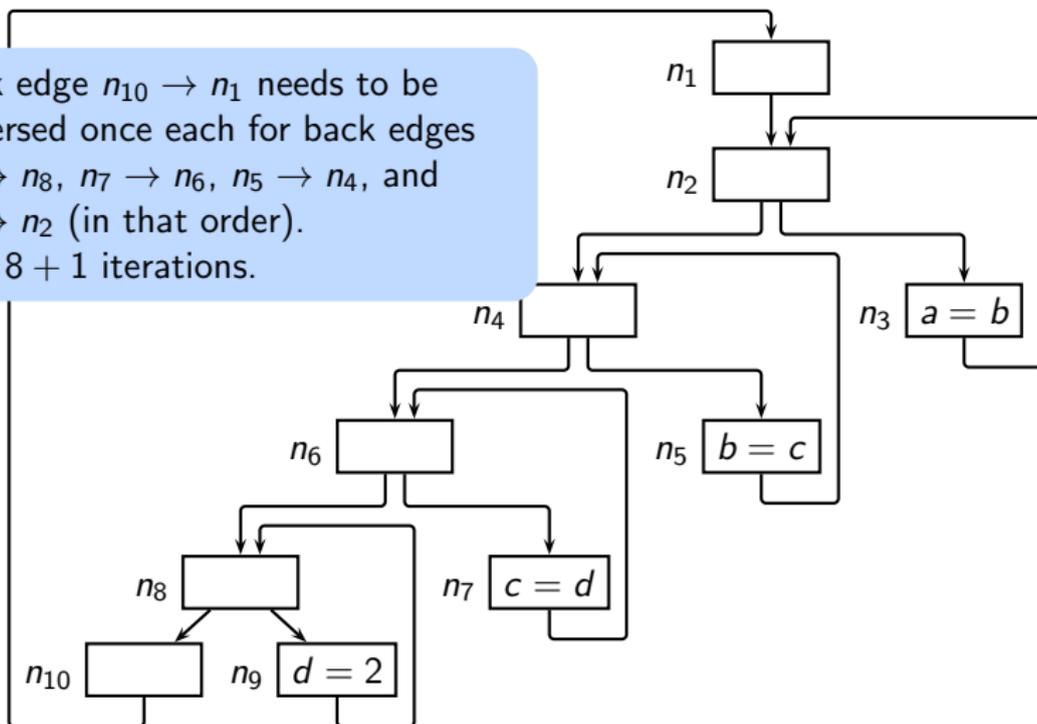
And now how many iterations do we need?



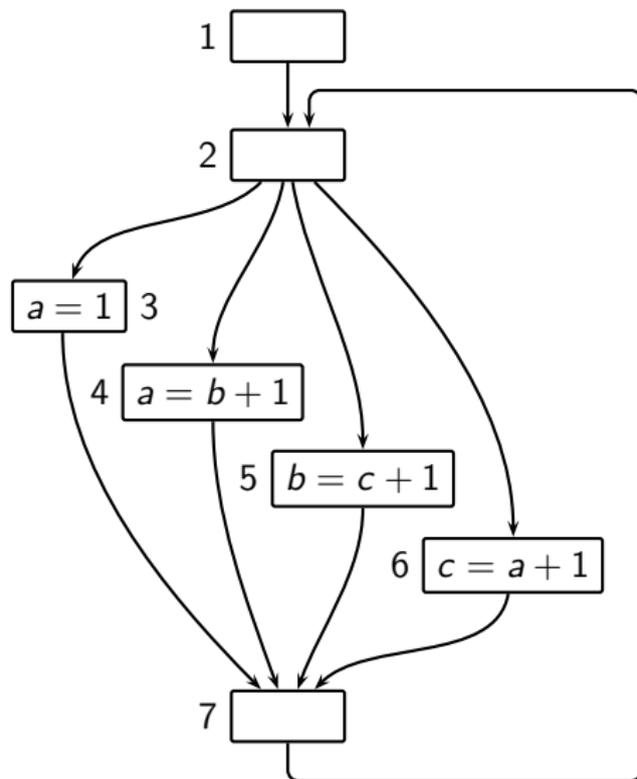
Tutorial Problem on Constant Propagation

And now how many iterations do we need?

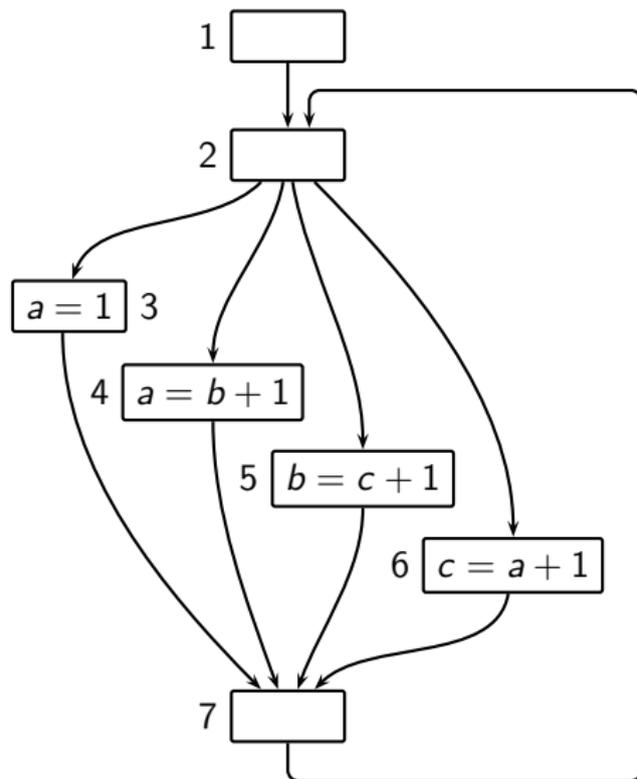
Back edge $n_{10} \rightarrow n_1$ needs to be traversed once each for back edges $n_9 \rightarrow n_8$, $n_7 \rightarrow n_6$, $n_5 \rightarrow n_4$, and $n_3 \rightarrow n_2$ (in that order).
 $\Rightarrow 8 + 1$ iterations.



Boundedness of Constant Propagation



Boundedness of Constant Propagation



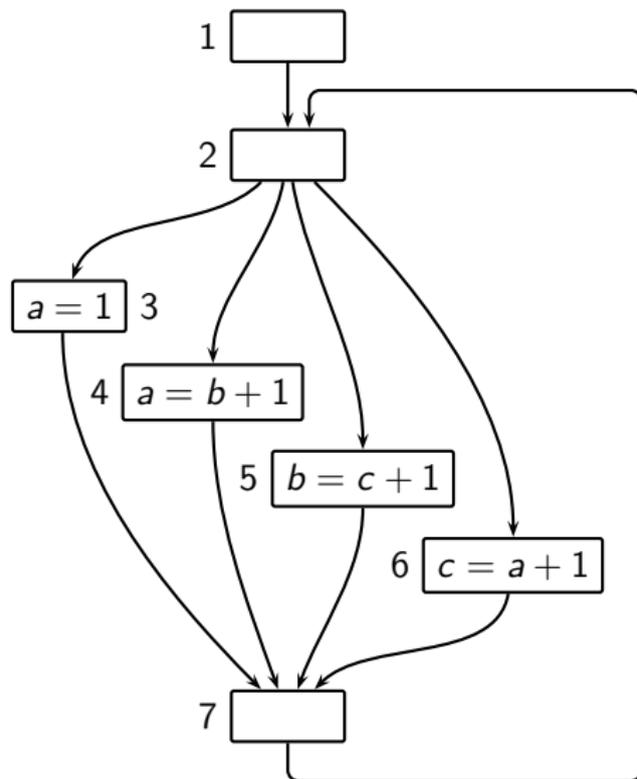
Summary flow function:

(data flow value at node 7)

$$f(\langle v_a, v_b, v_c \rangle) = \langle 1 \sqcap (v_b + 1), \\ (v_c + 1), \\ (v_a + 1) \rangle$$



Boundedness of Constant Propagation



Summary flow function:

(data flow value at node 7)

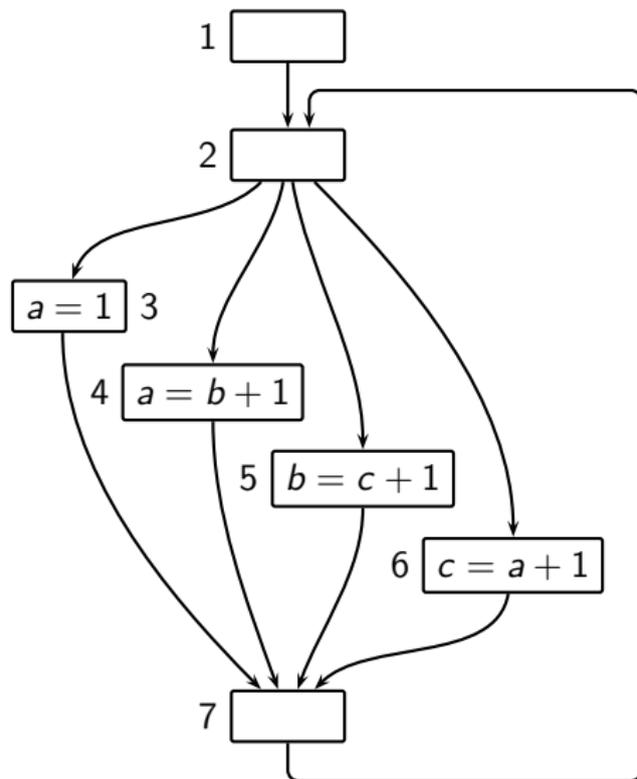
$$f(\langle v_a, v_b, v_c \rangle) = \langle 1 \sqcap (v_b + 1), \\ (v_c + 1), \\ (v_a + 1) \rangle$$

$$f^0(\top) = \langle \hat{\top}, \hat{\top}, \hat{\top} \rangle$$

$$f^1(\top) = \langle 1, \hat{\top}, \hat{\top} \rangle$$



Boundedness of Constant Propagation



Summary flow function:

(data flow value at node 7)

$$f(\langle v_a, v_b, v_c \rangle) = \langle 1 \sqcap (v_b + 1), \\ (v_c + 1), \\ (v_a + 1) \rangle$$

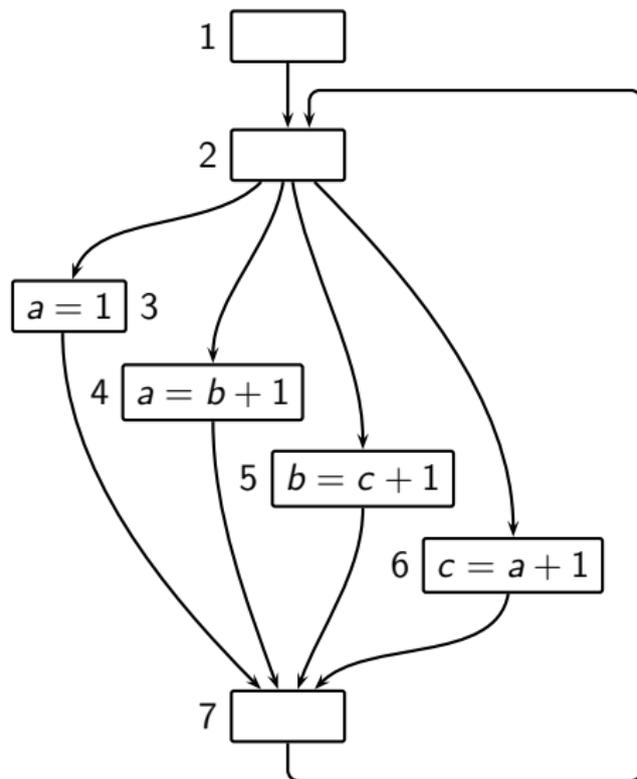
$$f^0(\top) = \langle \hat{\top}, \hat{\top}, \hat{\top} \rangle$$

$$f^1(\top) = \langle 1, \hat{\top}, \hat{\top} \rangle$$

$$f^2(\top) = \langle 1, \hat{\top}, 2 \rangle$$



Boundedness of Constant Propagation



Summary flow function:

(data flow value at node 7)

$$f(\langle v_a, v_b, v_c \rangle) = \langle 1 \sqcap (v_b + 1), \\ (v_c + 1), \\ (v_a + 1) \rangle$$

$$f^0(\top) = \langle \hat{\top}, \hat{\top}, \hat{\top} \rangle$$

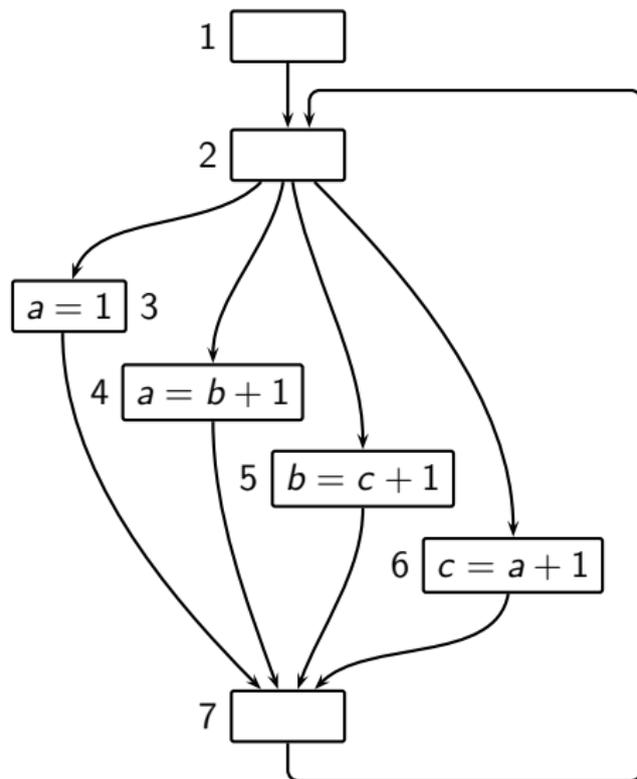
$$f^1(\top) = \langle 1, \hat{\top}, \hat{\top} \rangle$$

$$f^2(\top) = \langle 1, \hat{\top}, 2 \rangle$$

$$f^3(\top) = \langle 1, 3, 2 \rangle$$



Boundedness of Constant Propagation



Summary flow function:

(data flow value at node 7)

$$f(\langle v_a, v_b, v_c \rangle) = \langle 1 \sqcap (v_b + 1), \\ (v_c + 1), \\ (v_a + 1) \rangle$$

$$f^0(\top) = \langle \widehat{\top}, \widehat{\top}, \widehat{\top} \rangle$$

$$f^1(\top) = \langle 1, \widehat{\top}, \widehat{\top} \rangle$$

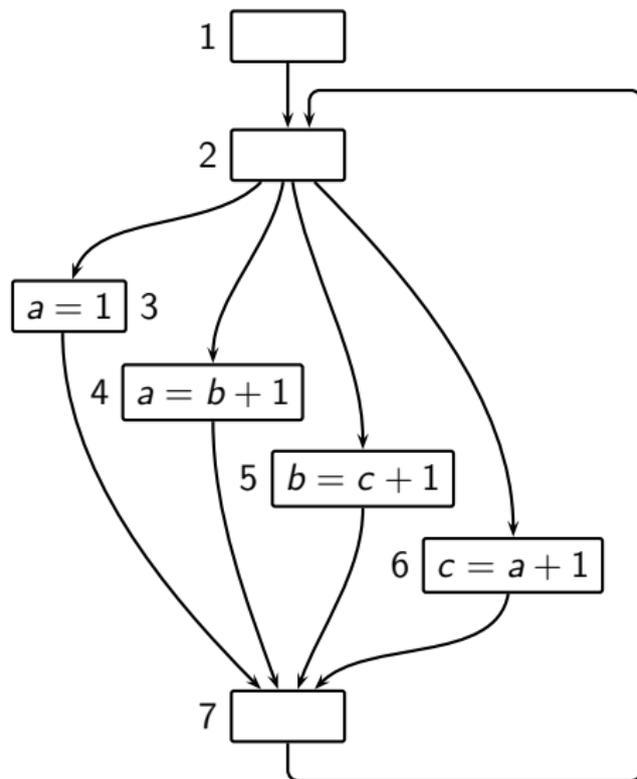
$$f^2(\top) = \langle 1, \widehat{\top}, 2 \rangle$$

$$f^3(\top) = \langle 1, 3, 2 \rangle$$

$$f^4(\top) = \langle \widehat{\perp}, 3, 2 \rangle$$



Boundedness of Constant Propagation



Summary flow function:

(data flow value at node 7)

$$f(\langle v_a, v_b, v_c \rangle) = \langle 1 \sqcap (v_b + 1), \\ (v_c + 1), \\ (v_a + 1) \rangle$$

$$f^0(\top) = \langle \widehat{\top}, \widehat{\top}, \widehat{\top} \rangle$$

$$f^1(\top) = \langle 1, \widehat{\top}, \widehat{\top} \rangle$$

$$f^2(\top) = \langle 1, \widehat{\top}, 2 \rangle$$

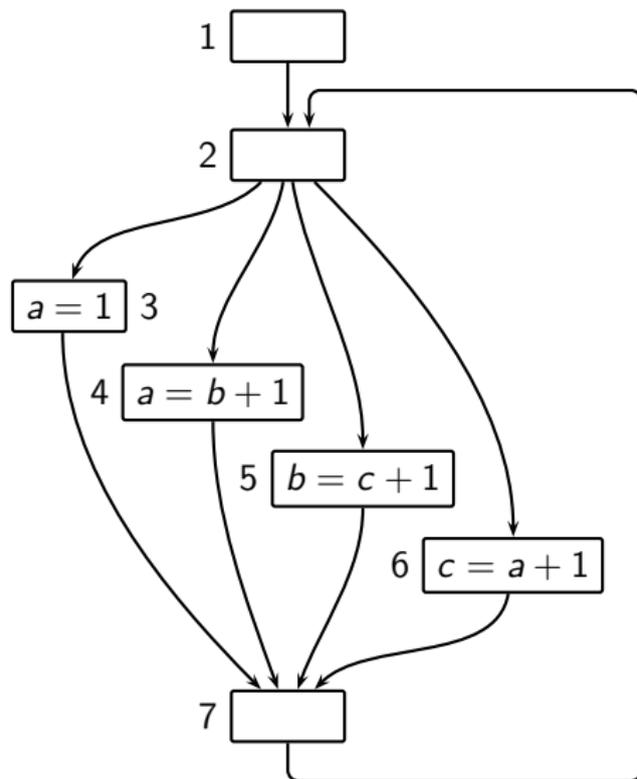
$$f^3(\top) = \langle 1, 3, 2 \rangle$$

$$f^4(\top) = \langle \widehat{\perp}, 3, 2 \rangle$$

$$f^5(\top) = \langle \widehat{\perp}, 3, \widehat{\perp} \rangle$$



Boundedness of Constant Propagation



Summary flow function:

(data flow value at node 7)

$$f(\langle v_a, v_b, v_c \rangle) = \langle 1 \sqcap (v_b + 1), \\ (v_c + 1), \\ (v_a + 1) \rangle$$

$$f^0(\top) = \langle \widehat{\top}, \widehat{\top}, \widehat{\top} \rangle$$

$$f^1(\top) = \langle 1, \widehat{\top}, \widehat{\top} \rangle$$

$$f^2(\top) = \langle 1, \widehat{\top}, 2 \rangle$$

$$f^3(\top) = \langle 1, 3, 2 \rangle$$

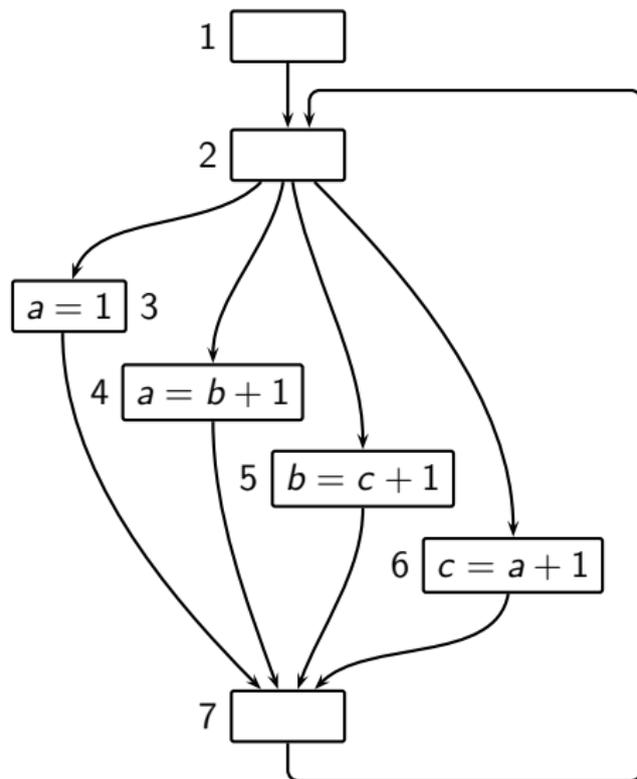
$$f^4(\top) = \langle \widehat{\perp}, 3, 2 \rangle$$

$$f^5(\top) = \langle \widehat{\perp}, 3, \widehat{\perp} \rangle$$

$$f^6(\top) = \langle \widehat{\perp}, \widehat{\perp}, \widehat{\perp} \rangle$$



Boundedness of Constant Propagation



Summary flow function:

(data flow value at node 7)

$$f(\langle v_a, v_b, v_c \rangle) = \langle 1 \sqcap (v_b + 1), \\ (v_c + 1), \\ (v_a + 1) \rangle$$

$$f^0(\top) = \langle \widehat{\top}, \widehat{\top}, \widehat{\top} \rangle$$

$$f^1(\top) = \langle 1, \widehat{\top}, \widehat{\top} \rangle$$

$$f^2(\top) = \langle 1, \widehat{\top}, 2 \rangle$$

$$f^3(\top) = \langle 1, 3, 2 \rangle$$

$$f^4(\top) = \langle \widehat{\perp}, 3, 2 \rangle$$

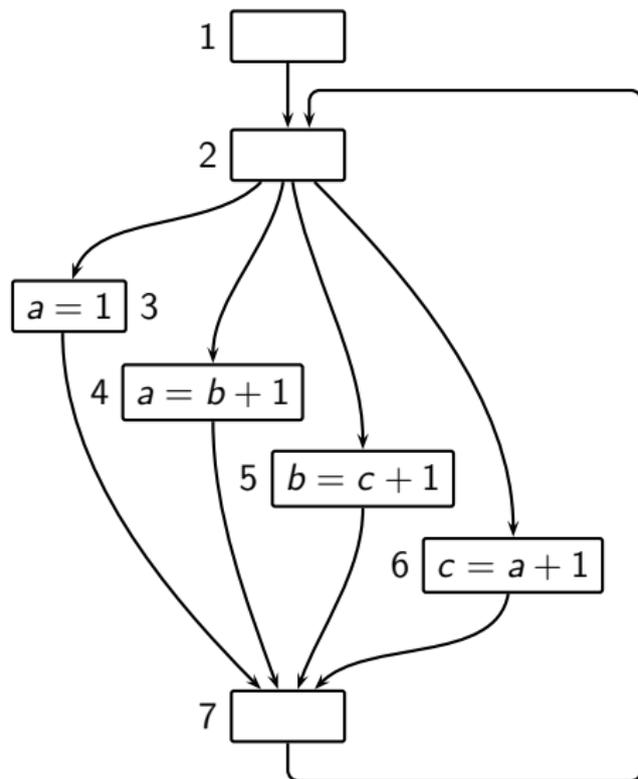
$$f^5(\top) = \langle \widehat{\perp}, 3, \widehat{\perp} \rangle$$

$$f^6(\top) = \langle \widehat{\perp}, \widehat{\perp}, \widehat{\perp} \rangle$$

$$f^7(\top) = \langle \widehat{\perp}, \widehat{\perp}, \widehat{\perp} \rangle$$



Boundedness of Constant Propagation



$$f^*(\top) = \prod_{i=0}^6 f^i(\top)$$



Boundedness of Constant Propagation

The moral of the story:

- The data flow value of every variable could change twice



Boundedness of Constant Propagation

The moral of the story:

- The data flow value of every variable could change twice
- In the worst case, only one change may happen in every step of a function application



Boundedness of Constant Propagation

The moral of the story:

- The data flow value of every variable could change twice
- In the worst case, only one change may happen in every step of a function application
- Maximum number of steps: $2 \times |\text{Var}|$



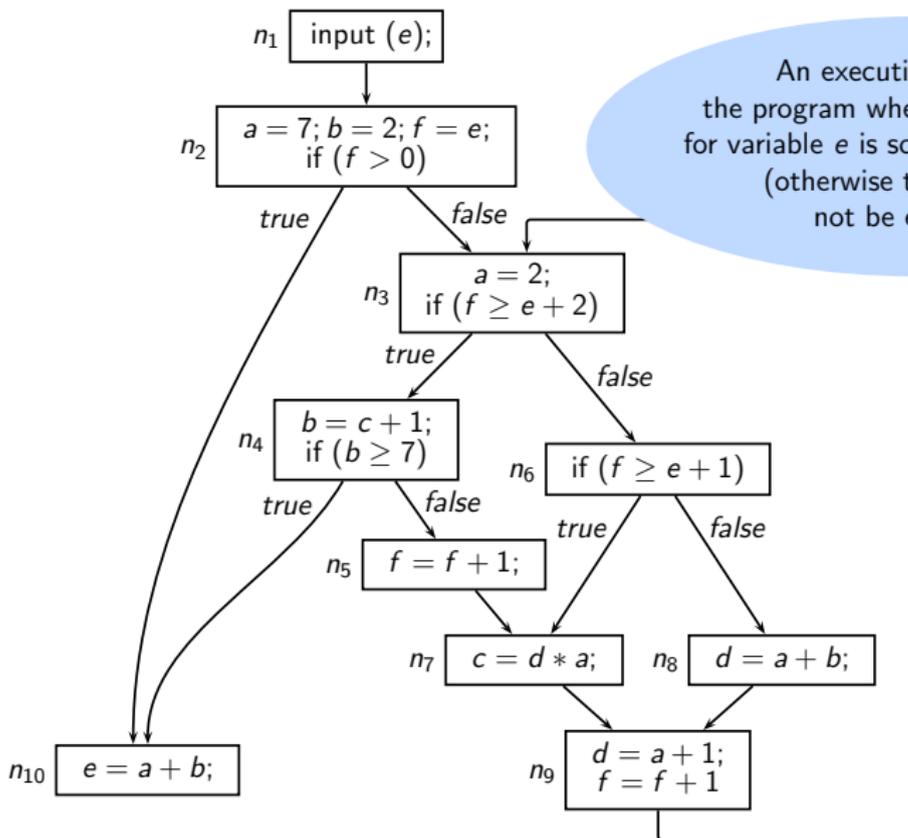
Boundedness of Constant Propagation

The moral of the story:

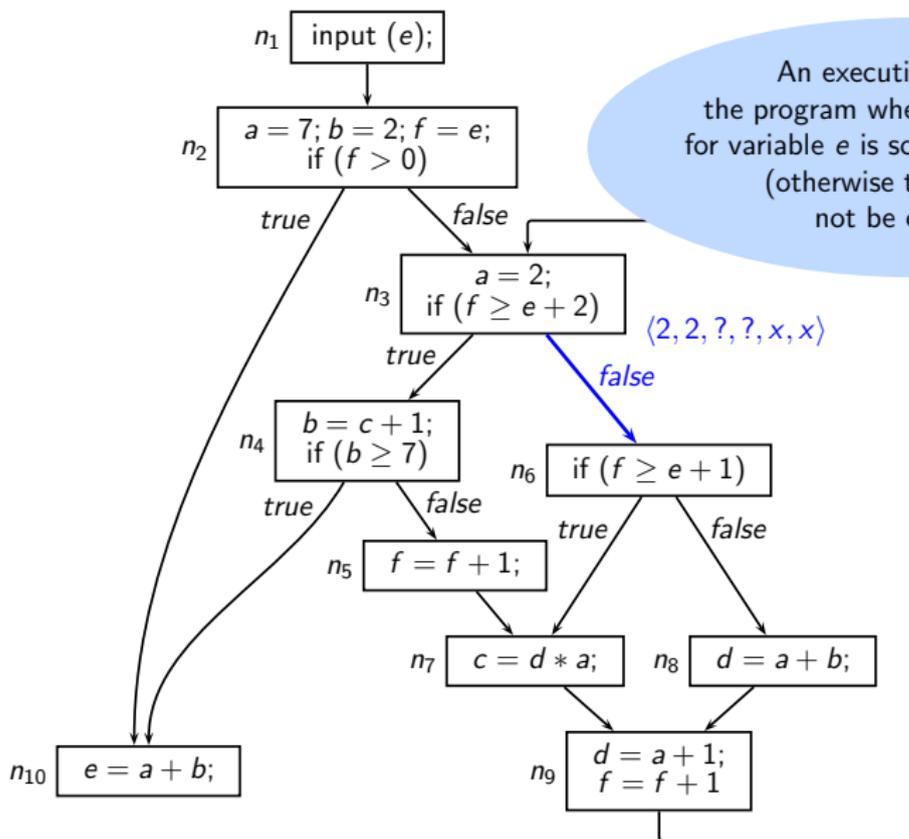
- The data flow value of every variable could change twice
- In the worst case, only one change may happen in every step of a function application
- Maximum number of steps: $2 \times |\text{Var}|$
- Boundedness parameter k is $(2 \times |\text{Var}|) + 1$



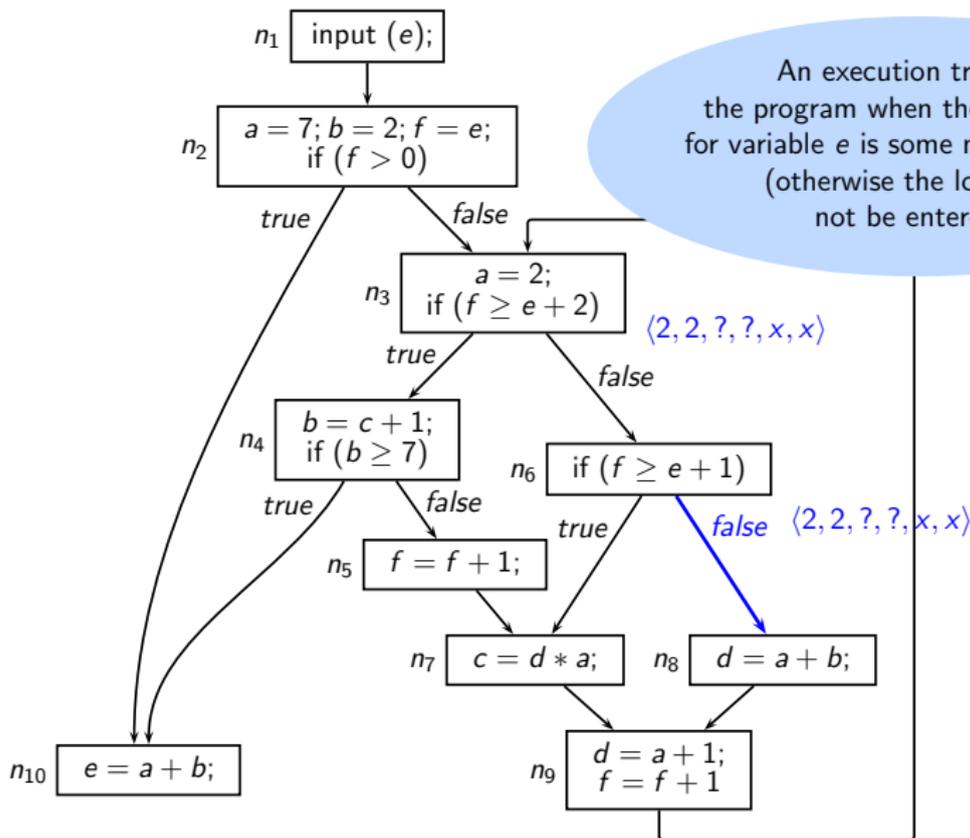
Conditional Constant Propagation



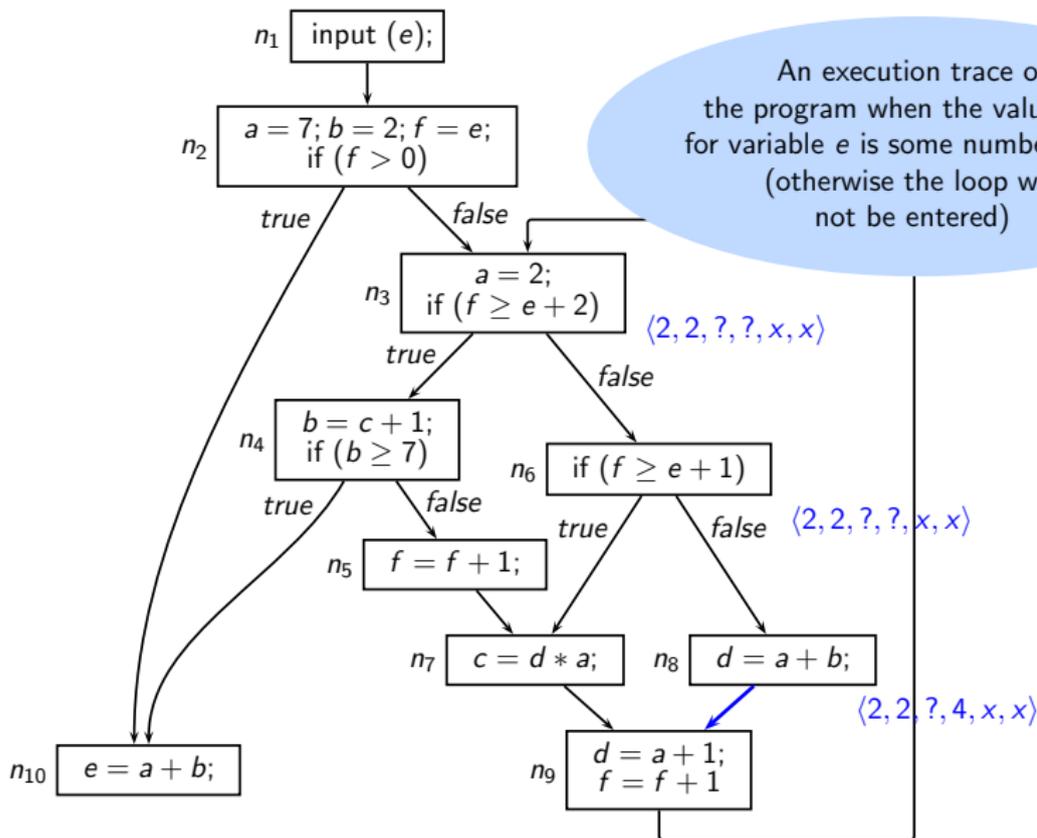
Conditional Constant Propagation



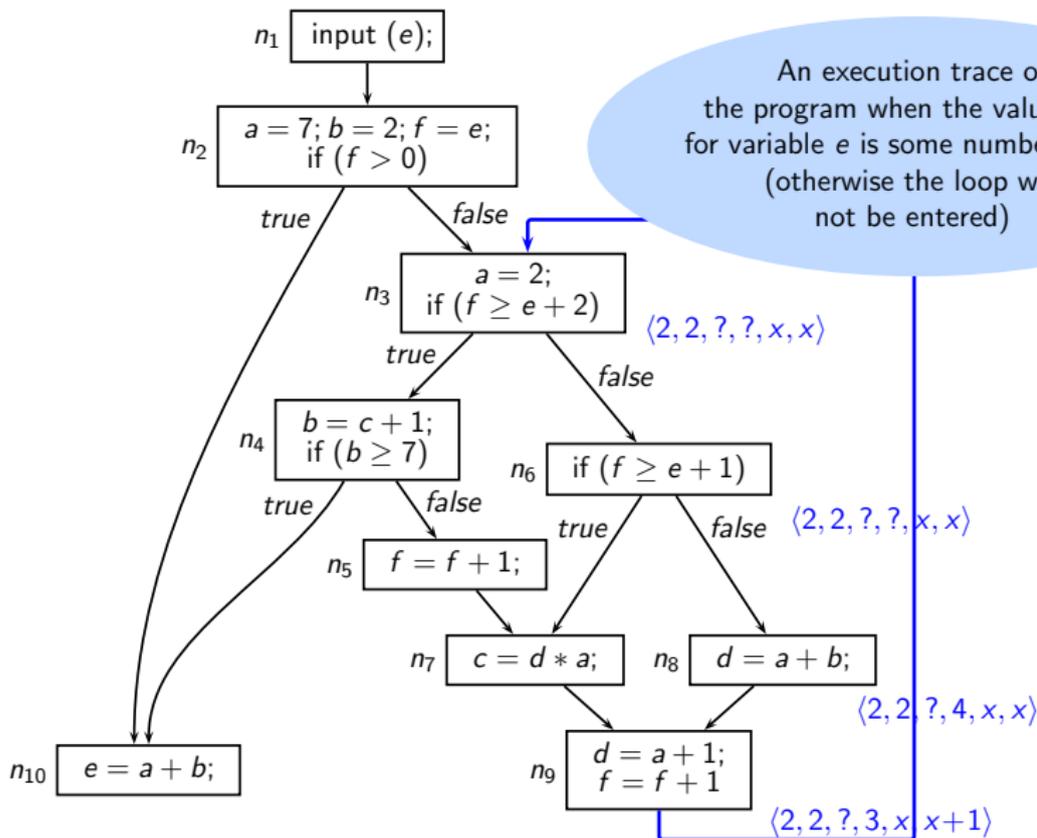
Conditional Constant Propagation



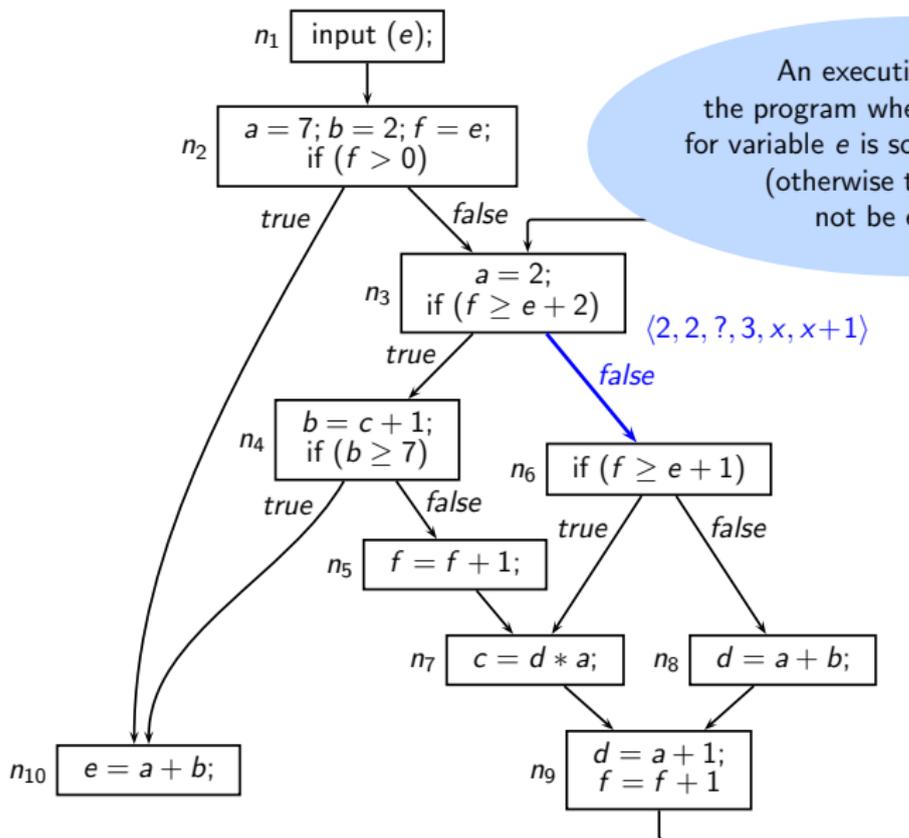
Conditional Constant Propagation



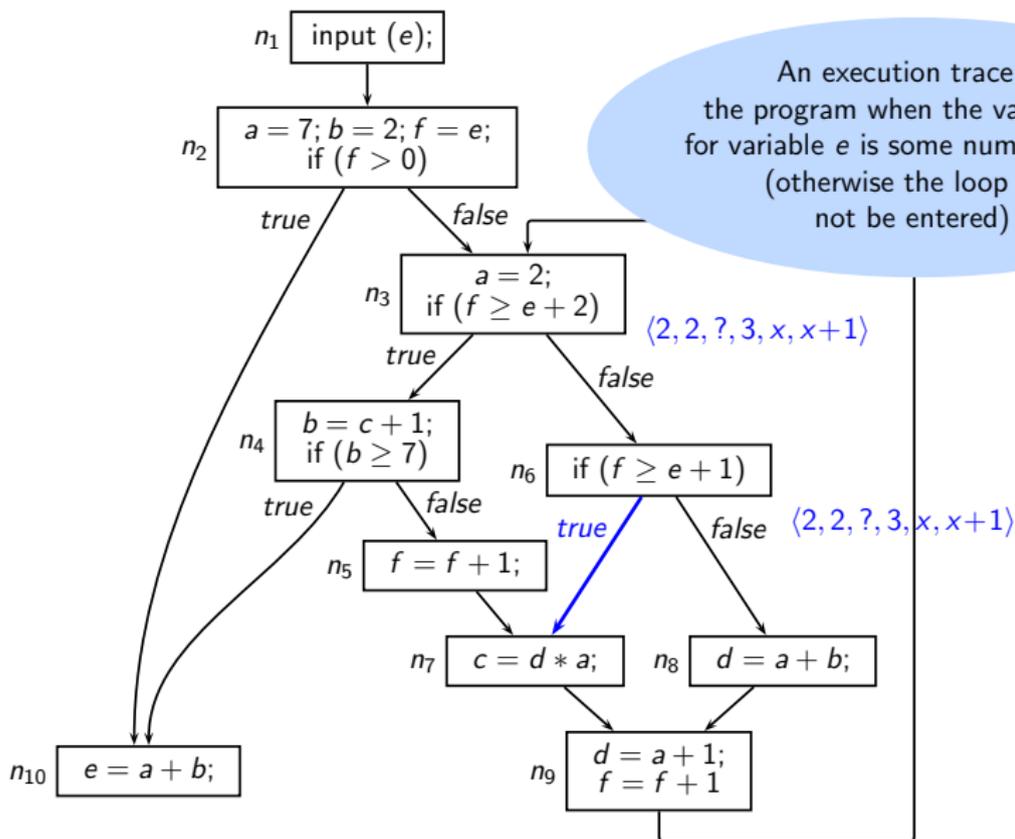
Conditional Constant Propagation



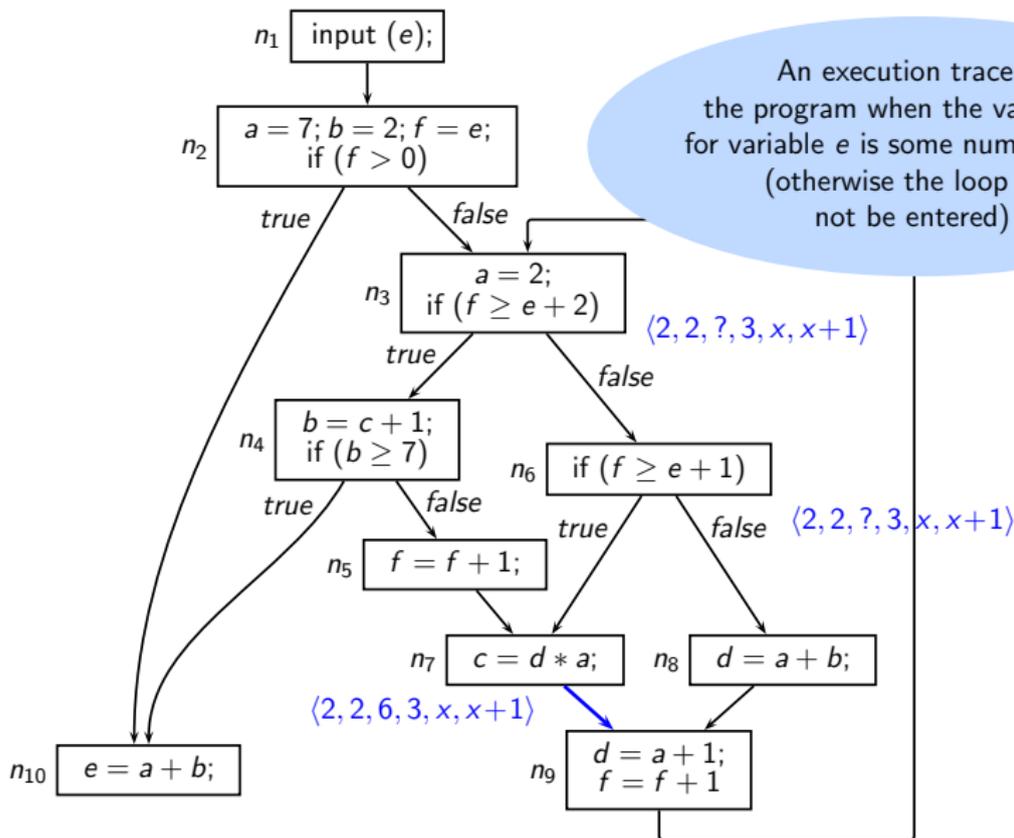
Conditional Constant Propagation



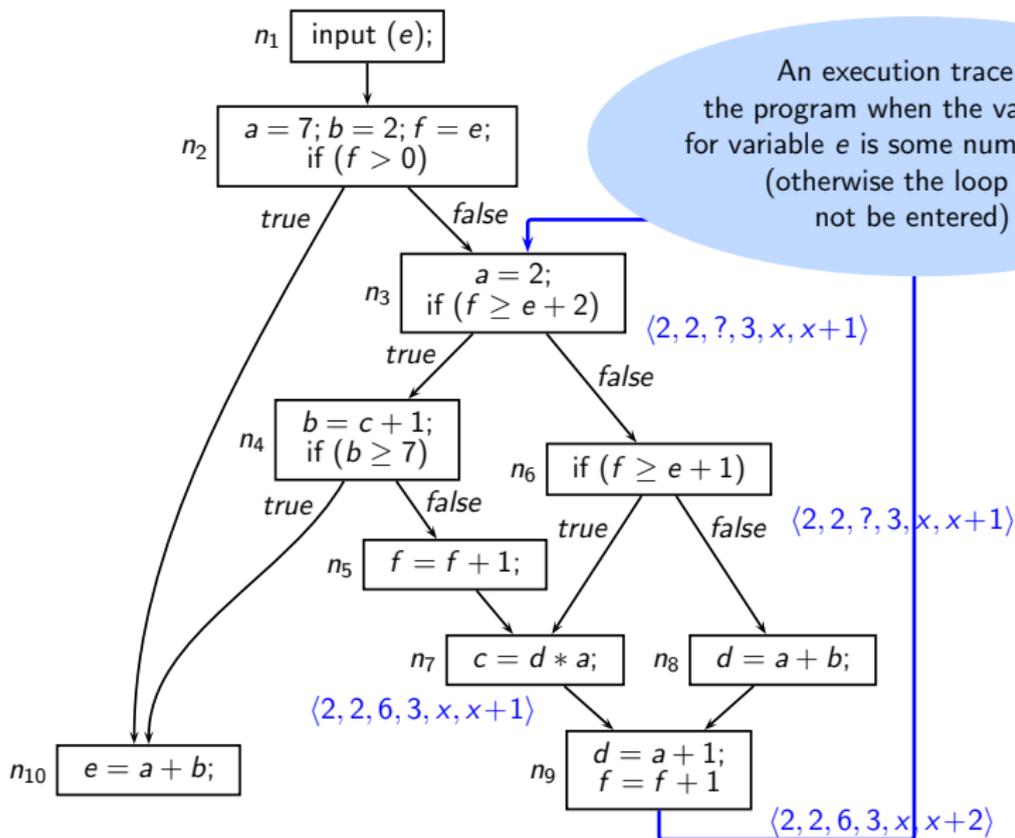
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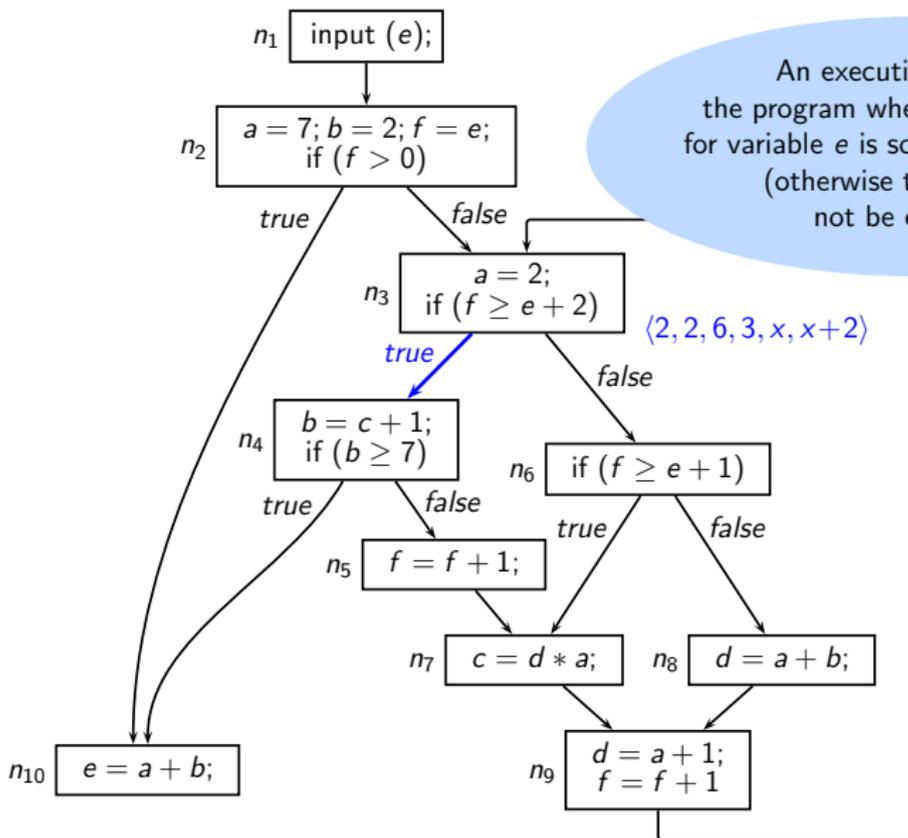
Conditional Constant Propagation



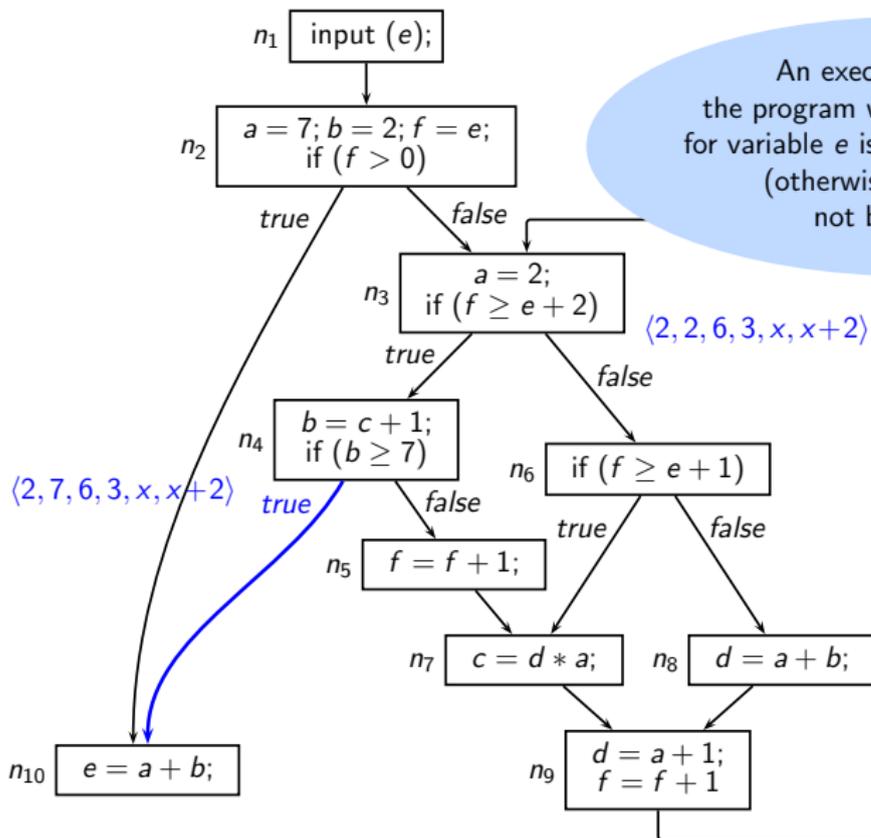
Conditional Constant Propagation



Conditional Constant Propagation



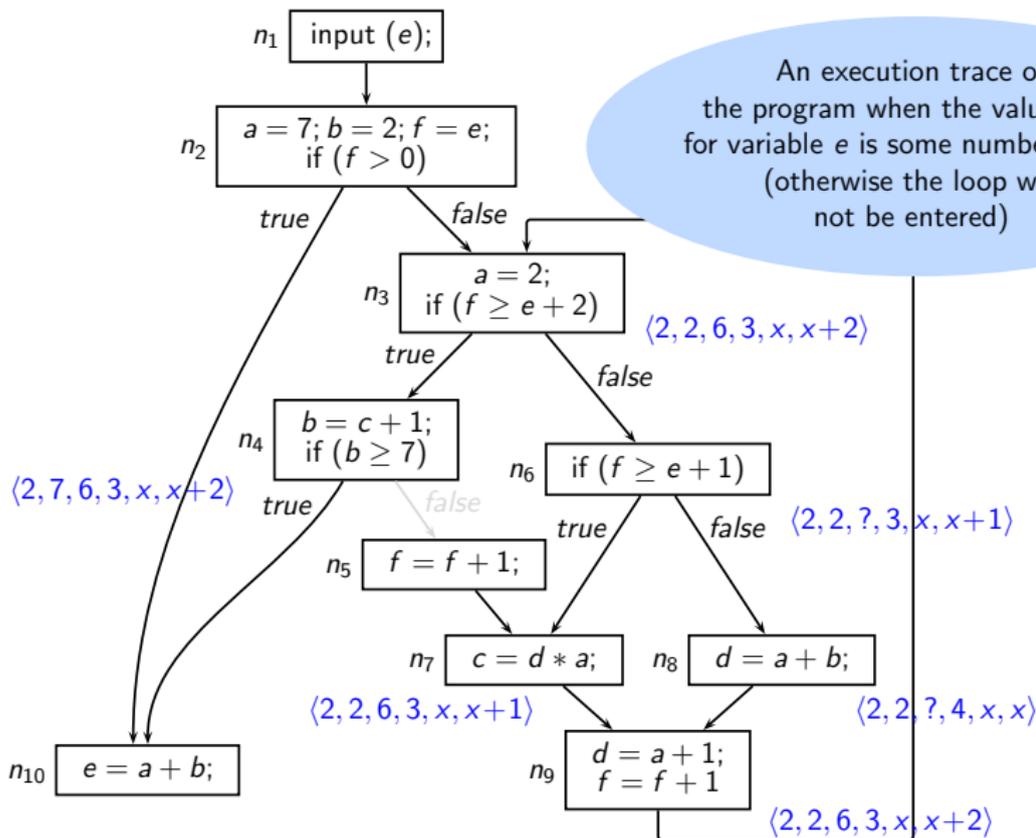
Conditional Constant Propagation



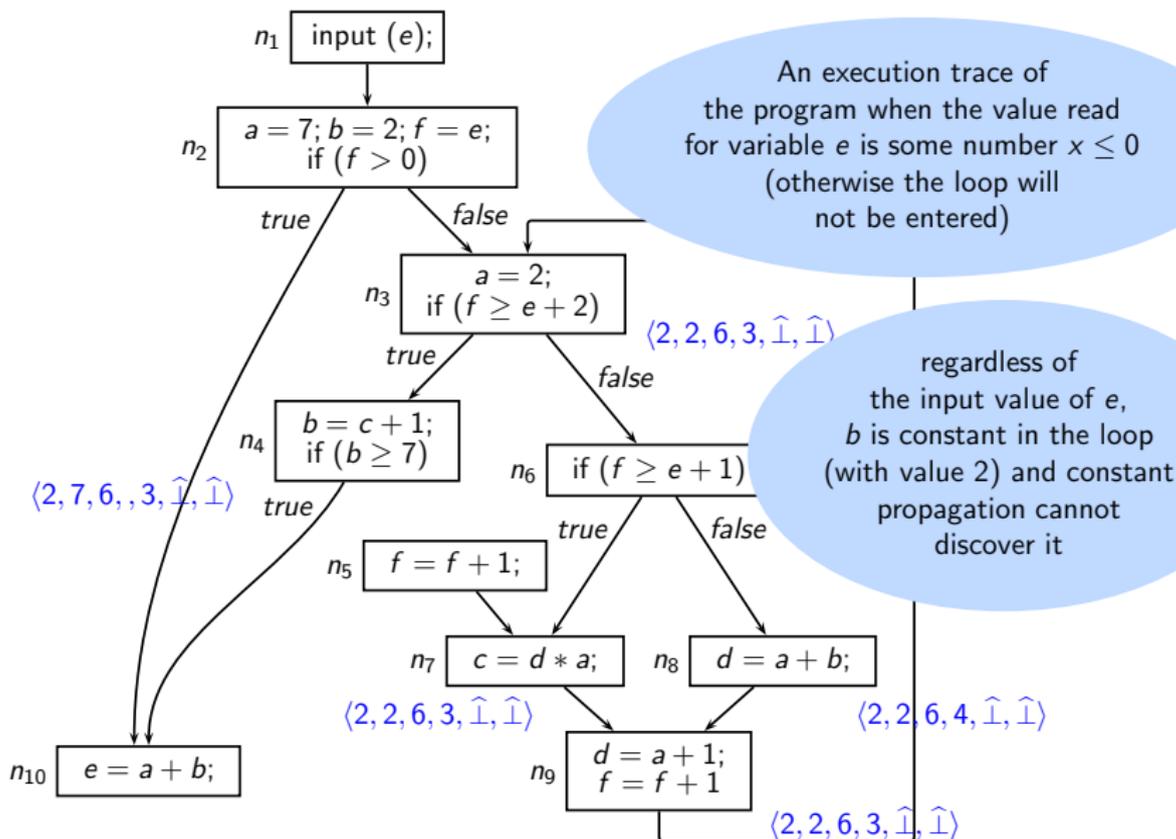
An execution trace of the program when the value read for variable e is some number $x \leq 0$ (otherwise the loop will not be entered)



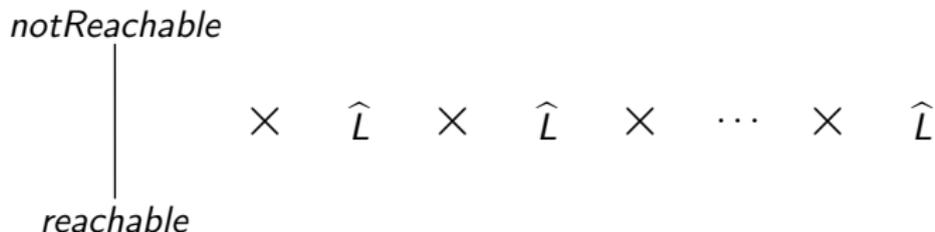
Conditional Constant Propagation



Conditional Constant Propagation



Lattice for Conditional Constant Propagation



- Let $\langle s, X \rangle$ denote an augmented data flow value where $s \in \{reachable, notReachable\}$ and $X \in L$.
- If we can maintain the invariant $s = notReachable \Rightarrow X = \top$, then the meet can be defined as

$$\langle s_1, X_1 \rangle \sqcap \langle s_2, X_2 \rangle = \langle s_1 \sqcap s_2, X_1 \sqcap X_2 \rangle$$



Data Flow Equations for Conditional Constant Propagation

$$In_n = \begin{cases} \langle \text{reachable}, BI \rangle & n \text{ is Start} \\ \prod_{p \in \text{pred}(n)} g_{p \rightarrow n}(Out_p) & \text{otherwise} \end{cases}$$

$$Out_n = \begin{cases} \langle \text{reachable}, f_n(X) \rangle & In_n = \langle \text{reachable}, X \rangle \\ \langle \text{notReachable}, \top \rangle & \text{otherwise} \end{cases}$$

$$g_{m \rightarrow n}(s, X) = \begin{cases} \langle s, X \rangle & \text{label}(m \rightarrow n) \in \text{evalCond}(m, X) \\ \langle \text{notReachable}, \top \rangle & \text{otherwise} \end{cases}$$

- $\text{label}(m \rightarrow n)$ is T or F if edge $m \rightarrow n$ is a conditional branch
Otherwise $\text{label}(m \rightarrow n)$ is T
- $\text{evalCond}(m, X)$ evaluates the condition in block m using the data flow values in X



Compile Time Evaluation of Conditions using the Data Flow Values

$evalCond(m, X)$	
$\{T, F\}$	Block m does not have a condition, or some variable in the condition is $\hat{\perp}$ in X
$\{\}$	No variable in the condition in block m is $\hat{\perp}$ in X , but some variable is $\hat{\top}$ in X
$\{T\}$	The condition in block m evaluates to T with the data flow values in X
$\{F\}$	The condition in block m evaluates to F with the data flow values in X



Conditional Constant Propagation

	Iteration #1	Changes in iteration #2	Changes in iteration #3
In_{n_1}	$R, \langle \hat{\top}, \hat{\top}, \hat{\top}, \hat{\top}, \hat{\top}, \hat{\top} \rangle$		
Out_{n_1}	$R, \langle \hat{\top}, \hat{\top}, \hat{\top}, \hat{\top}, \perp, \hat{\top} \rangle$		
In_{n_2}	$R, \langle \hat{\top}, \hat{\top}, \hat{\top}, \hat{\top}, \perp, \hat{\top} \rangle$		
Out_{n_2}	$R, \langle 7, 2, \hat{\top}, \hat{\top}, \perp, \hat{\top} \rangle$		
In_{n_3}	$R, \langle 7, 2, \hat{\top}, \hat{\top}, \perp, \hat{\top} \rangle$	$R, \langle \hat{\perp}, 2, \hat{\top}, 3, \hat{\perp}, \hat{\perp} \rangle$	$R, \langle \hat{\perp}, 2, 6, 3, \hat{\perp}, \hat{\perp} \rangle$
Out_{n_3}	$R, \langle 2, 2, \hat{\top}, \hat{\top}, \perp, \hat{\perp} \rangle$	$R, \langle 2, 2, \hat{\top}, 3, \hat{\perp}, \hat{\perp} \rangle$	$R, \langle 2, 2, 6, 3, \hat{\perp}, \hat{\perp} \rangle$
In_{n_4}	$R, \langle 2, 2, \hat{\top}, \hat{\top}, \perp, \hat{\perp} \rangle$	$R, \langle 2, 2, \hat{\top}, 3, \hat{\perp}, \hat{\perp} \rangle$	$R, \langle 2, 2, 6, 3, \hat{\perp}, \hat{\perp} \rangle$
Out_{n_4}	$R, \langle 2, \hat{\top}, \hat{\top}, \hat{\top}, \perp, \hat{\perp} \rangle$	$R, \langle 2, \hat{\top}, \hat{\top}, 3, \hat{\perp}, \hat{\perp} \rangle$	$R, \langle 2, 7, 6, 3, \hat{\perp}, \hat{\perp} \rangle$
In_{n_5}	$N, \top = \langle \hat{\top}, \hat{\top}, \hat{\top}, \hat{\top}, \hat{\top}, \hat{\top} \rangle$		
Out_{n_5}	$N, \top = \langle \hat{\top}, \hat{\top}, \hat{\top}, \hat{\top}, \hat{\top}, \hat{\top} \rangle$		
In_{n_6}	$R, \langle 2, 2, \hat{\top}, \hat{\top}, \perp, \hat{\perp} \rangle$	$R, \langle 2, 2, \hat{\top}, 3, \hat{\perp}, \hat{\perp} \rangle$	$R, \langle 2, 2, 6, 3, \hat{\perp}, \hat{\perp} \rangle$
Out_{n_6}	$R, \langle 2, 2, \hat{\top}, \hat{\top}, \perp, \hat{\perp} \rangle$	$R, \langle 2, 2, \hat{\top}, 3, \hat{\perp}, \hat{\perp} \rangle$	$R, \langle 2, 2, 6, 3, \hat{\perp}, \hat{\perp} \rangle$
In_{n_7}	$R, \langle 2, 2, \hat{\top}, \hat{\top}, \perp, \hat{\perp} \rangle$	$R, \langle 2, 2, \hat{\top}, 3, \hat{\perp}, \hat{\perp} \rangle$	$R, \langle 2, 2, 6, 3, \hat{\perp}, \hat{\perp} \rangle$
Out_{n_7}	$R, \langle 2, 2, \hat{\top}, \hat{\top}, \perp, \hat{\perp} \rangle$	$R, \langle 2, 2, 6, 3, \hat{\perp}, \hat{\perp} \rangle$	
In_{n_8}	$R, \langle 2, 2, \hat{\top}, \hat{\top}, \perp, \hat{\perp} \rangle$	$R, \langle 2, 2, \hat{\top}, 3, \hat{\perp}, \hat{\perp} \rangle$	$R, \langle 2, 2, 6, 3, \hat{\perp}, \hat{\perp} \rangle$
Out_{n_8}	$R, \langle 2, 2, \hat{\top}, 4, \hat{\perp}, \hat{\perp} \rangle$		$R, \langle 2, 2, 6, 4, \hat{\perp}, \hat{\perp} \rangle$
In_{n_9}	$R, \langle 2, 2, \hat{\top}, 4, \hat{\perp}, \hat{\perp} \rangle$	$R, \langle 2, 2, 6, \hat{\perp}, \hat{\perp}, \hat{\perp} \rangle$	
Out_{n_9}	$R, \langle 2, 2, \hat{\top}, 3, \hat{\perp}, \hat{\perp} \rangle$	$R, \langle 2, 2, 6, 3, \hat{\perp}, \hat{\perp} \rangle$	
$In_{n_{10}}$	$R, \langle 7, 2, \hat{\top}, \hat{\top}, \perp, \hat{\perp} \rangle$	$R, \langle \hat{\perp}, 2, \hat{\top}, 3, \hat{\perp}, \hat{\perp} \rangle$	$R, \langle \hat{\perp}, \hat{\perp}, 6, 3, \hat{\perp}, \hat{\perp} \rangle$
$Out_{n_{10}}$	$R, \langle 7, 2, \hat{\top}, \hat{\top}, 9, \hat{\perp} \rangle$	$R, \langle \hat{\perp}, 2, \hat{\top}, 3, \hat{\perp}, \hat{\perp} \rangle$	$R, \langle \hat{\perp}, \hat{\perp}, 6, 3, \hat{\perp}, \hat{\perp} \rangle$



Part 4

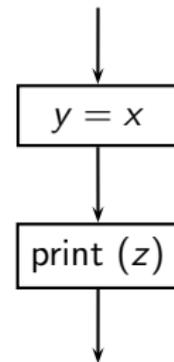
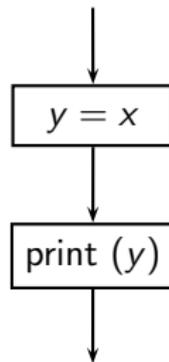
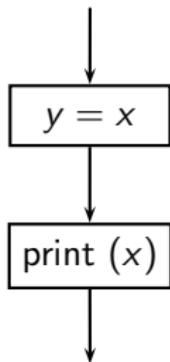
Strongly Live Variables Analysis

Strongly Live Variables Analysis

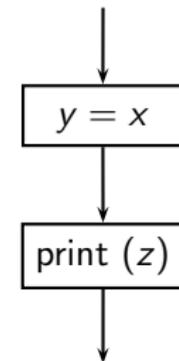
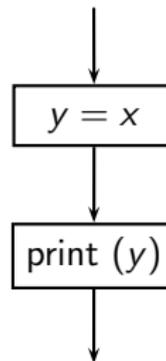
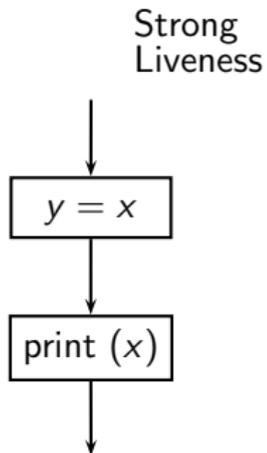
- A variable is strongly live if
 - ▶ it is used in a statement other than assignment statement, or (same as simple liveness)
 - ▶ it is used in an assignment statement defining a variable that is strongly live (different from simple liveness)
- Killing: An assignment statement, an input statement, or BI (this is same as killing in simple liveness)
- Generation: A direct use or a use for defining values that are strongly live (this is different from generation in simple liveness)



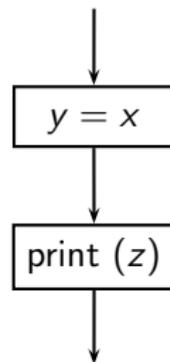
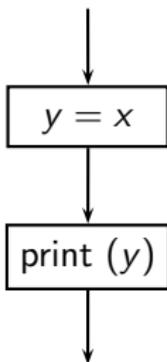
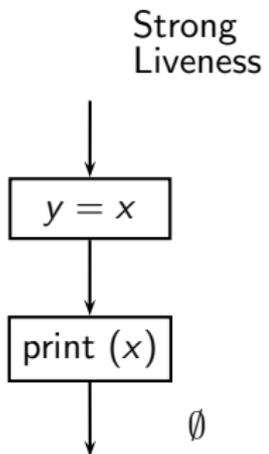
Understanding Strong Liveness



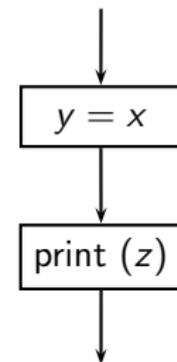
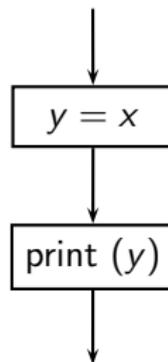
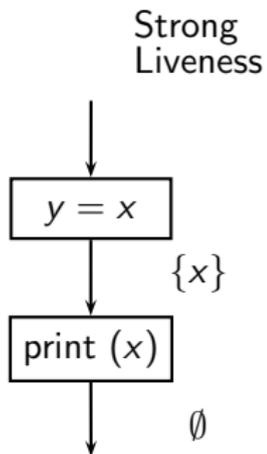
Understanding Strong Liveness



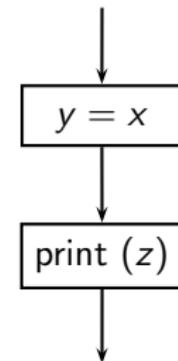
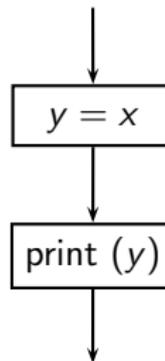
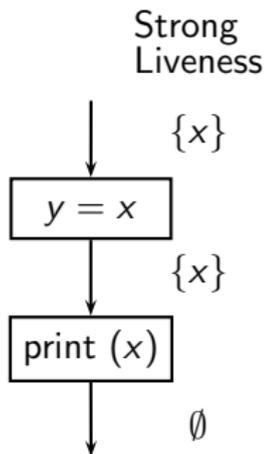
Understanding Strong Liveness



Understanding Strong Liveness



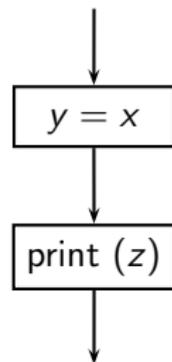
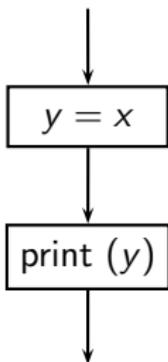
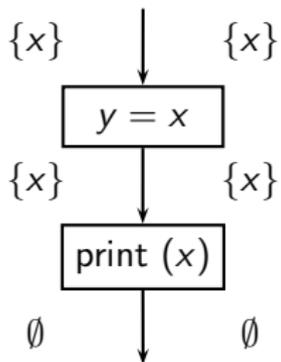
Understanding Strong Liveness



Understanding Strong Liveness

Simple
Liveness

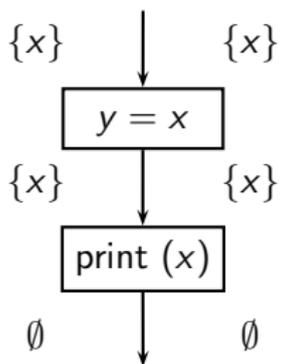
Strong
Liveness



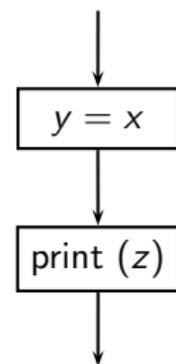
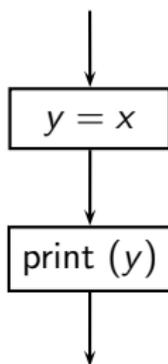
Understanding Strong Liveness

Simple
Liveness

Strong
Liveness



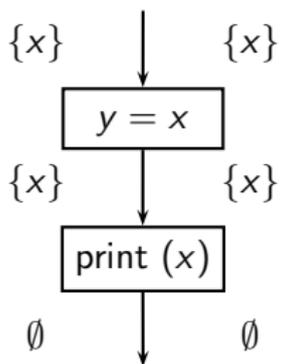
Same



Understanding Strong Liveness

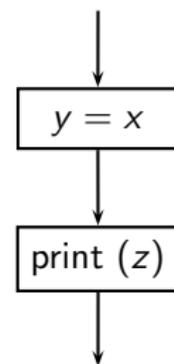
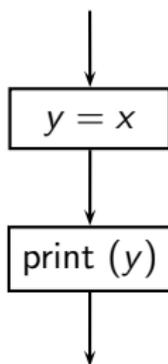
Simple
Liveness

Strong
Liveness



Same

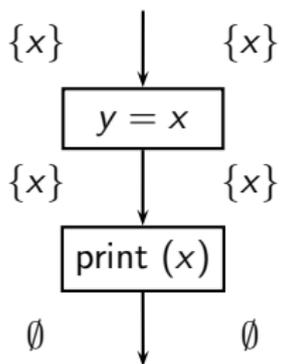
Strong
Liveness



Understanding Strong Liveness

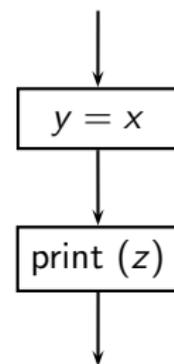
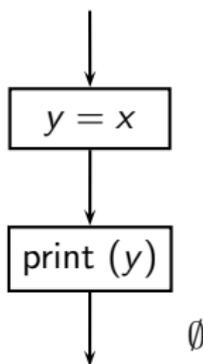
Simple
Liveness

Strong
Liveness



Same

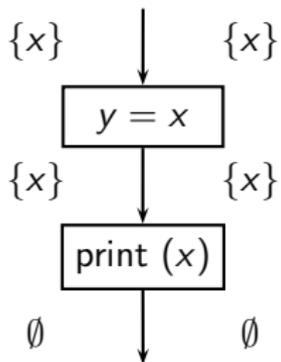
Strong
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Understanding Strong Liveness

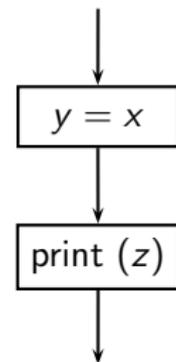
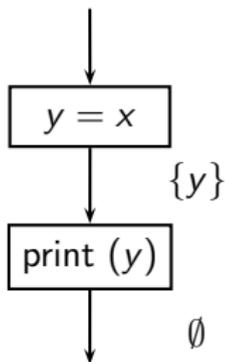
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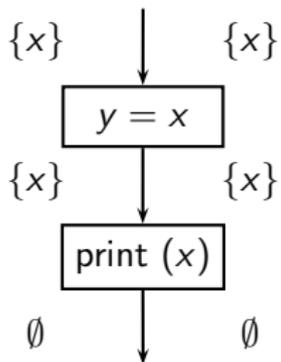


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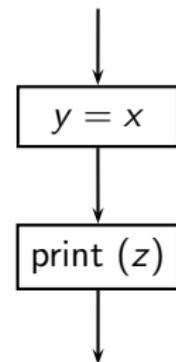
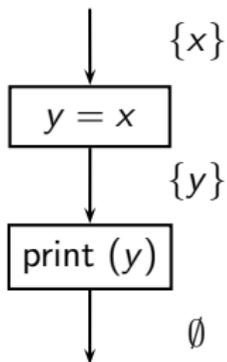
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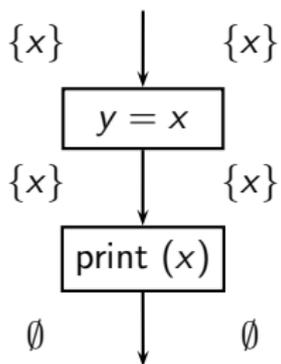
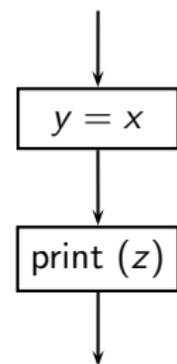
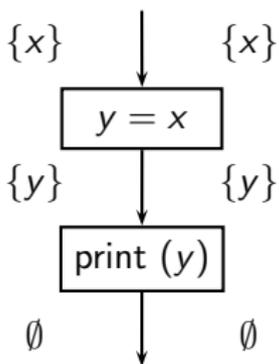
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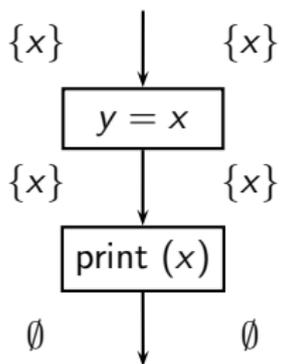
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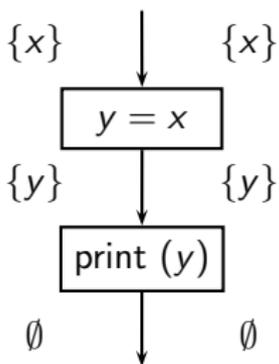
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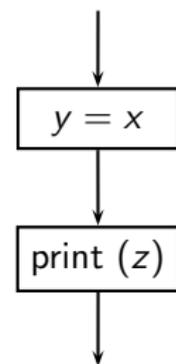
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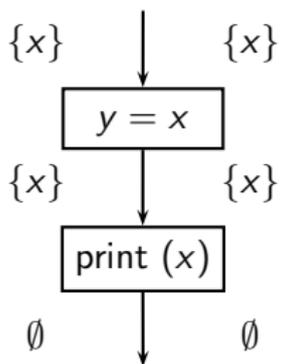
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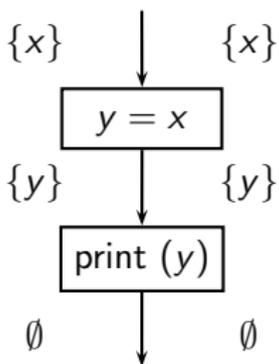
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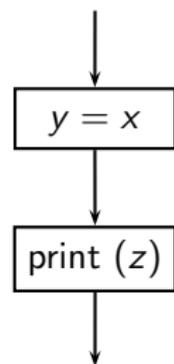
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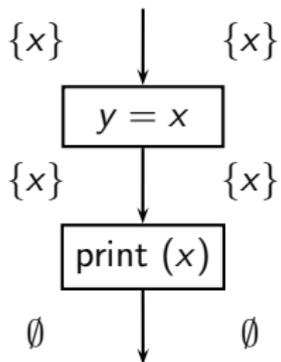
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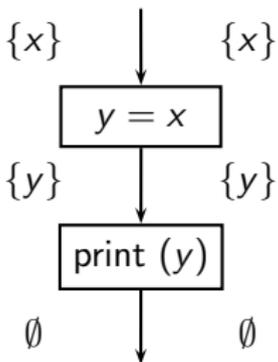
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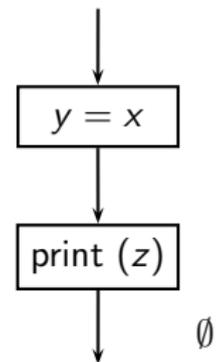
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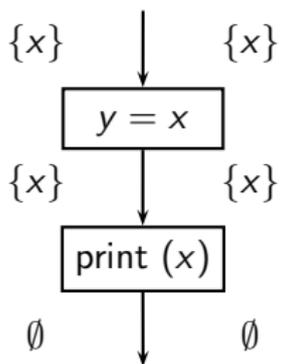


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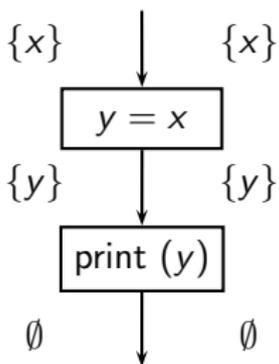
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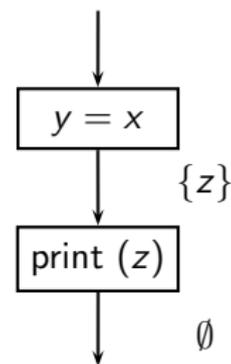
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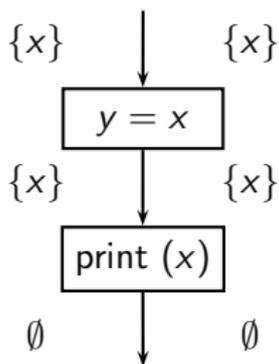
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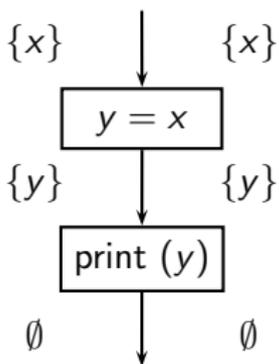
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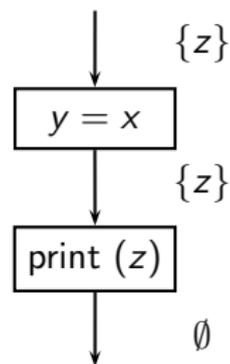
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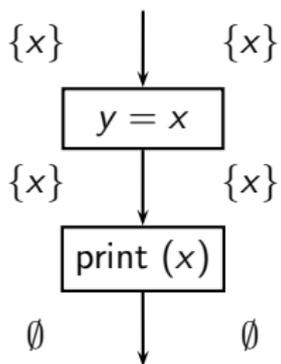
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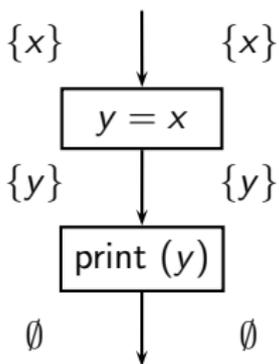
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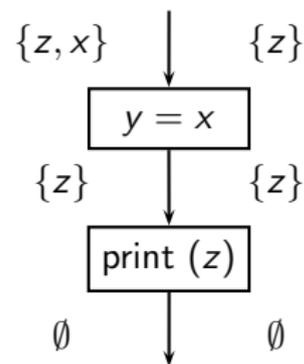
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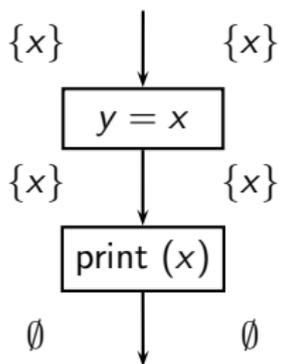
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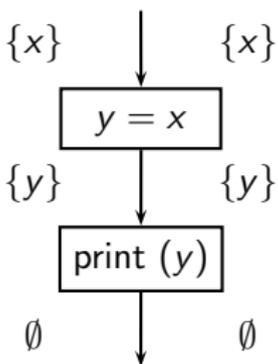
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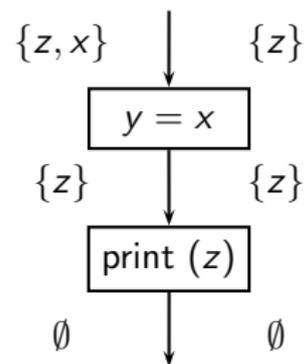
Same

Simple Liveness Strong Liveness



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Simple Liveness Strong Liveness



Different



Live Variables Analysis: Simple and Strong Liveness

- A variable is live at a program point if its current value is likely to be used later



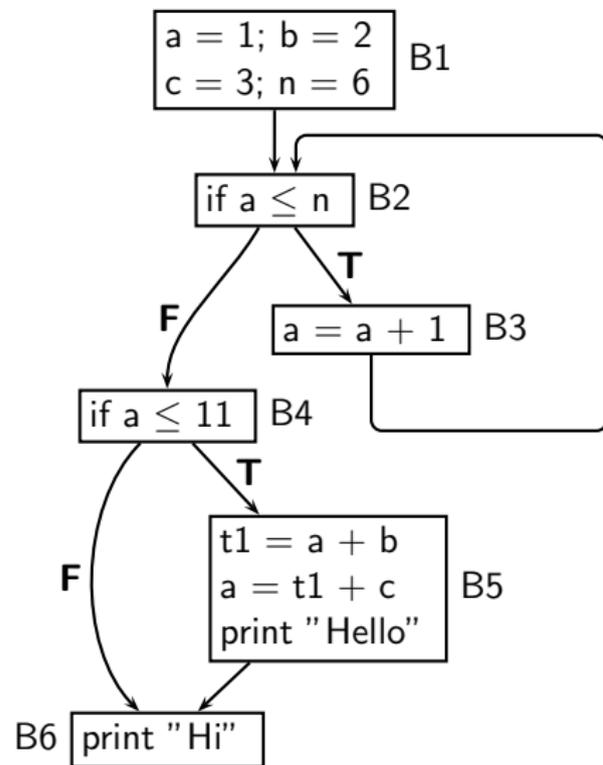
Live Variables Analysis: Simple and Strong Liveness

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- We want to compute the smallest set of variables that are live



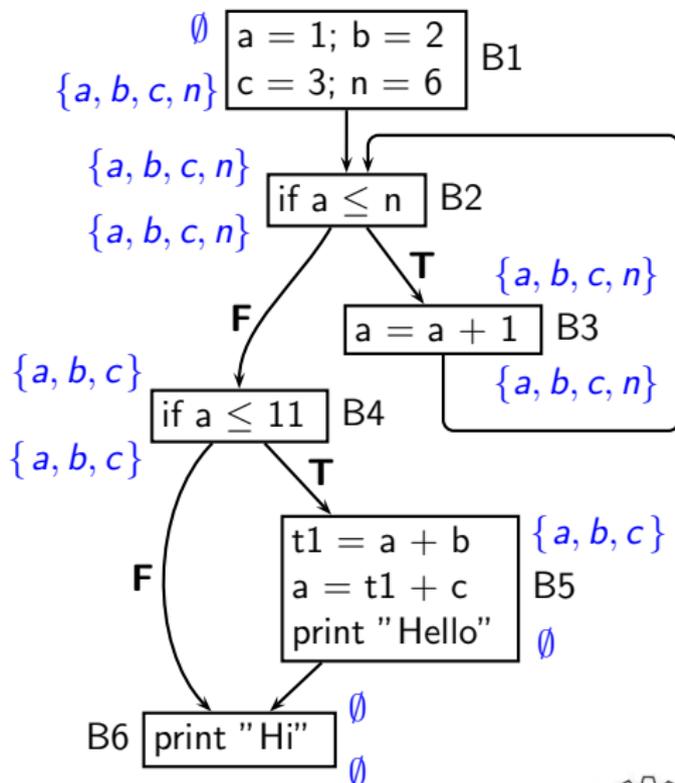
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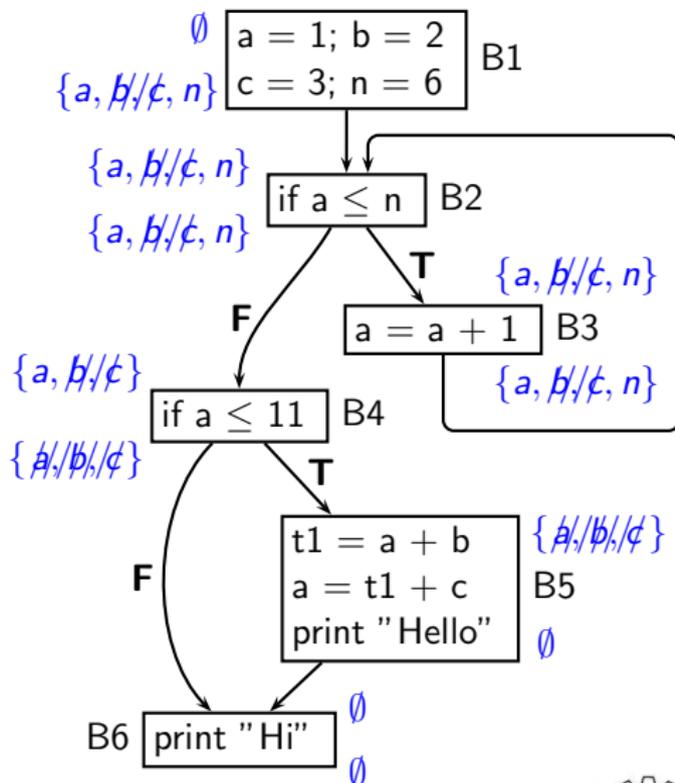
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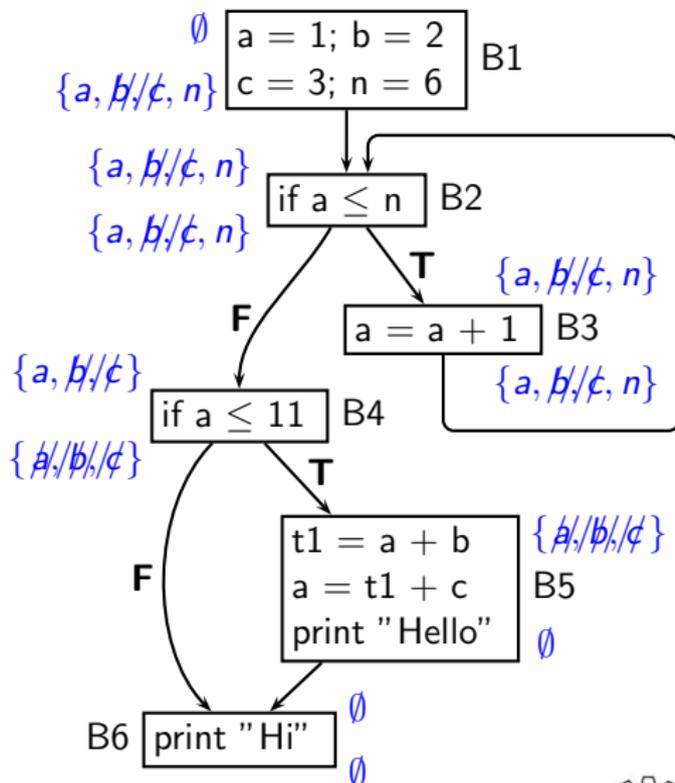
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- Strong liveness checks the liveness of the result before declaring the operands to be live
- Strong liveness is more precise than simple liveness



Data Flow Equations for Strongly Live Variables Analysis

$$In_n = f_n(Out_n)$$

$$Out_n = \begin{cases} BI & n \text{ is } End \\ \bigcup_{s \in succ(n)} In_s & \text{otherwise} \end{cases}$$

where,

$$f_n(X) = \begin{cases} (X - \{y\}) \cup (Opd(e) \cap \mathbb{V}ar) & n \text{ is } y = e, e \in \mathbb{E}xpr, y \in X \\ X - \{y\} & n \text{ is } input(y) \\ X \cup \{y\} & n \text{ is } use(y) \\ X & \text{otherwise} \end{cases}$$



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If y is not strongly live, the assignment is skipped using the "otherwise" clause



Properties of Strongly Live Variable Analysis

- What is \widehat{L} for strongly live variables analysis?
- Is strongly live variables analysis a bit vector framework?
- Is strongly live variables analysis a separable framework?
- Is strongly live variables analysis distributive? Monotonic?



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- Is strongly live variables analysis distributive? Monotonic?
 - ▶ Distributive, and hence monotonic



Distributivity of Strongly Live Variables Analysis (1)

We need to prove that

$$\forall X_1, X_2 \in L, f_n(X_1 \cup X_2) = f_n(X_1) \cup f_n(X_2)$$



Distributivity of Strongly Live Variables Analysis (1)

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(A fixed set of variable are excluded or included)
- Formally,
 - ▶ We prove it for $input(y)$, $use(y)$, $y = e$, and empty statements independently



Distributivity of Strongly Live Variables Analysis (2)

- For $input(y)$ statement:
- For $use(y)$ statement:
- For empty statement:



Distributivity of Strongly Live Variables Analysis (2)

- For *input*(y) statement:
$$\begin{aligned} f_n(X_1 \cup X_2) &= (X_1 \cup X_2) - \{y\} \\ &= (X_1 - \{y\}) \cup (X_2 - \{y\}) \\ &= f_n(X_1) \cup f_n(X_2) \end{aligned}$$
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- For empty statement:
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Distributivity of Strongly Live Variables Analysis (3)

For $y = e$ statement: Let $Y = \text{Opd}(e) \cap \text{Var}$. There are three cases:

- $y \in X_1, y \in X_2$.
- $y \in X_1, y \notin X_2$.
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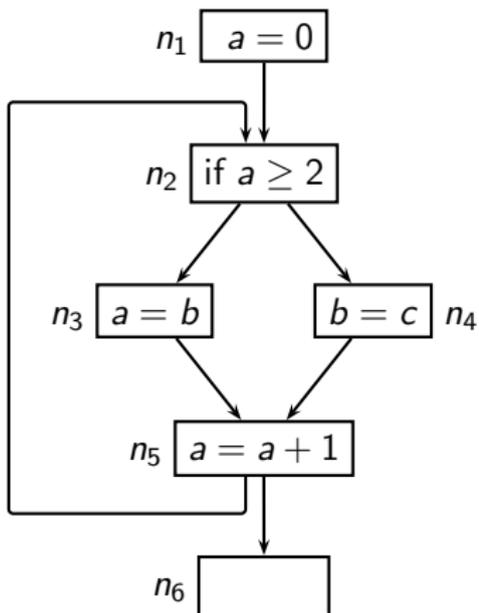
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$$f_n(X_1 \cup X_2) = X_1 \cup X_2 = f_n(X_1) \cup f_n(X_2)$$



Tutorial Problem for strongly Live Variables Analysis



Result of Strongly Live Variables Analysis

Node	Iteration #1		Iteration #2		Iteration #3		Iteration #4	
	Out_n	In_n	Out_n	In_n	Out_n	In_n	Out_n	In_n
n_6	\emptyset	\emptyset	\emptyset	\emptyset	\emptyset	\emptyset	\emptyset	\emptyset
n_5	\emptyset	\emptyset	$\{a\}$	$\{a\}$	$\{a, b\}$	$\{a, b\}$	$\{a, b, c\}$	$\{a, b, c\}$
n_4	\emptyset	\emptyset	$\{a\}$	$\{a\}$	$\{a, b\}$	$\{a, c\}$	$\{a, b, c\}$	$\{a, c\}$
n_3	\emptyset	\emptyset	$\{a\}$	$\{b\}$	$\{a, b\}$	$\{b\}$	$\{a, b, c\}$	$\{b, c\}$
n_2	\emptyset	$\{a\}$	$\{a, b\}$	$\{a, b\}$	$\{a, b, c\}$	$\{a, b, c\}$	$\{a, b, c\}$	$\{a, b, c\}$
n_1	$\{a\}$	\emptyset	$\{a, b\}$	$\{b\}$	$\{a, b, c\}$	$\{b, c\}$	$\{a, b, c\}$	$\{b, c\}$



Tutorial Problem: Strongly May-Must Liveness Analysis?

- Instead of viewing liveness information as
 - ▶ a map $\mathbb{V}\text{ar} \rightarrow \{0, 1\}$ with the lattice $\{0, 1\}$,
view it as
 - ▶ a map $\mathbb{V}\text{ar} \rightarrow \widehat{L}$ where \widehat{L} is the May-Must Lattice
- Write the data flow equations
- Prove that the flow functions are distributive



Part 5

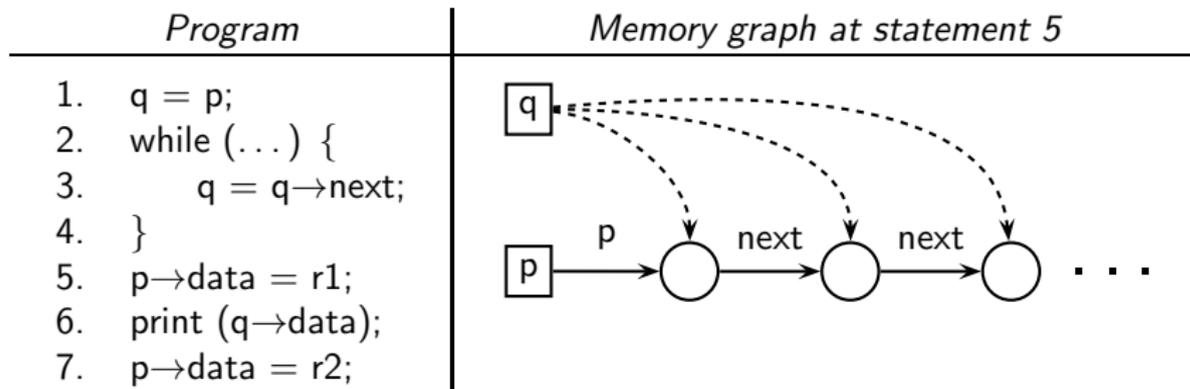
Pointer Analyses

An Outline of Pointer Analysis Coverage

- The larger perspective
- Comparing Points-to and Alias information
- Flow Insensitive Points-to Analysis
- Flow Sensitive Points-to Analysis
- Pointer Analyses: An Engineer's Landscape
- Liveness Based Points-to Analysis
- Generalizations to Heap, Arrays, Pointer Arithmetic, and Unions



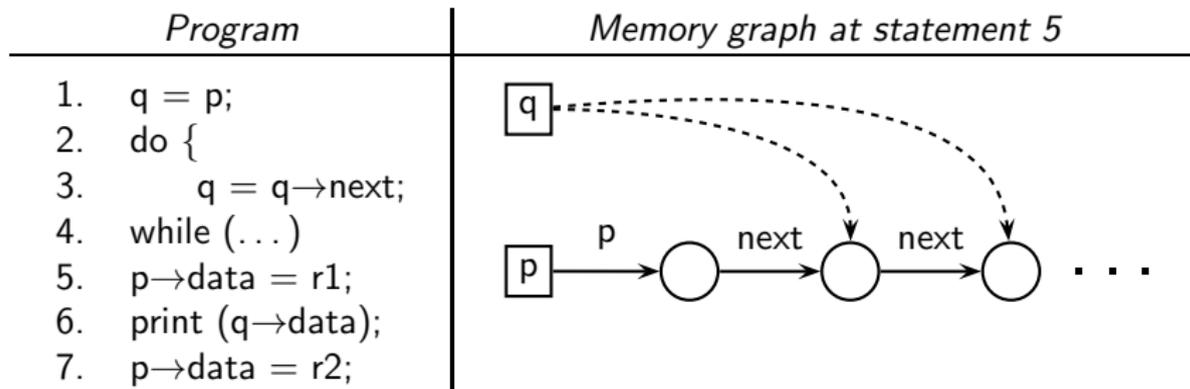
Code Optimization In Presence of Pointers



- Is $p \rightarrow \text{data}$ live at the exit of line 5? Can we delete line 5?



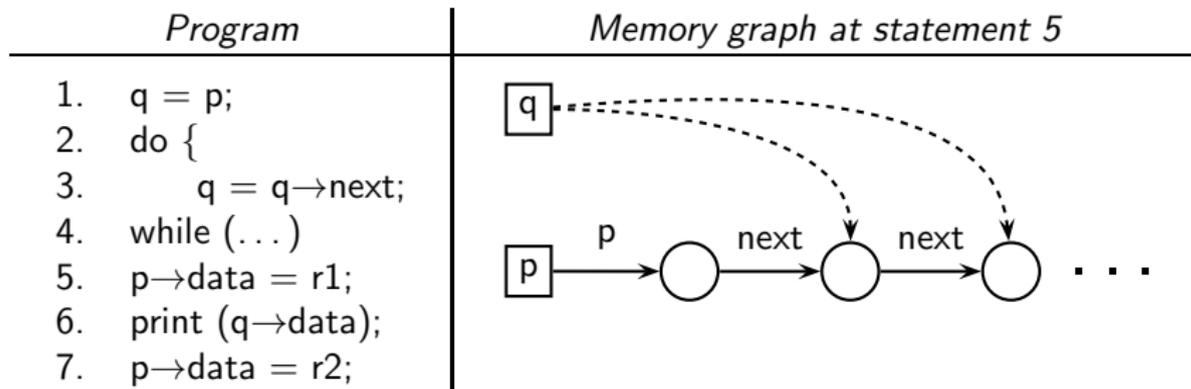
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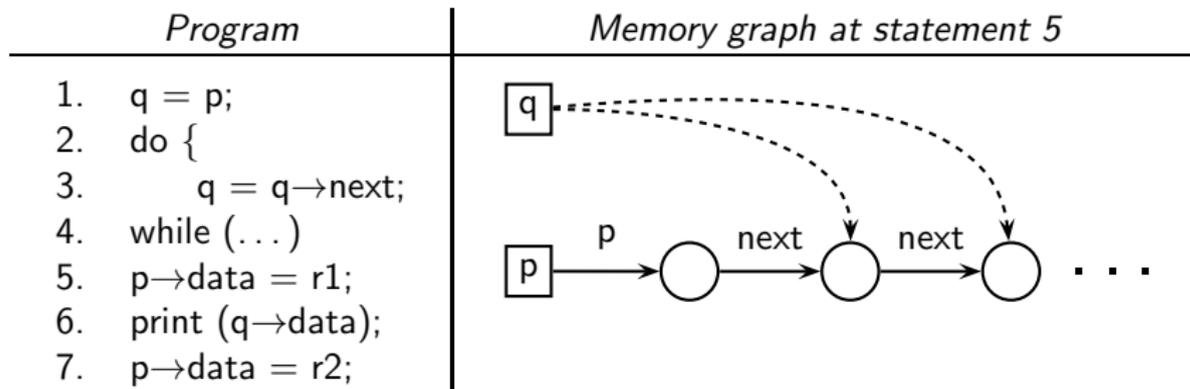
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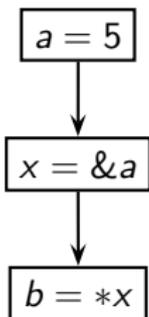
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- Is $p \rightarrow \text{data}$ live at the exit of line 5? Can we delete line 5?
- We cannot delete line 5 if p and q can be possibly aliased (while loop or do-while loop with a circular list)
- We can delete line 5 if p and q are definitely not aliased (do-while loop without a circular list)



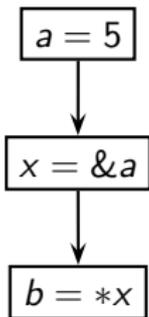
Code Optimization In Presence of Pointers



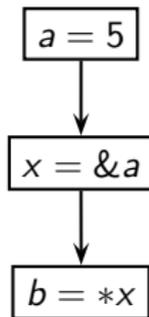
Original Program



Code Optimization In Presence of Pointers



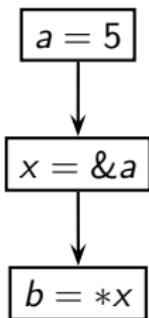
Original Program



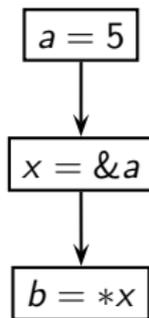
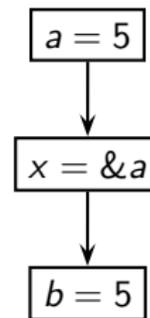
Constant Propagation
without aliasing



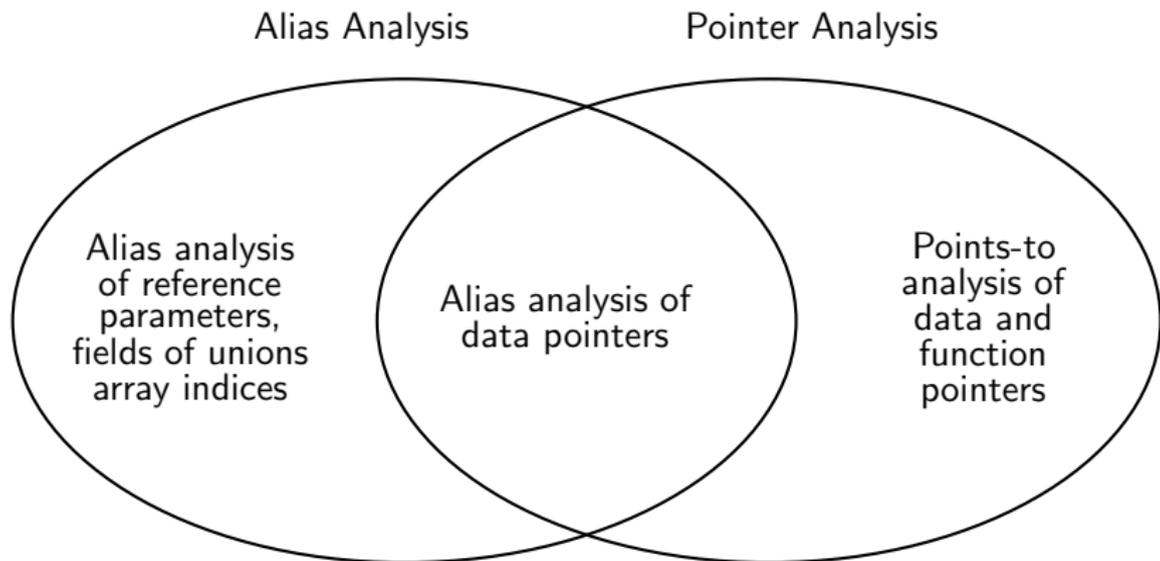
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Original Program

Constant Propagation
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The World of Pointer Analysis



Pointer Analysis Musings

- Pointer analysis collects information about indirect accesses in programs
 - ▶ Enables precise data analysis
 - ▶ Enable precise interprocedural control flow analysis
- Needs to scale to large programs
- Pointer Analysis Musings
 - Which Pointer Analysis should I Use?
Michael Hind and Anthony Pioli. ISTAA 2000
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 - 2017 .. 😞



The Mathematics of Pointer Analysis

In the most general situation

- Alias analysis is undecidable.
Landi-Ryder [POPL 1991], Landi [LOPLAS 1992],
Ramalingam [TOPLAS 1994]
- Flow insensitive alias analysis is NP-hard
Horwitz [TOPLAS 1997]
- Points-to analysis is undecidable
Chakravarty [POPL 2003]



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Adjust your expectations suitably to avoid disappointments!



The Engineering of Pointer Analysis

So what should we expect?



The Engineering of Pointer Analysis

So what should we expect? To quote Hind [PASTE 2001]



The Engineering of Pointer Analysis

So what should we expect? To quote Hind [PASTE 2001]

- “Fortunately many approximations exist”



The Engineering of Pointer Analysis

So what should we expect? To quote Hind [PASTE 2001]

- “Fortunately many approximations exist”
- “**Unfortunately too many** approximations exist!”



The Engineering of Pointer Analysis

So what should we expect? To quote Hind [PASTE 2001]

- “Fortunately many approximations exist”
- “**Unfortunately too many** approximations exist!”

Engineering of pointer analysis is much more dominant than its science



Pointer Analysis: Engineering or Science?

- Engineering view.
 - ▶ Build quick **approximations**
 - ▶ The tyranny of (exclusive) OR!
Precision OR Efficiency?
- Science view.
 - ▶ Build clean **abstractions**
 - ▶ Can we harness the Genius of AND?
Precision AND Efficiency?



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Our working definition



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Our working definition
 - ▶ Abstractions focus on precision and conciseness of modelling
 - ▶ Approximations focus on efficiency and scalability

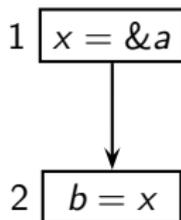


An Outline of Pointer Analysis Coverage

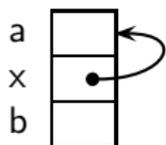
- The larger perspective
- Comparing Points-to and Alias information **Next Topic**
- Flow Insensitive Points-to Analysis
- Flow Sensitive Points-to Analysis
- Pointer Analyses: An Engineer's Landscape
- Liveness Based Points-to Analysis
- Generalizations to Heap, Arrays, Pointer Arithmetic, and Unions



Alias Information Vs. Points-to Information



Alias Information Vs. Points-to Information



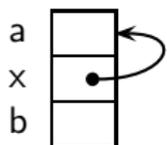
1 $x = \&a$

2 $b = x$

" x Points-to a "
denoted $x \mapsto a$

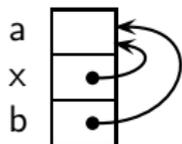


Alias Information Vs. Points-to Information



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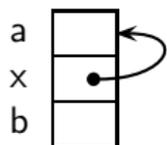


2 $b = x$

" x and b are Aliases"
denoted $x \overset{\circ}{=} b$

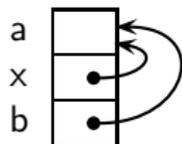


Alias Information Vs. Points-to Information



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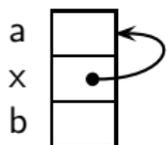
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Symmetric
and
Reflexive



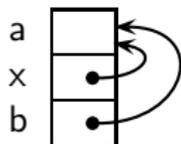
Alias Information Vs. Points-to Information



1 $x = \&a$

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Symmetric
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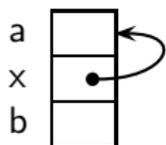
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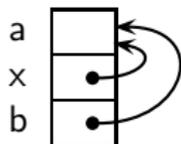
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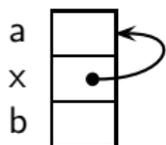
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- What about transitivity?



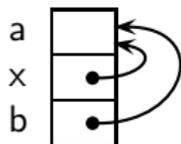
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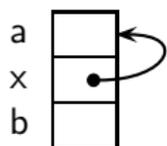
" x and b are Aliases"
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Symmetric
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Reflexive

- What about transitivity?
 - ▶ Points-to: No.



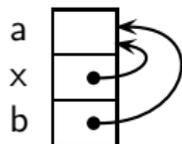
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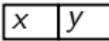
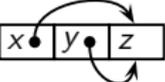
" x and b are Aliases"
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Symmetric
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Reflexive

- What about transitivity?
 - ▶ Points-to: No.
 - ▶ Alias: Depends.

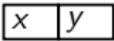
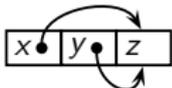


Comparing Points-to and Alias Relations (1)

Statement	Memory	Points-to	Aliases		
$x = \&y$	Before (assume) 	Existing	Existing		
	After 	New	$x \mapsto y$	New Direct	$x \overset{\circ}{=} \&y$
$x = y$	Before (assume) 	Existing	$y \mapsto z$	Existing	$y \overset{\circ}{=} \&z$
	After 	New	$x \mapsto z$	New Direct	$x \overset{\circ}{=} y$
		New Indirect	$x \overset{\circ}{=} \&z$		



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		New	$x \mapsto z$	New Direct	$x \overset{\circ}{=} y$
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- Indirect aliases. Substitute a name by its aliases for transitivity



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		New Direct $x \overset{\circ}{=} y$	
	After	New $x \mapsto z$	New Indirect $x \overset{\circ}{=} \&z$

- Indirect aliases. Substitute a name by its aliases for transitivity
 - Derived aliases. Apply indirection operator to aliases (ignored here)
- $$x \overset{\circ}{=} y \Rightarrow *x \overset{\circ}{=} *y$$

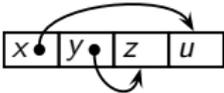


Comparing Points-to and Alias Relations (2)

Statement	Memory	Points-to	Aliases
$*x = y$			
$x = *y$			

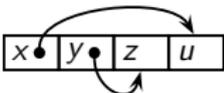
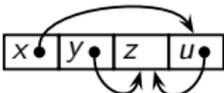


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$x \mapsto u$									
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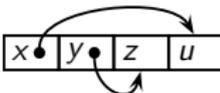
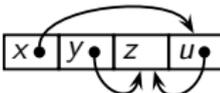


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Statement	Memory	Points-to	Aliases				
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	After 		New Direct $*x \stackrel{\circ}{=} y$				
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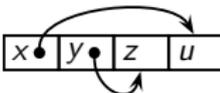
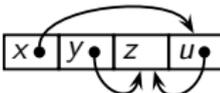


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Existing	$x \mapsto u$ $y \mapsto z$							
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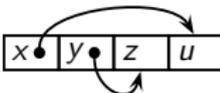
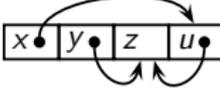
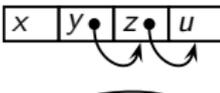
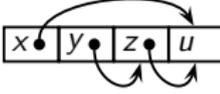


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$*x = y$	Before (assume) 	Existing $x \mapsto u$ $y \mapsto z$ ----- New $u \mapsto z$	Existing $x \overset{\circ}{=} \&u$ $y \overset{\circ}{=} \&z$
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$x = *y$	Before (assume) 	Existing $y \mapsto z$ $z \mapsto u$	Existing $y \overset{\circ}{=} \&z$ $z \overset{\circ}{=} \&u$ $*y \overset{\circ}{=} \&u$

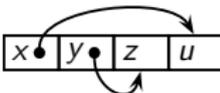
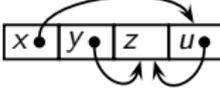
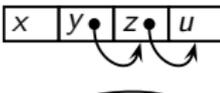
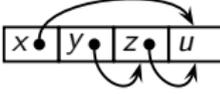


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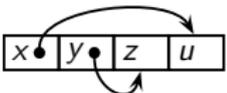
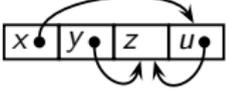
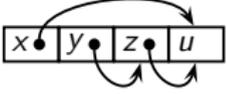


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After 	New Direct	$x \stackrel{\circ}{=} *y$						
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Comparing Points-to and Alias Relations (2)

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$*x = y$	Before (assume) 	Existing $x \mapsto u$ $y \mapsto z$ ----- New $u \mapsto z$	Existing	$x \stackrel{\circ}{=} \&u$ $y \stackrel{\circ}{=} \&z$
	After 		New Indirect	$u \stackrel{\circ}{=} \&z$ $y \stackrel{\circ}{=} u$ $*x \stackrel{\circ}{=} \&z$
$x = *y$	Before (assume) 	Existing $y \mapsto z$ $z \mapsto u$ ----- New $x \mapsto u$	Existing	$y \stackrel{\circ}{=} \&z$ $z \stackrel{\circ}{=} \&u$ $*y \stackrel{\circ}{=} \&u$
	After 		New Direct	$x \stackrel{\circ}{=} *y$
New Indirect				
$x \stackrel{\circ}{=} \&u$ $x \stackrel{\circ}{=} z$				
The resulting memories look similar but are different. In the first case we have $u \mapsto z$ whereas in the second case the arrow direction is opposite (i.e. $z \mapsto u$).				



Comparing Points-to and Alias Relations (3)

- Points-to information records edges in the memory graph
 - ▶ aliases of the kind $x \overset{\circ}{=} \&y$
 x holds the address of y

- Alias information records paths in the memory graph
 - ▶ paths incident on the same node
 x and y hold the same address (and the address is left implicit)



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Comparing Points-to and Alias Relations (3)

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 - ▶ if we have $x \doteq y$ then $*x \doteq *y$ is redundant and is not recorded



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More compact but less general

- Alias information records paths in the memory graph
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More general and more complex



An Outline of Pointer Analysis Coverage

- The larger perspective
- Comparing Points-to and Alias information
- Flow Insensitive Points-to Analysis **Next Topic**
- Flow Sensitive Points-to Analysis
- Pointer Analyses: An Engineer's Landscape
- Liveness Based Points-to Analysis
- Generalizations to Heap, Arrays, Pointer Arithmetic, and Unions



Flow Sensitive Vs. Flow Insensitive Pointer Analysis

- Flow insensitive pointer analysis
 - ▶ Inclusion based: Andersen's approach
 - ▶ Equality based: Steensgaard's approach
- Flow sensitive pointer analysis
 - ▶ May points-to analysis
 - ▶ Must points-to analysis



Flow Insensitivity in Data Flow Analysis

- Assumption: Statements can be executed in any order.
- Instead of computing point-specific data flow information, summary data flow information is computed.

The summary information is required to be a safe approximation of point-specific information for each point.

- $\text{Kill}_n(X)$ component is ignored.

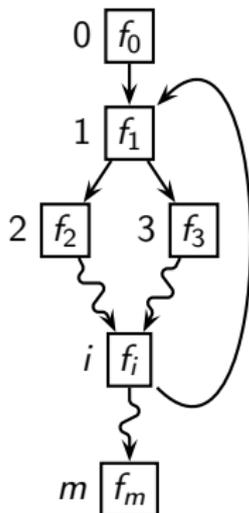
If statement n kills data flow information, there is an alternate path that excludes n .

*The control flow graph is a complete graph
(except for the Start and End nodes)*

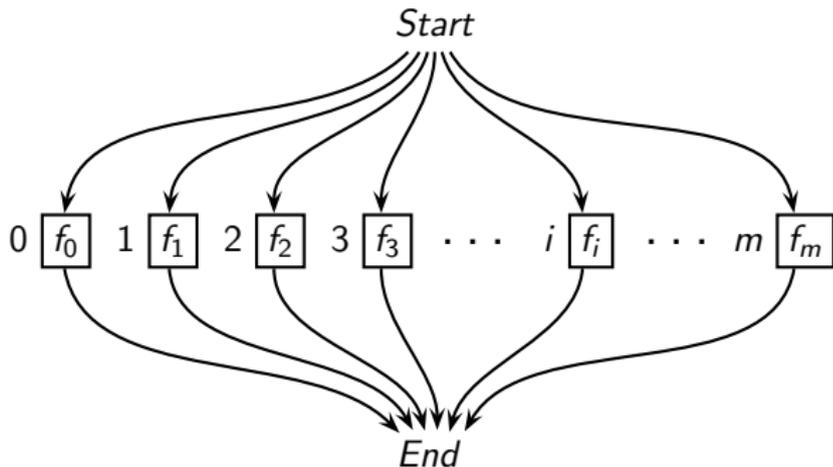


Flow Insensitivity in Data Flow Analysis

Assuming that there are no dependent parts in Gen_n and Kill_n is ignored



Control flow graph

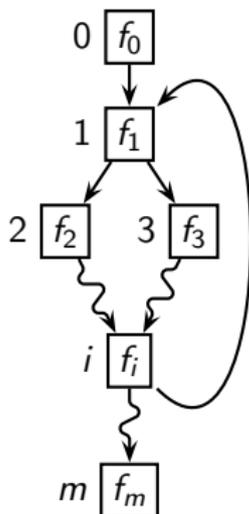


Flow insensitive analysis

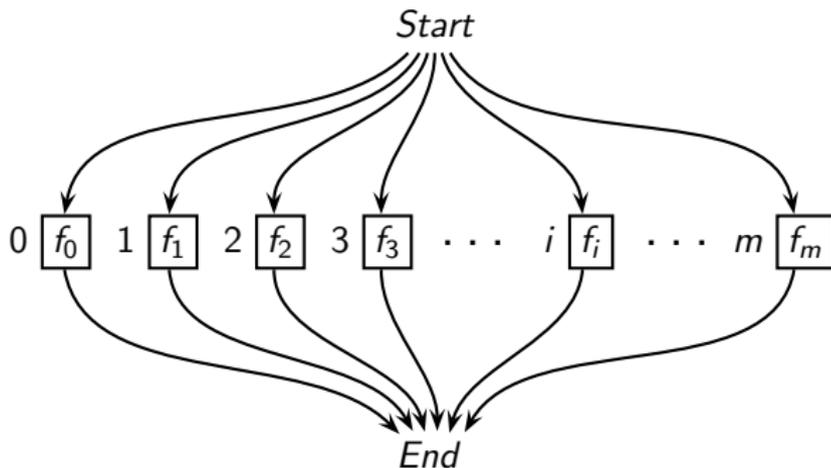


Flow Insensitivity in Data Flow Analysis

Assuming that there are no dependent parts in Gen_n and $Kill_n$ is ignored



Control flow graph



Flow insensitive analysis

Function composition is replaced by function confluence

Examples of Flow Insensitive Analyses



Examples of Flow Insensitive Analyses

- Type checking/inferencing
(What about interpreted languages?)



Examples of Flow Insensitive Analyses

- Type checking/inferencing
(What about interpreted languages?)
- Address taken analysis
Which variables have their addresses taken?



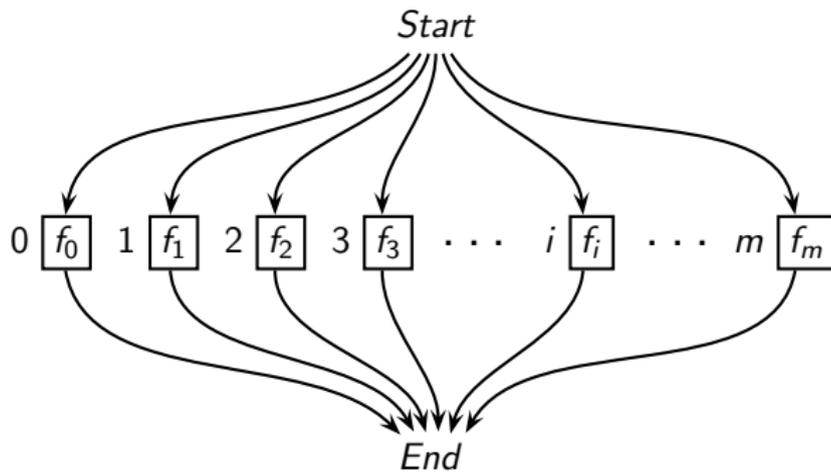
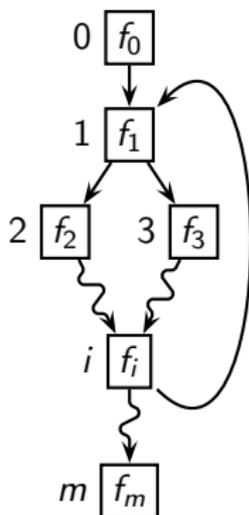
Examples of Flow Insensitive Analyses

- Type checking/inferencing
(What about interpreted languages?)
- Address taken analysis
Which variables have their addresses taken?
- Side effects analysis
Does a procedure modify a global variable? Reference Parameter?



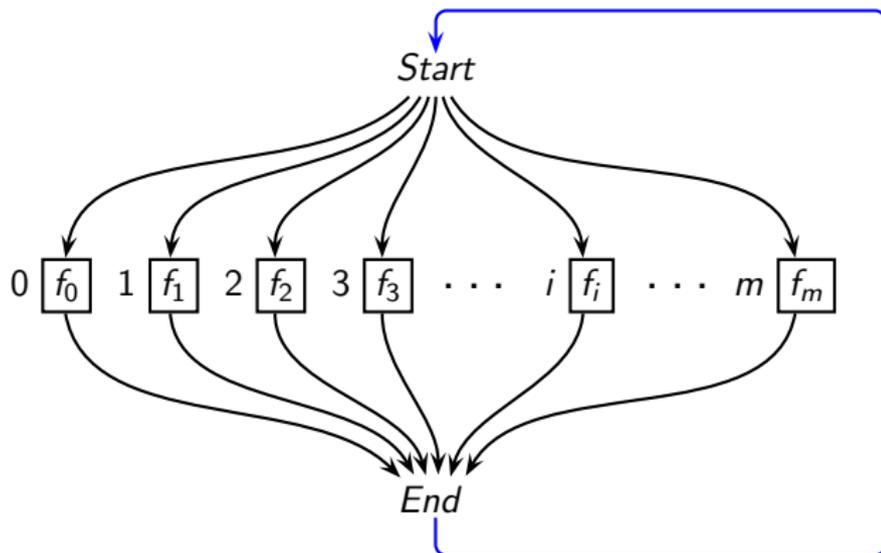
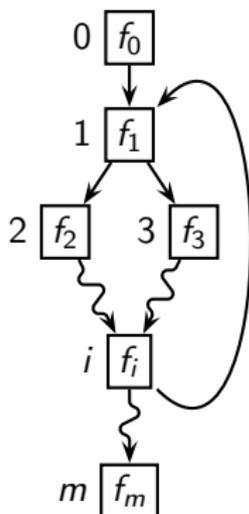
Flow Insensitivity in Data Flow Analysis

Assuming $\text{Gen}_n(X)$ has dependent parts and $\text{Kill}_n(X)$ is ignored



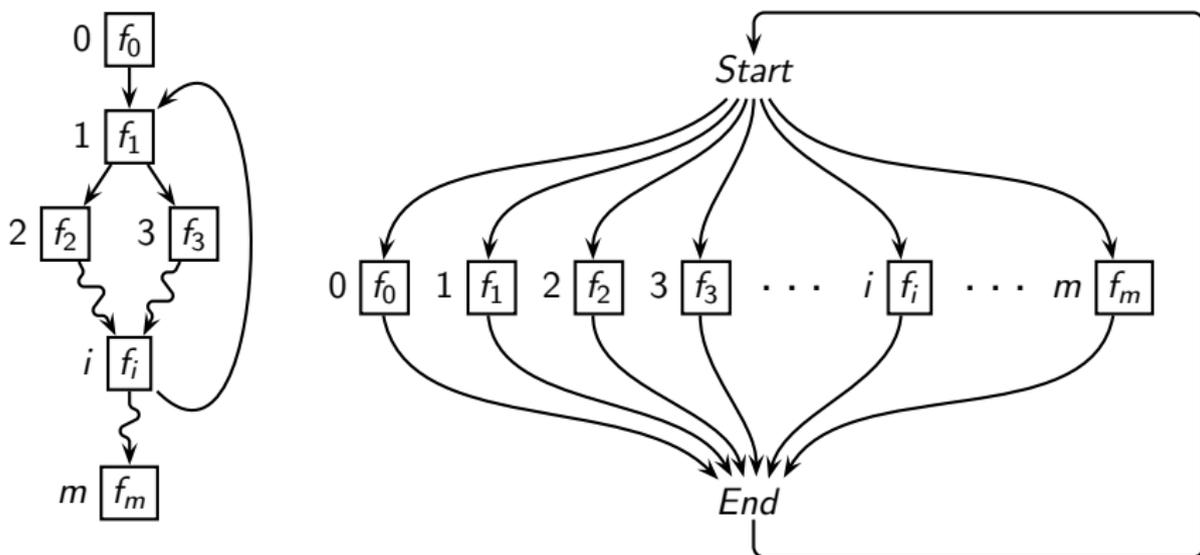
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Flow Insensitivity in Data Flow Analysis

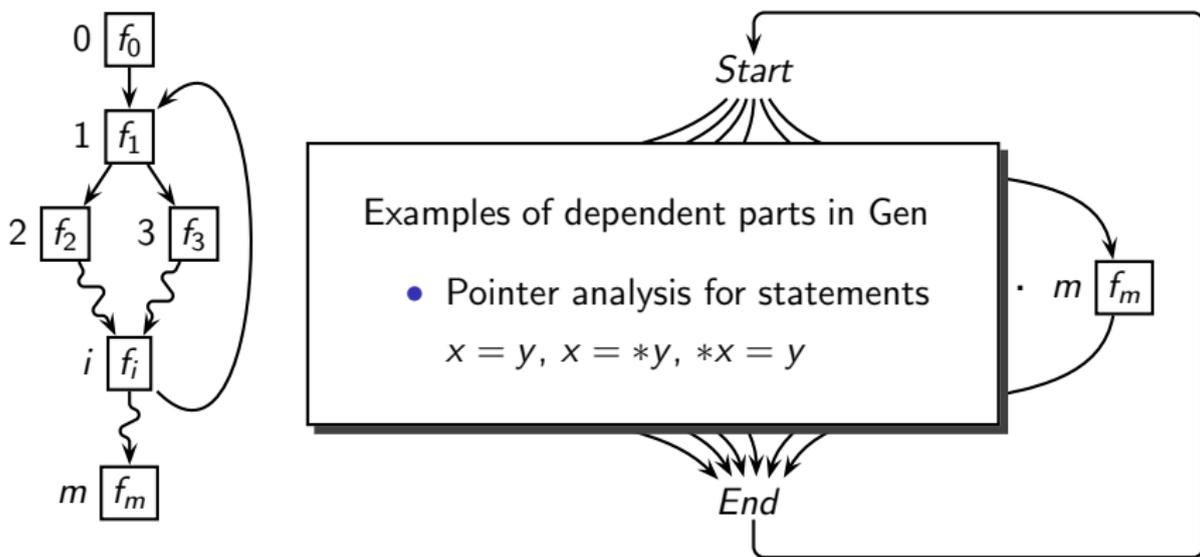
Assuming $\text{Gen}_n(X)$ has dependent parts and $\text{Kill}_n(X)$ is ignored



Allows arbitrary compositions of flow functions in any order
 \Rightarrow *Flow insensitivity*

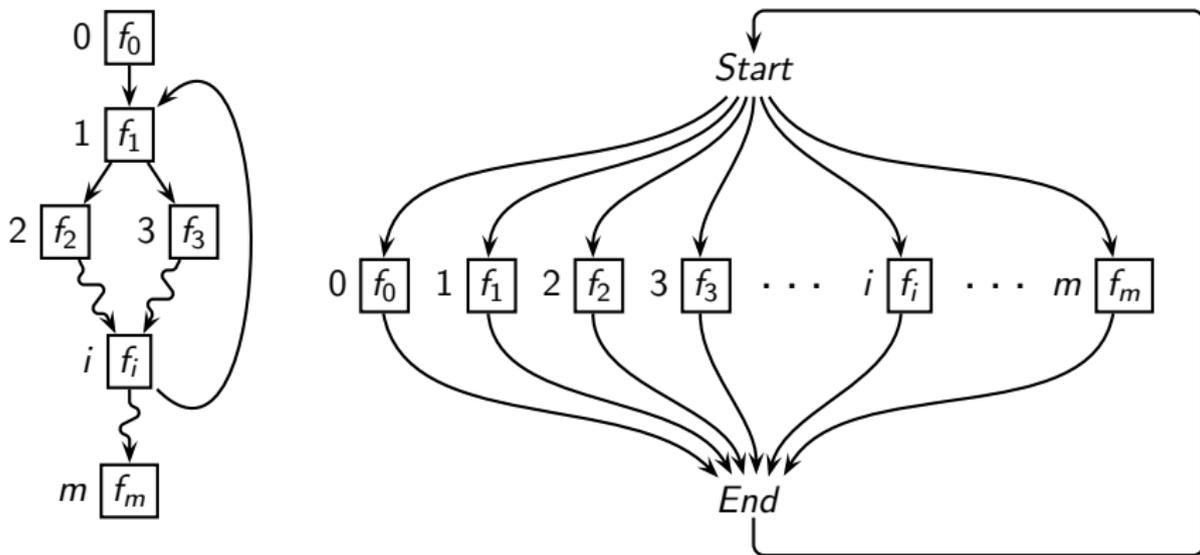
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Assuming $\text{Gen}_n(X)$ has dependent parts and $\text{Kill}_n(X)$ is ignored



Flow Insensitivity in Data Flow Analysis

Assuming $\text{Gen}_n(X)$ has dependent parts and $\text{Kill}_n(X)$ is ignored



In practice, dependent constraints are collected in a global repository in one pass and then are solved independently



Notation for Andersen's and Steensgaard's Points-to Analysis

- P_x denotes the set of pointees of pointer variable x
- $Unify(x, y)$ unifies locations x and y
 - ▶ x and y are treated as equivalent locations
 - ▶ the pointees of the unified locations are also unified transitively
- $UnifyPTS(x, y)$ unifies the pointees of x and y
 - ▶ x and y themselves are not unified



Andersen's and Steensgaard's Points-to Analysis

Statement	Andersen's Points-to Sets	Steensgaard's Points-to Sets
$x = \&y$	$P_x \supseteq \{y\}$	$P_x \supseteq \{y\}$ $Unify(y, z)$ for some $z \in P_x$
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Andersen's view

Steensgaard's view



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Andersen's view

- x points to y
- Include y in the points-to set of x

Steensgaard's view



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Steensgaard's view

- Equivalence between: All pointees of x
- Unify y and pointees of x



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Inclusion



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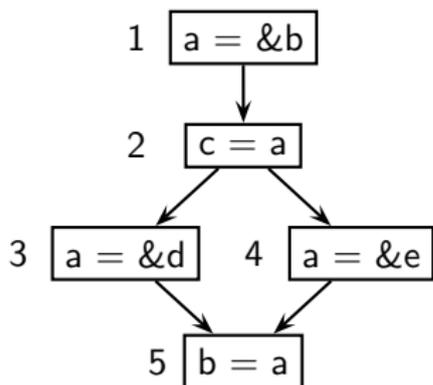
Inclusion

Equality



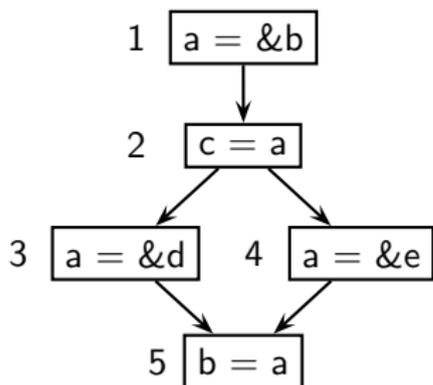
Inclusion Based (aka Andersen's) Points-to Analysis: Example 1

Program



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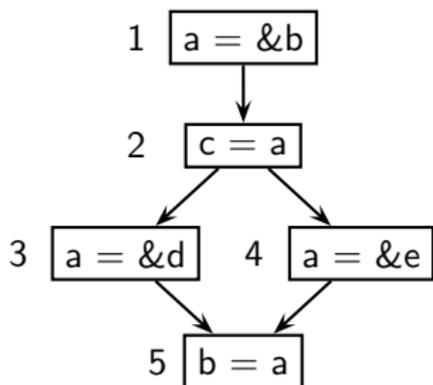


Node	Constraint
1	$P_a \supseteq \{b\}$
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3	$P_a \supseteq \{d\}$
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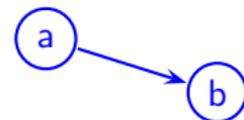
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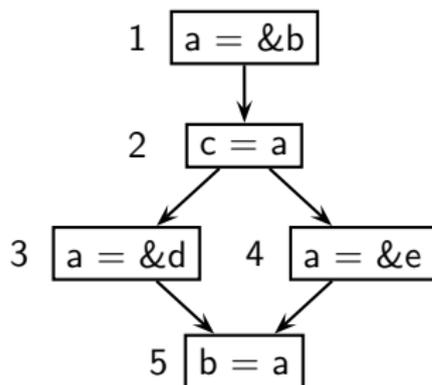
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Points-to Graph



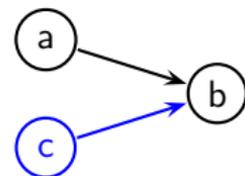
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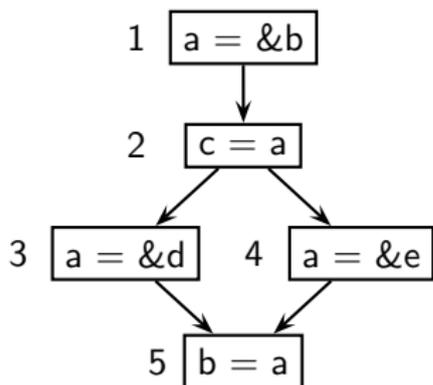
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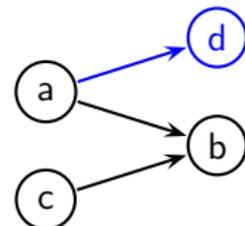
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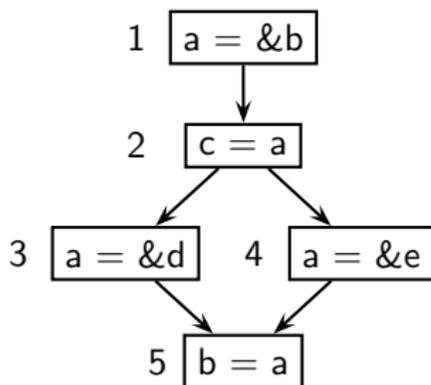
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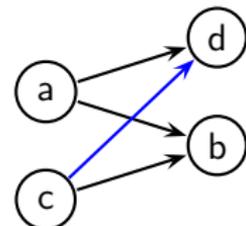
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5	$P_b \supseteq P_a$

Points-to Graph

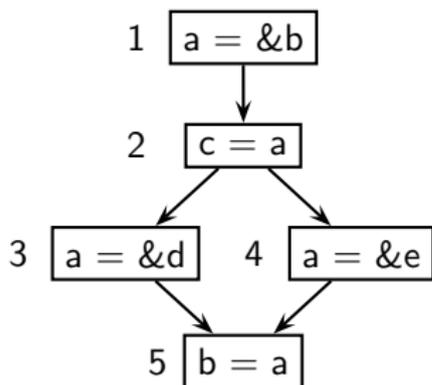


- Since P_a has changed, P_c needs to be processed again



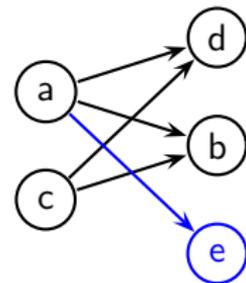
Inclusion Based (aka Andersen's) Points-to Analysis: Example 1

Program



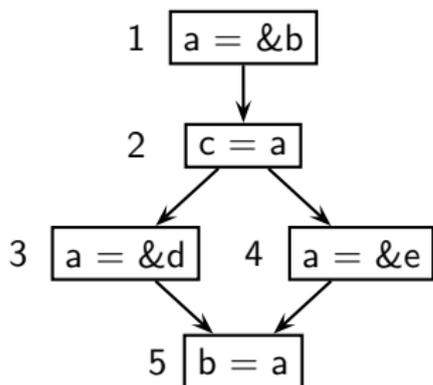
Node	Constraint
1	$P_a \supseteq \{b\}$
2	$P_c \supseteq P_a$
3	$P_a \supseteq \{d\}$
4	$P_a \supseteq \{e\}$
5	$P_b \supseteq P_a$

Points-to Graph



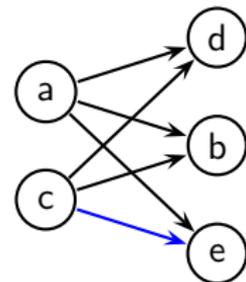
Inclusion Based (aka Andersen's) Points-to Analysis: Example 1

Program



Node	Constraint
1	$P_a \supseteq \{b\}$
2	$P_c \supseteq P_a$
3	$P_a \supseteq \{d\}$
4	$P_a \supseteq \{e\}$
5	$P_b \supseteq P_a$

Points-to Graph

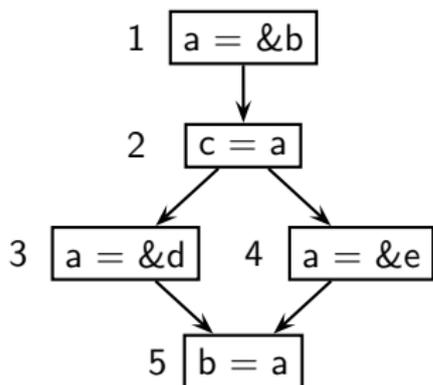


- Observe that P_c is processed for the third time
- Order of processing the sets influences efficiency significantly
- A plethora of heuristics have been proposed



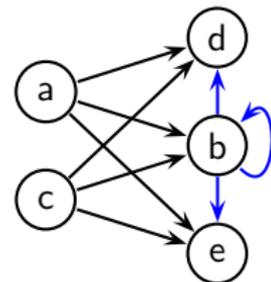
Inclusion Based (aka Andersen's) Points-to Analysis: Example 1

Program



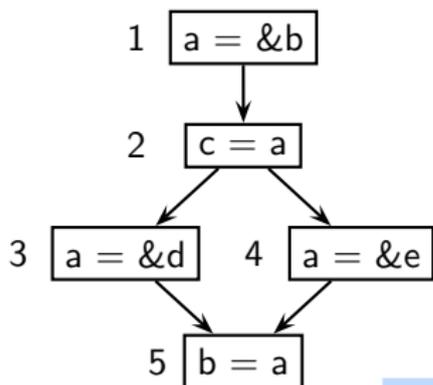
Node	Constraint
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3	$P_a \supseteq \{d\}$
4	$P_a \supseteq \{e\}$
5	$P_b \supseteq P_a$

Points-to Graph



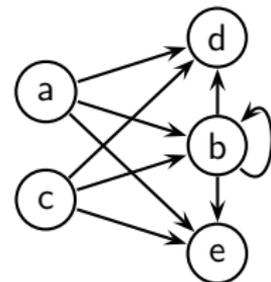
Inclusion Based (aka Andersen's) Points-to Analysis: Example 1

Program



Node	Constraint
1	$P_a \supseteq \{b\}$
2	$P_c \supseteq P_a$
3	$P_a \supseteq \{d\}$
4	$P_a \supseteq \{e\}$
5	$P_b \supseteq P_a$

Points-to Graph



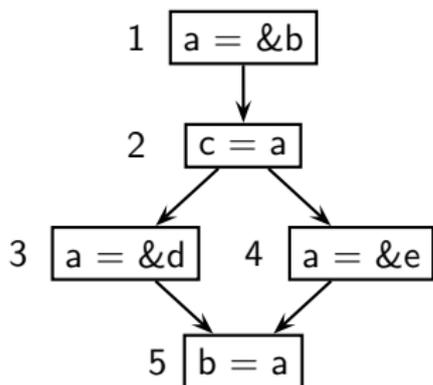
Actually:

- c does not point to any location in block 1
- a does not point b in block 5
(the method ignores the kill due to 3 and 4)
- b does not point to itself at any time



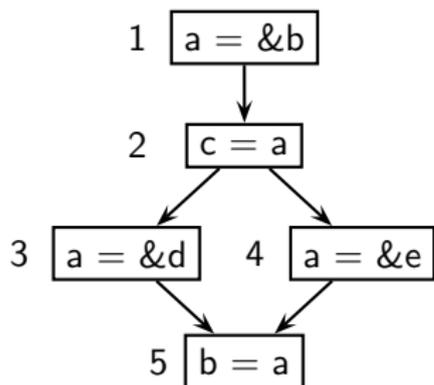
Equality Based (aka Steensgaard's) Points-to Analysis: Example 1

Program



Equality Based (aka Steensgaard's) Points-to Analysis: Example 1

Program

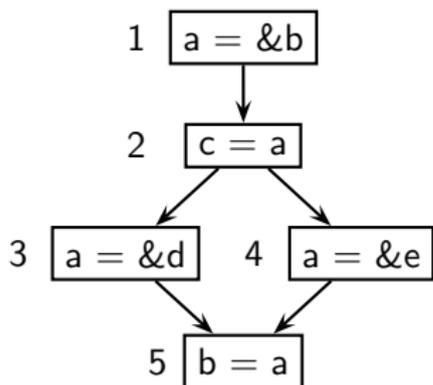


Node	Constraint
1	$P_a \supseteq \{b\}$ $Unify(x, d), x \in P_a$
2	$UnifyPTS(c, a)$
3	$P_a \supseteq \{d\}$ $Unify(x, d), x \in P_a$
4	$P_a \supseteq \{e\}$ $Unify(x, e), x \in P_a$
5	$UnifyPTS(b, a)$



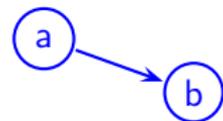
Equality Based (aka Steensgaard's) Points-to Analysis: Example 1

Program



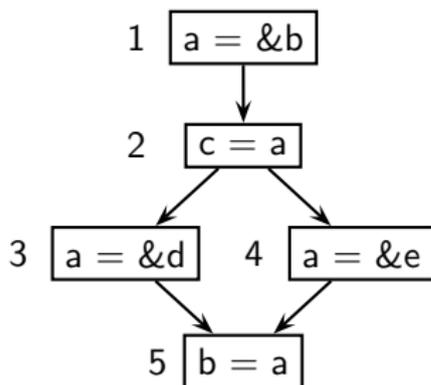
Node	Constraint
1	$P_a \supseteq \{b\}$ $Unify(x, d), x \in P_a$
2	$UnifyPTS(c, a)$
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4	$P_a \supseteq \{e\}$ $Unify(x, e), x \in P_a$
5	$UnifyPTS(b, a)$

Points-to Graph



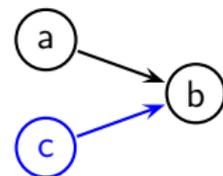
Equality Based (aka Steensgaard's) Points-to Analysis: Example 1

Program



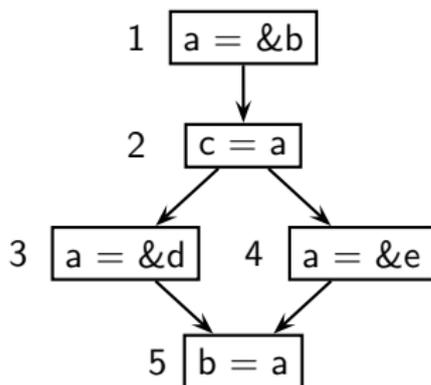
Node	Constraint
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4	$P_a \supseteq \{e\}$ $Unify(x, e), x \in P_a$
5	$UnifyPTS(b, a)$

Points-to Graph



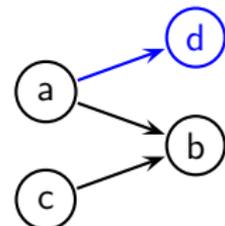
Equality Based (aka Steensgaard's) Points-to Analysis: Example 1

Program



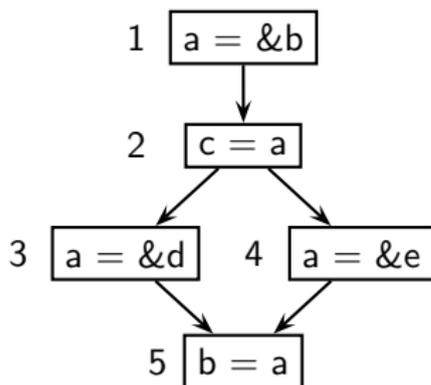
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5	$UnifyPTS(b, a)$

Points-to Graph



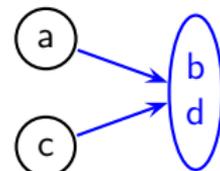
Equality Based (aka Steensgaard's) Points-to Analysis: Example 1

Program



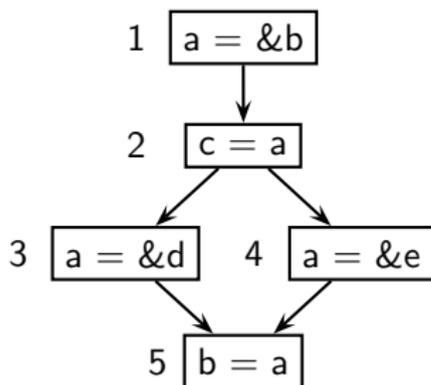
Node	Constraint
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4	$P_a \supseteq \{e\}$ $Unify(x, e), x \in P_a$
5	$UnifyPTS(b, a)$

Points-to Graph



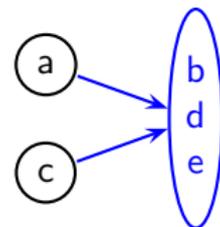
Equality Based (aka Steensgaard's) Points-to Analysis: Example 1

Program



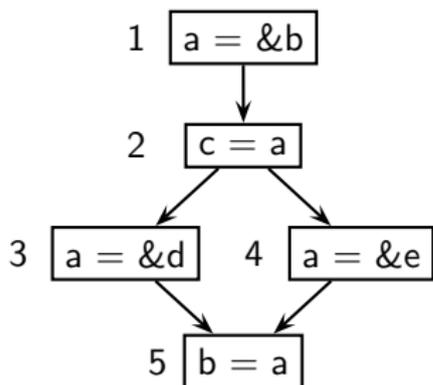
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4	$P_a \supseteq \{e\}$ $Unify(x, e), x \in P_a$
5	$UnifyPTS(b, a)$

Points-to Graph



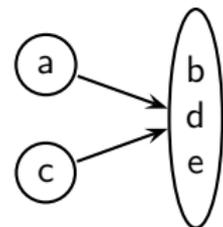
Equality Based (aka Steensgaard's) Points-to Analysis: Example 1

Program



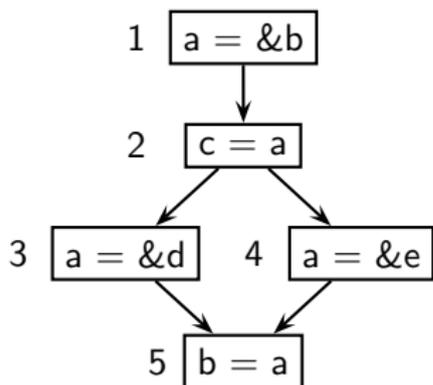
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4	$P_a \supseteq \{e\}$ $Unify(x, e), x \in P_a$
5	$UnifyPTS(b, a)$

Points-to Graph



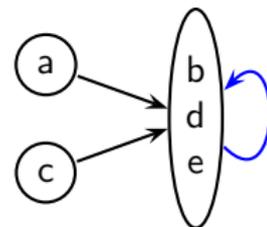
Equality Based (aka Steensgaard's) Points-to Analysis: Example 1

Program



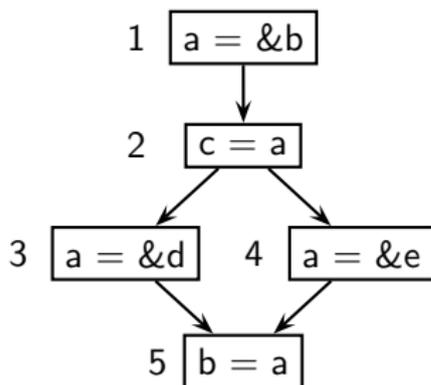
Node	Constraint
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4	$P_a \supseteq \{e\}$ $Unify(x, e), x \in P_a$
5	$UnifyPTS(b, a)$

Points-to Graph



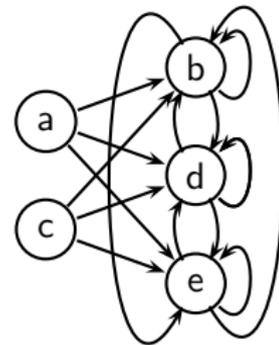
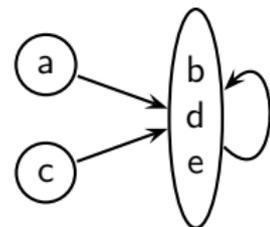
Equality Based (aka Steensgaard's) Points-to Analysis: Example 1

Program



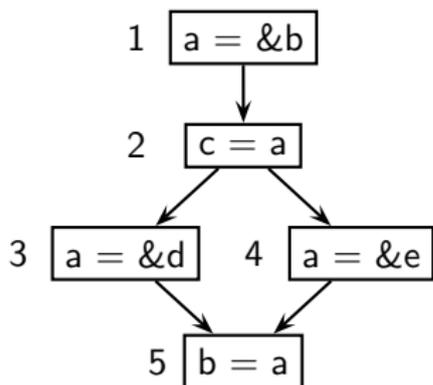
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4	$P_a \supseteq \{e\}$ $Unify(x, e), x \in P_a$
5	$UnifyPTS(b, a)$

Points-to Graph



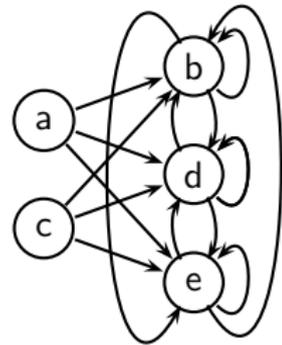
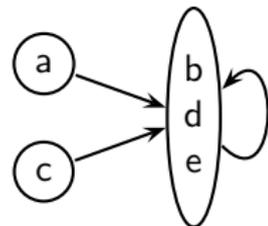
Equality Based (aka Steensgaard's) Points-to Analysis: Example 1

Program



Node	Constraint
1	$P_a \supseteq \{b\}$ $Unify(x, d), x \in P_a$
2	$UnifyPTS(c, a)$
3	$P_a \supseteq \{d\}$ $Unify(x, d), x \in P_a$
4	$P_a \supseteq \{e\}$ $Unify(x, e), x \in P_a$
5	$UnifyPTS(b, a)$

Points-to Graph



- The full blown up points-to graph has far more edges than in the graph created by Andersen's method
- Far more efficient but far less precise



Comparing Equality and Inclusion Based Analyses (2)

- Andersen's algorithm is cubic in number of pointers
- Steensgaard's algorithm is nearly linear in number of pointers



Comparing Equality and Inclusion Based Analyses (2)

- Andersen's algorithm is cubic in number of pointers
- Steensgaard's algorithm is nearly linear in number of pointers
 - ▶ How can it be more efficient by an orders of magnitude?



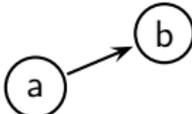
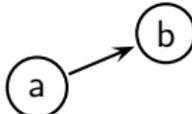
Efficiency of Equality Based Approach

Program	Andersen's approach	Steensgaard's approach
<code>a = &b</code> <code>a = &c</code> <code>b = &d</code> <code>b = &c</code>		

- Andersen's inclusion based wisdom:
 - ▶ Add edges and let the number of successors increase
- Steensgaard's equality based wisdom:
 - ▶ Merge multiple successors and maintain a single successor of any node



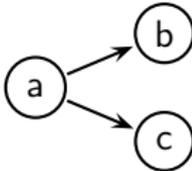
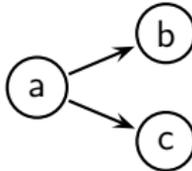
Efficiency of Equality Based Approach

Program	Andersen's approach	Steensgaard's approach
<pre>a = &b a = &c b = &d b = &c</pre>	 <pre>graph LR a((a)) --> b((b))</pre>	 <pre>graph LR a((a)) --> b((b))</pre>

- Andersen's inclusion based wisdom:
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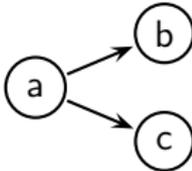
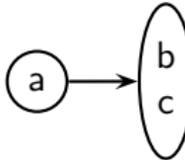
Efficiency of Equality Based Approach

Program	Andersen's approach	Steensgaard's approach
<pre>a = &b a = &c b = &d b = &c</pre>	 <pre>graph LR a((a)) --> b((b)) a --> c((c))</pre>	 <pre>graph LR a((a)) --> b((b)) a --> c((c))</pre>

- Andersen's inclusion based wisdom:
 - ▶ Add edges and let the number of successors increase
- Steensgaard's equality based wisdom:
 - ▶ Merge multiple successors and maintain a single successor of any node



Efficiency of Equality Based Approach

Program	Andersen's approach	Steensgaard's approach
<pre>a = &b a = &c b = &d b = &c</pre>	 <pre>graph LR a((a)) --> b((b)) a --> c((c))</pre>	 <pre>graph LR a((a)) --> bc([b c])</pre>

- Andersen's inclusion based wisdom:
 - ▶ Add edges and let the number of successors increase
- Steensgaard's equality based wisdom:
 - ▶ Merge multiple successors and maintain a single successor of any node



Efficiency of Equality Based Approach

Program	Andersen's approach	Steensgaard's approach
<pre> a = &b a = &c b = &d b = &c </pre>	<pre> graph LR a((a)) --> b((b)) a --> c((c)) b --> d((d)) </pre>	<pre> graph LR a((a)) --> bc([b c]) bc --> d((d)) </pre>

- Andersen's inclusion based wisdom:
 - ▶ Add edges and let the number of successors increase
- Steensgaard's equality based wisdom:
 - ▶ Merge multiple successors and maintain a single successor of any node



Efficiency of Equality Based Approach

Program	Andersen's approach	Steensgaard's approach
<pre> a = &b a = &c b = &d b = &c </pre>	<pre> graph LR a((a)) --> b((b)) a --> c((c)) b --> d((d)) </pre>	<pre> graph LR a((a)) --> bc([b c]) bc --> bc bc --> d((d)) </pre>

- Andersen's inclusion based wisdom:
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Efficiency of Equality Based Approach

Program	Andersen's approach	Steensgaard's approach
<pre> a = &b a = &c b = &d b = &c </pre>	<pre> graph LR a((a)) --> b((b)) a --> c((c)) b --> d((d)) </pre>	<pre> graph LR a((a)) --> abc([b c d]) abc --> abc </pre>

- Andersen's inclusion based wisdom:
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- Steensgaard's equality based wisdom:
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Efficiency of Equality Based Approach

Program	Andersen's approach	Steensgaard's approach
<pre> a = &b a = &c b = &d b = &c </pre>	<pre> graph LR a((a)) --> b((b)) a --> c((c)) b --> d((d)) </pre>	

- Andersen's inclusion based wisdom:
 - ▶ Add edges and let the number of successors increase
- Steensgaard's equality based wisdom:
 - ▶ Merge multiple successors and maintain a single successor of any node
 - ▶ Since a larger number of pointers treated are alike and fewer distinctions are maintained, we get much smaller points-to graphs



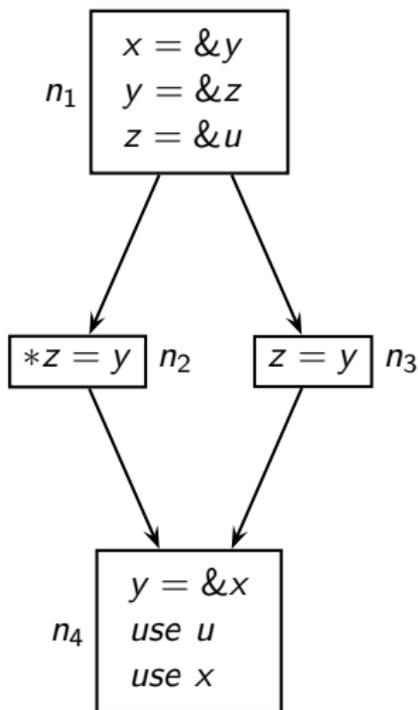
Efficiency of Equality Based Approach

Program	Andersen's approach	Steensgaard's approach
<pre> a = &b a = &c b = &d b = &c </pre>	<pre> graph LR a((a)) --> b((b)) a --> c((c)) b --> d((d)) </pre>	<pre> graph LR a((a)) --> abc([b c d]) abc --> abc </pre>

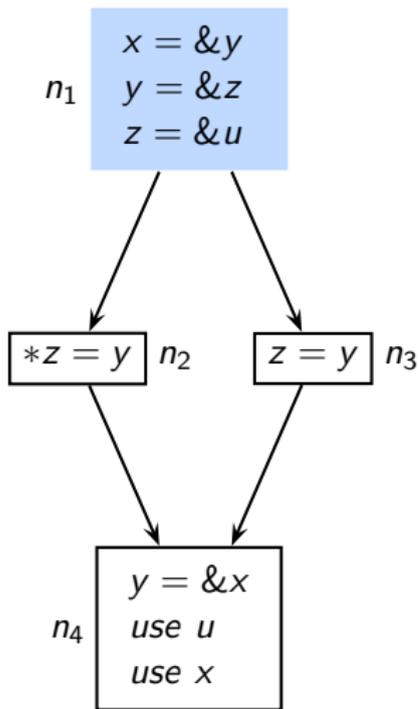
- Andersen's inclusion based wisdom:
 - ▶ Add edges and let the number of successors increase
- Steensgaard's equality based wisdom:
 - ▶ Merge multiple successors and maintain a single successor of any node
 - ▶ Since a larger number of pointers treated are alike and fewer distinctions are maintained, we get much smaller points-to graphs
 - ▶ Efficient *Union-Find* algorithms to merge intersecting subsets



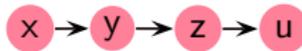
Inclusion Based (aka Andersen's) Points-to Analysis: Example 2



Inclusion Based (aka Andersen's) Points-to Analysis: Example 2



- x “points-to” y
- y “points-to” z
- z “points-to” u



Points-to Graph

Constraints on
Points-to Sets

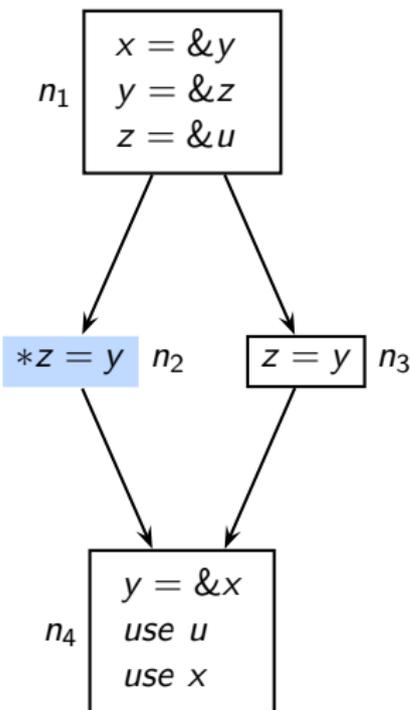
$$P_x \supseteq \{y\}$$

$$P_y \supseteq \{z\}$$

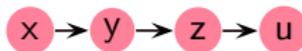
$$P_z \supseteq \{u\}$$



Inclusion Based (aka Andersen's) Points-to Analysis: Example 2



- Pointees of z should point to pointees of y also
- u should point to z



Points-to Graph

Constraints on
Points-to Sets

$$P_x \supseteq \{y\}$$

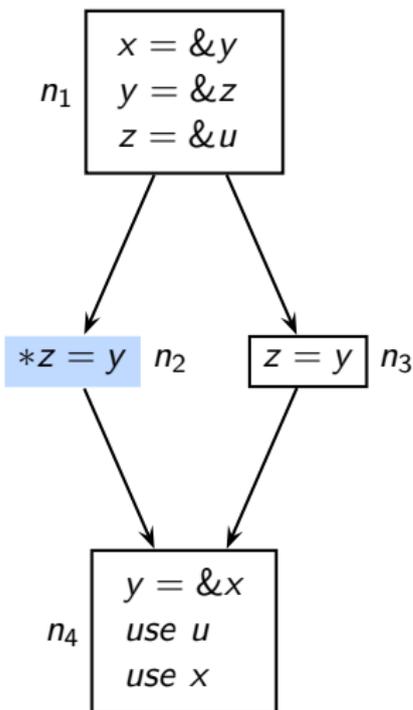
$$P_y \supseteq \{z\}$$

$$P_z \supseteq \{u\}$$

$$\forall w \in P_z, P_w \supseteq P_y$$



Inclusion Based (aka Andersen's) Points-to Analysis: Example 2



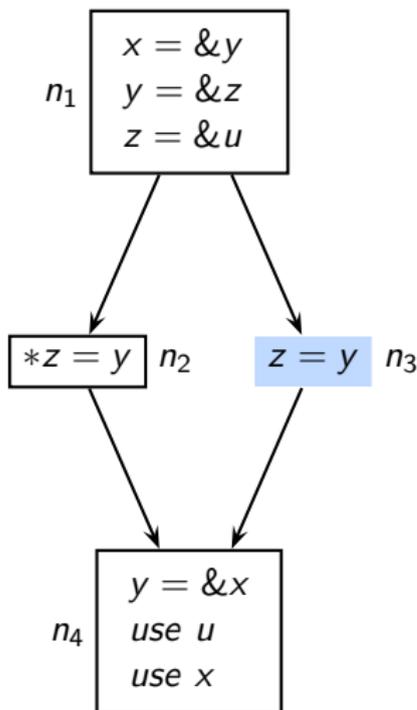
Points-to Graph

Constraints on
Points-to Sets

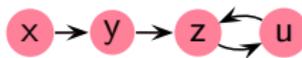
$$\begin{aligned}
 P_x &\supseteq \{y\} \\
 P_y &\supseteq \{z\} \\
 P_z &\supseteq \{u\} \\
 \forall w \in P_z, P_w &\supseteq P_y
 \end{aligned}$$



Inclusion Based (aka Andersen's) Points-to Analysis: Example 2



- z should point to pointees of y
- z should point to z



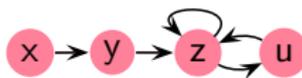
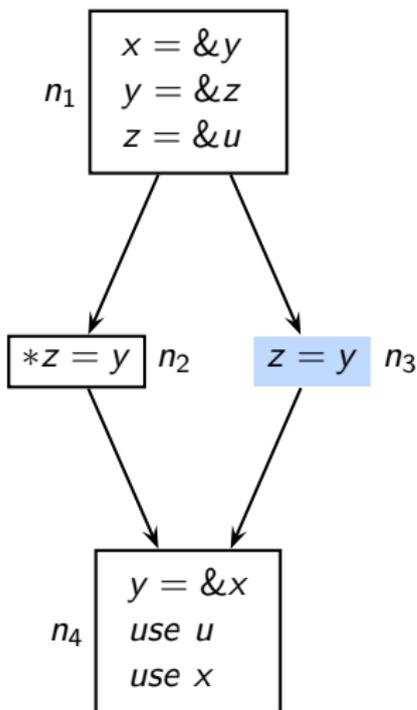
Points-to Graph

Constraints on
Points-to Sets

$$\begin{aligned}
 P_x &\supseteq \{y\} \\
 P_y &\supseteq \{z\} \\
 P_z &\supseteq \{u\} \\
 \forall w \in P_z, P_w &\supseteq P_y \\
 P_z &\supseteq P_y
 \end{aligned}$$



Inclusion Based (aka Andersen's) Points-to Analysis: Example 2



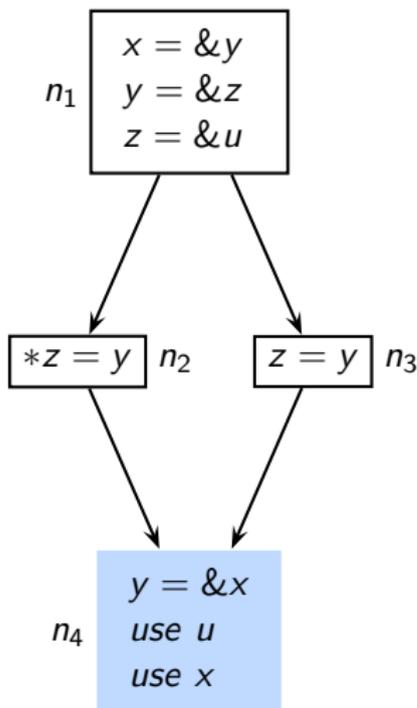
Points-to Graph

Constraints on
Points-to Sets

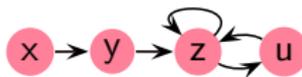
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 P_z &\supseteq \{u\} \\
 \forall w \in P_z, P_w &\supseteq P_y \\
 P_z &\supseteq P_y
 \end{aligned}$$



Inclusion Based (aka Andersen's) Points-to Analysis: Example 2



- y should point to x also



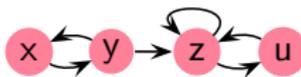
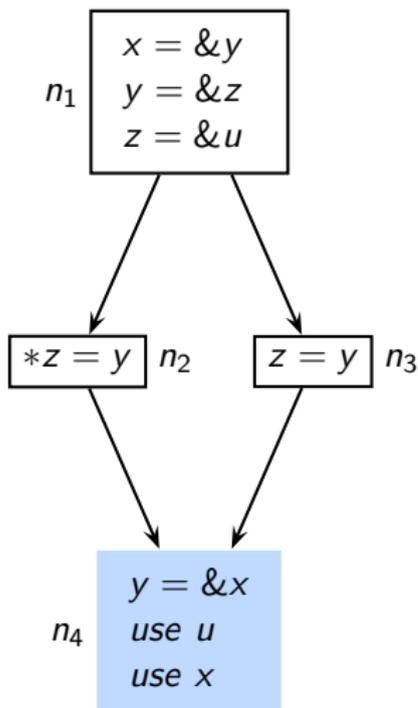
Points-to Graph

Constraints on
Points-to Sets

$$\begin{aligned}
 P_x &\supseteq \{y\} \\
 P_y &\supseteq \{z\} \\
 P_z &\supseteq \{u\} \\
 \forall w \in P_z, P_w &\supseteq P_y \\
 P_z &\supseteq P_y \\
 P_y &\supseteq \{x\}
 \end{aligned}$$



Inclusion Based (aka Andersen's) Points-to Analysis: Example 2



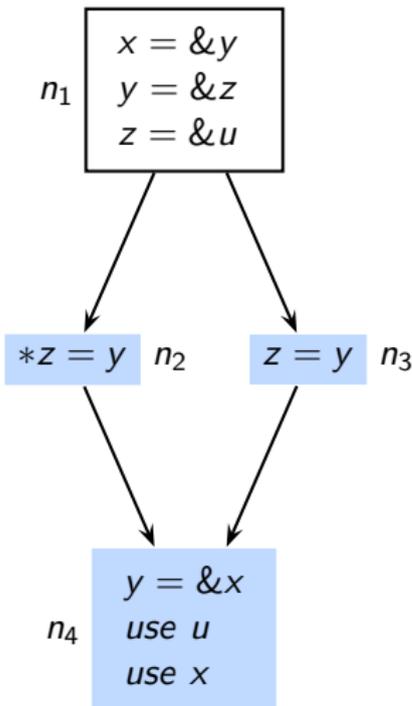
Points-to Graph

Constraints on
Points-to Sets

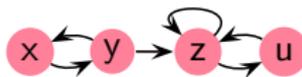
$$\begin{aligned}
 P_x &\supseteq \{y\} \\
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 P_z &\supseteq P_y \\
 P_y &\supseteq \{x\}
 \end{aligned}$$



Inclusion Based (aka Andersen's) Points-to Analysis: Example 2



- z and its pointees should point to new pointee of y also
- u and z should point to x



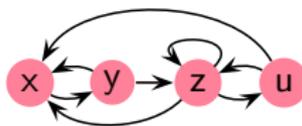
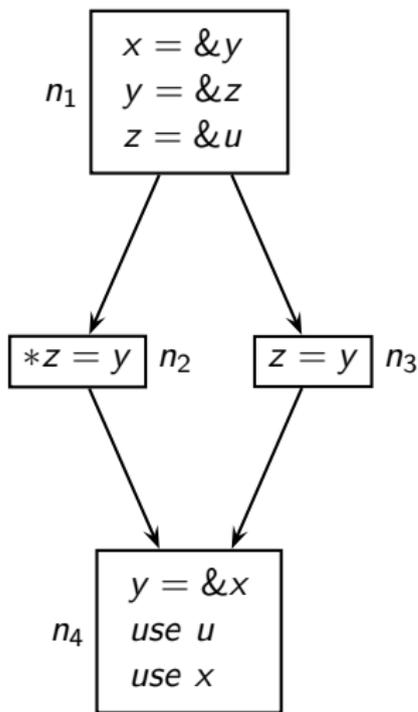
Points-to Graph

Constraints on
Points-to Sets

$$\begin{aligned}
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 \forall w \in P_z, P_w &\supseteq P_y \\
 P_z &\supseteq P_y \\
 P_y &\supseteq \{x\}
 \end{aligned}$$



Inclusion Based (aka Andersen's) Points-to Analysis: Example 2



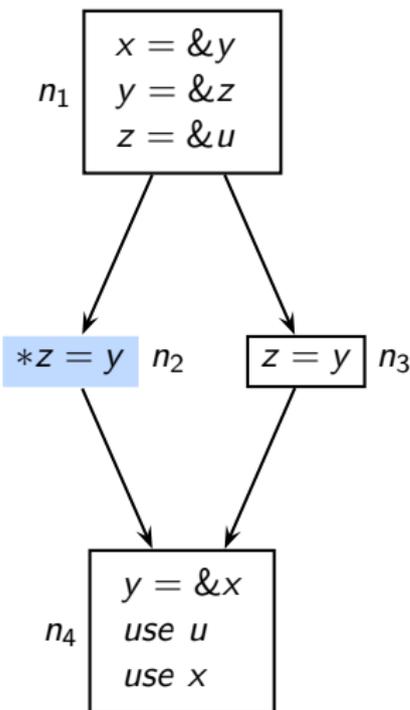
Points-to Graph

Constraints on
Points-to Sets

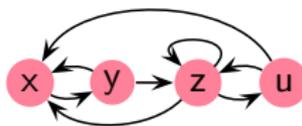
$$\begin{aligned}
 P_x &\supseteq \{y\} \\
 P_y &\supseteq \{z\} \\
 P_z &\supseteq \{u\} \\
 \forall w \in P_z, P_w &\supseteq P_y \\
 P_z &\supseteq P_y \\
 P_y &\supseteq \{x\}
 \end{aligned}$$



Inclusion Based (aka Andersen's) Points-to Analysis: Example 2



- Pointees of z should point to pointees of y
- x should point to itself and z



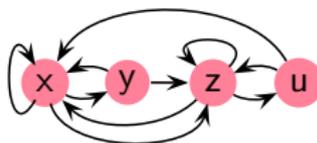
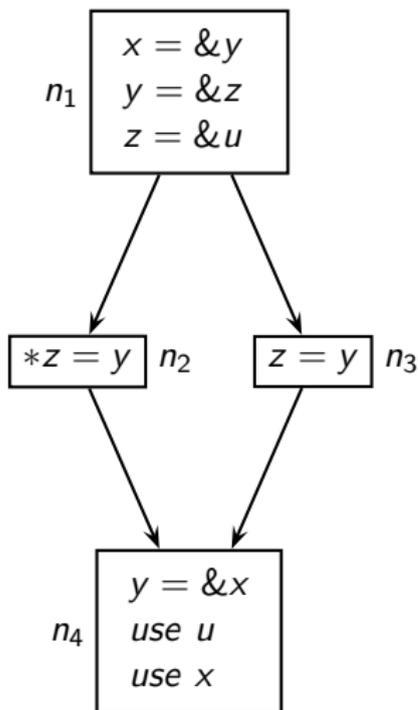
Points-to Graph

Constraints on
Points-to Sets

$$\begin{aligned}
 P_x &\supseteq \{y\} \\
 P_y &\supseteq \{z\} \\
 P_z &\supseteq \{u\} \\
 \forall w \in P_z, P_w &\supseteq P_y \\
 P_z &\supseteq P_y \\
 P_y &\supseteq \{x\}
 \end{aligned}$$



Inclusion Based (aka Andersen's) Points-to Analysis: Example 2



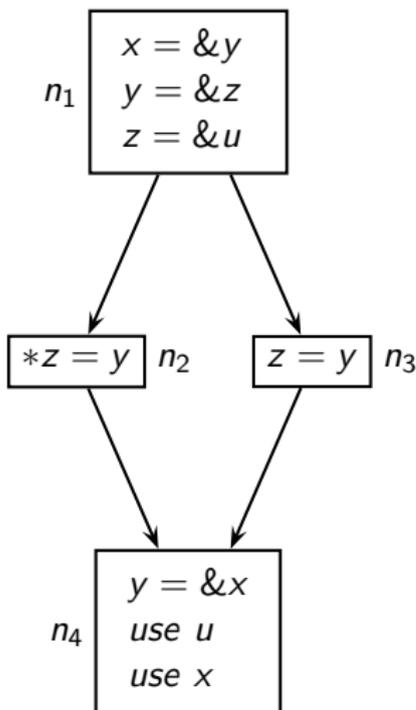
Points-to Graph

Constraints on
Points-to Sets

$$\begin{aligned}
 P_x &\supseteq \{y\} \\
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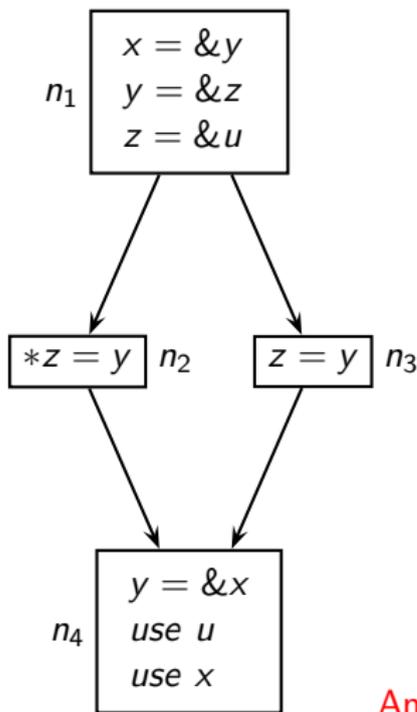
Equality Based (aka Steensgaard's) Points-to Analysis: Example 2



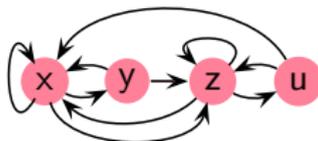
- Treat all pointees of a pointer as “equivalent” locations
- Transitive closure
Pointees of all equivalent locations become equivalent



Equality Based (aka Steensgaard's) Points-to Analysis: Example 2



- Treat all pointees of a pointer as “equivalent” locations
- Transitive closure
Pointees of all equivalent locations become equivalent



Andersen's Points-to Graph

Effective additional constraints

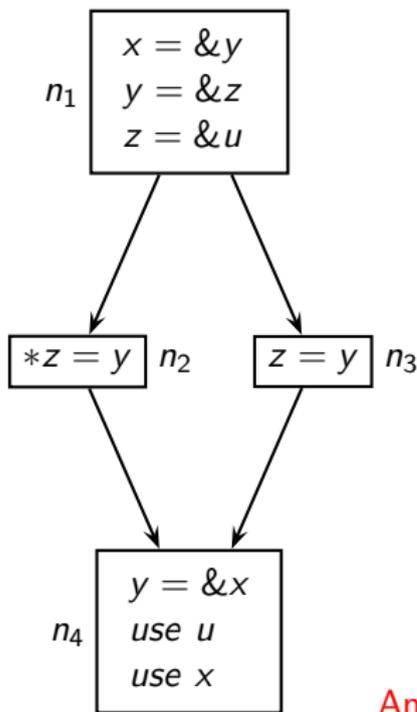
$$\frac{\text{Unify}(x, y)}{\text{/* pointees of } x \text{ */}}$$

$$\frac{\text{Unify}(x, z)}{\text{/* pointees of } y \text{ */}}$$

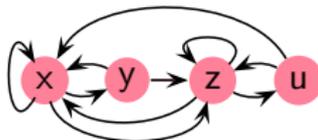
$$\frac{\text{Unify}(x, u)}{\text{/* pointees of } z \text{ */}}$$



Equality Based (aka Steensgaard's) Points-to Analysis: Example 2



- Treat all pointees of a pointer as “equivalent” locations
- Transitive closure
Pointees of all equivalent locations become equivalent



Andersen's Points-to Graph

Effective additional constraints

$$\frac{\text{Unify}(x, y)}{\text{/* pointees of } x \text{ */}}$$

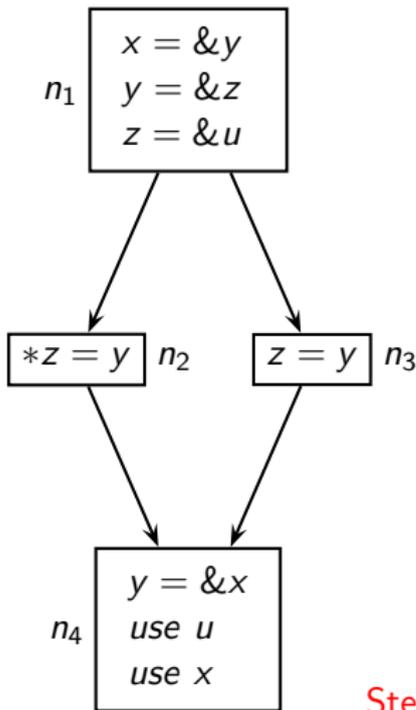
$$\frac{\text{Unify}(x, z)}{\text{/* pointees of } y \text{ */}}$$

$$\frac{\text{Unify}(x, u)}{\text{/* pointees of } z \text{ */}}$$

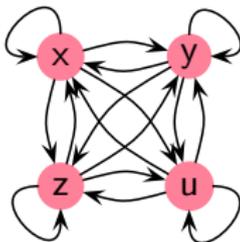
$\Rightarrow x, y, z, u$ are equivalent



Equality Based (aka Steensgaard's) Points-to Analysis: Example 2



- Treat all pointees of a pointer as “equivalent” locations
- Transitive closure
Pointees of all equivalent locations become equivalent



Steensgaard's Points-to Graph

Effective additional constraints

$Unify(x, y)$

$/*$ pointees of x $*/$

$Unify(x, z)$

$/*$ pointees of y $*/$

$Unify(x, u)$

$/*$ pointees of z $*/$

$\Rightarrow x, y, z, u$ are equivalent

\Rightarrow Complete graph



Tutorial Problem for Flow Insensitive Pointer Analysis (1)

Program

$p = \&q$

$r = \&s$

$t = \&p$

$u = p$

$*t = r$

Inclusion based

Equality based

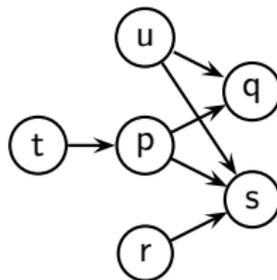


Tutorial Problem for Flow Insensitive Pointer Analysis (1)

Program

```
p = &q  
r = &s  
t = &p  
u = p  
*t = r
```

Inclusion based



Equality based

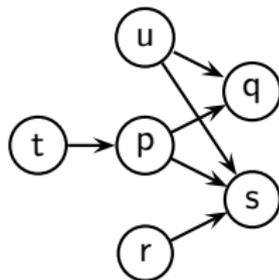


Tutorial Problem for Flow Insensitive Pointer Analysis (1)

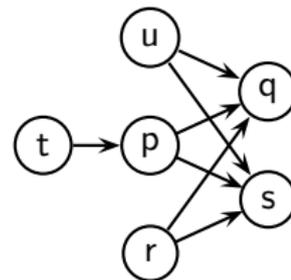
Program

```
p = &q  
r = &s  
t = &p  
u = p  
*t = r
```

Inclusion based



Equality based

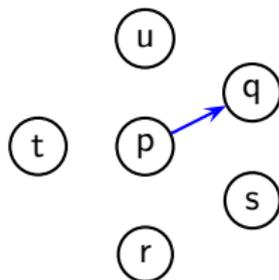


Tutorial Problem for Flow Insensitive Pointer Analysis (1)

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p = &q  
r = &s  
t = &p  
u = p  
*t = r
```

Inclusion based



Equality based

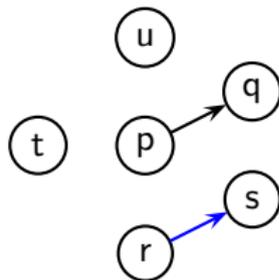


Tutorial Problem for Flow Insensitive Pointer Analysis (1)

Program

```
p = &q  
r = &s  
t = &p  
u = p  
*t = r
```

Inclusion based



Equality based

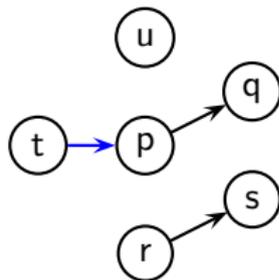


Tutorial Problem for Flow Insensitive Pointer Analysis (1)

Program

```
p = &q  
r = &s  
t = &p  
u = p  
*t = r
```

Inclusion based



Equality based

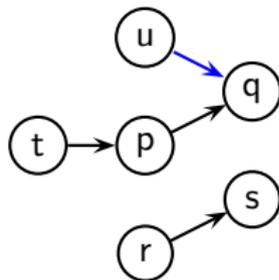


Tutorial Problem for Flow Insensitive Pointer Analysis (1)

Program

```
p = &q  
r = &s  
t = &p  
u = p  
*t = r
```

Inclusion based



Equality based

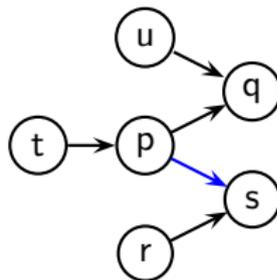


Tutorial Problem for Flow Insensitive Pointer Analysis (1)

Program

```
p = &q  
r = &s  
t = &p  
u = p  
*t = r
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Inclusion based



Equality based

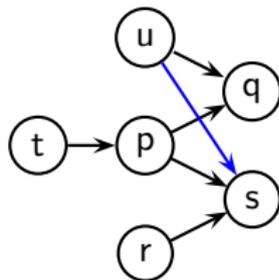


Tutorial Problem for Flow Insensitive Pointer Analysis (1)

Program

```
p = &q  
r = &s  
t = &p  
u = p  
*t = r
```

Inclusion based



Equality based

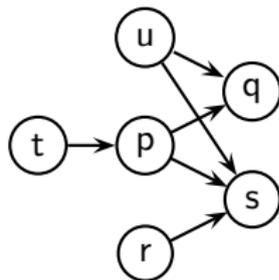


Tutorial Problem for Flow Insensitive Pointer Analysis (1)

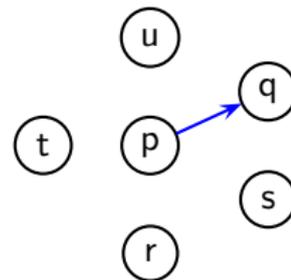
Program

```
p = &q  
r = &s  
t = &p  
u = p  
*t = r
```

Inclusion based



Equality based

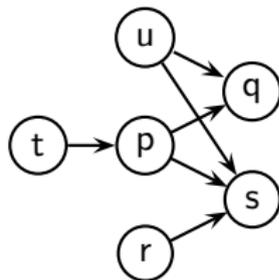


Tutorial Problem for Flow Insensitive Pointer Analysis (1)

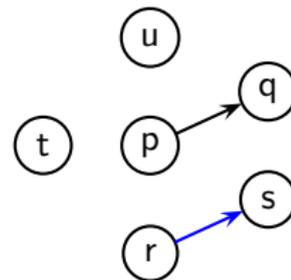
Program

```
p = &q  
r = &s  
t = &p  
u = p  
*t = r
```

Inclusion based



Equality based

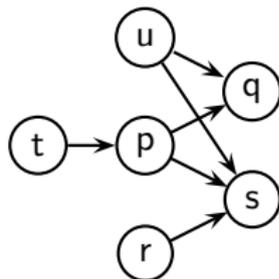


Tutorial Problem for Flow Insensitive Pointer Analysis (1)

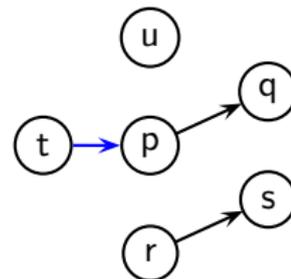
Program

```
p = &q  
r = &s  
t = &p  
u = p  
*t = r
```

Inclusion based



Equality based

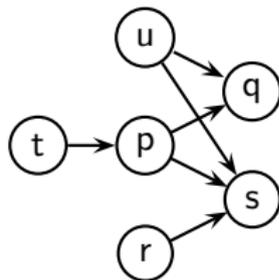


Tutorial Problem for Flow Insensitive Pointer Analysis (1)

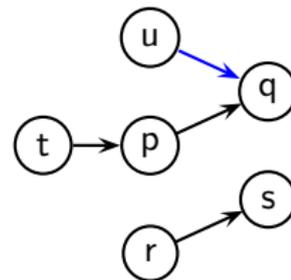
Program

```
p = &q  
r = &s  
t = &p  
u = p  
*t = r
```

Inclusion based



Equality based

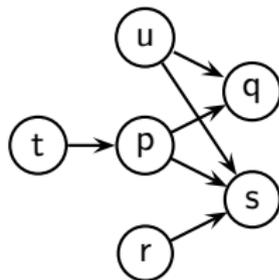


Tutorial Problem for Flow Insensitive Pointer Analysis (1)

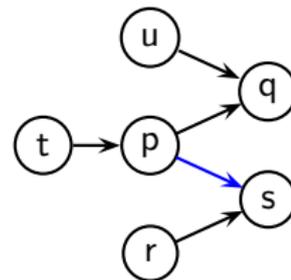
Program

```
p = &q  
r = &s  
t = &p  
u = p  
*t = r
```

Inclusion based



Equality based

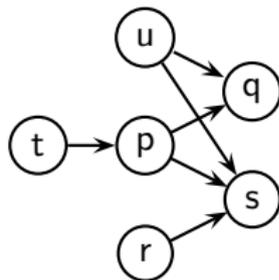


Tutorial Problem for Flow Insensitive Pointer Analysis (1)

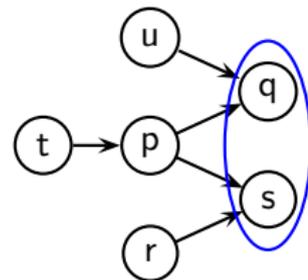
Program

```
p = &q  
r = &s  
t = &p  
u = p  
*t = r
```

Inclusion based



Equality based

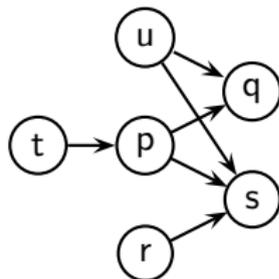


Tutorial Problem for Flow Insensitive Pointer Analysis (1)

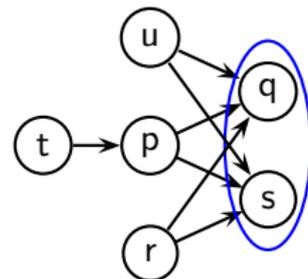
Program

```
p = &q  
r = &s  
t = &p  
u = p  
*t = r
```

Inclusion based



Equality based

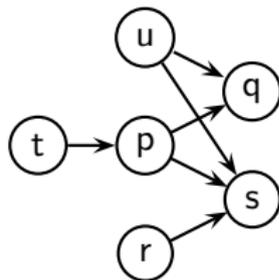


Tutorial Problem for Flow Insensitive Pointer Analysis (1)

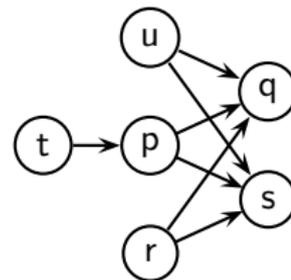
Program

```
p = &q  
r = &s  
t = &p  
u = p  
*t = r
```

Inclusion based



Equality based



Tutorial Problems for Flow Insensitive Pointer Analysis (2)

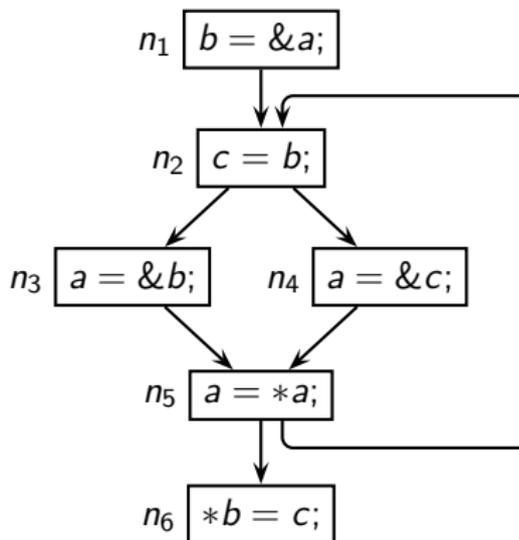
Compute flow insensitive points-to information using inclusion based method as well as equality based method

```
if (...)
    p = &x;
else
    p = &y;
x = &a;
y = &b;
*p = &c;
*y = &a;
```



Tutorial Problem for Flow Insensitive Pointer Analysis (3)

Compute flow insensitive points-to information using inclusion based method as well as equality based method

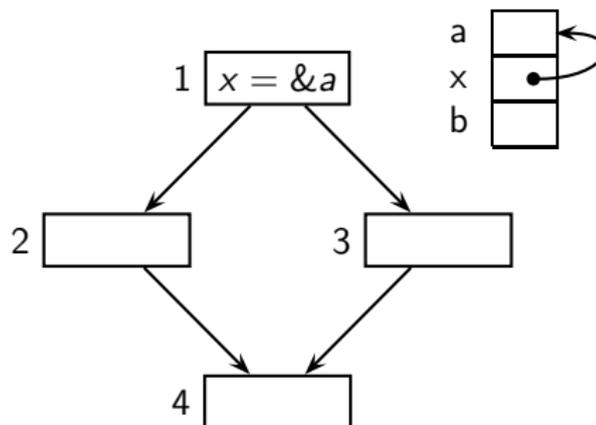


An Outline of Pointer Analysis Coverage

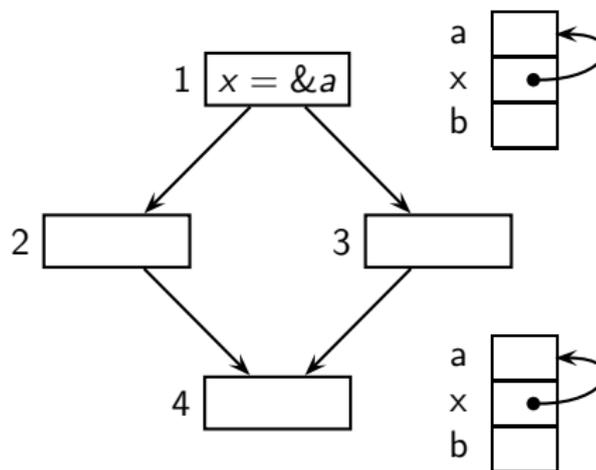
- The larger perspective
- Comparing Points-to and Alias information
- Flow Insensitive Points-to Analysis
- Flow Sensitive Points-to Analysis **Next Topic**
- Pointer Analyses: An Engineer's Landscape
- Liveness Based Points-to Analysis
- Generalizations to Heap, Arrays, Pointer Arithmetic, and Unions



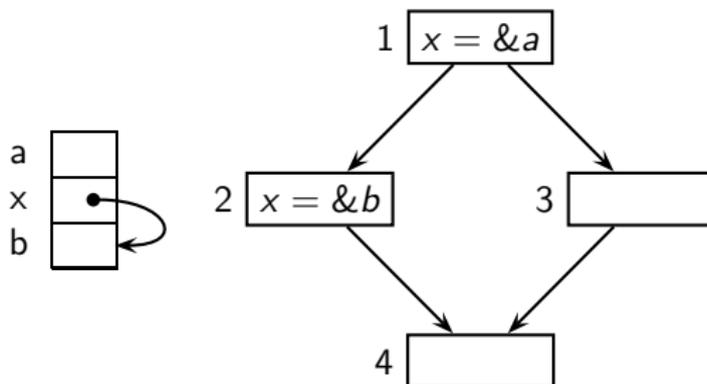
Must Points-to Information



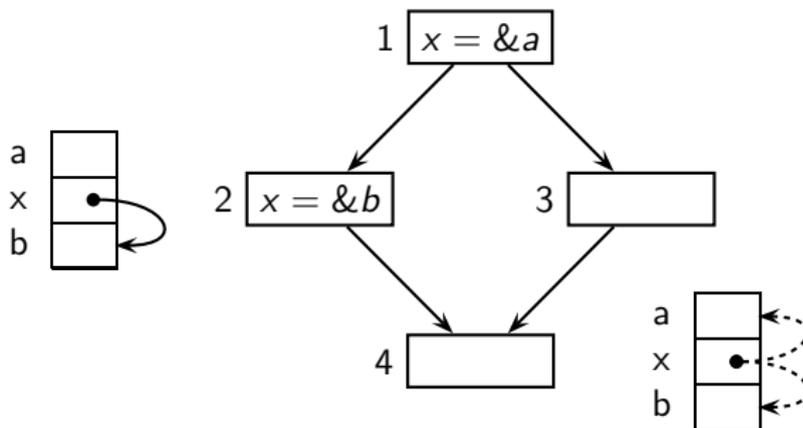
Must Points-to Information



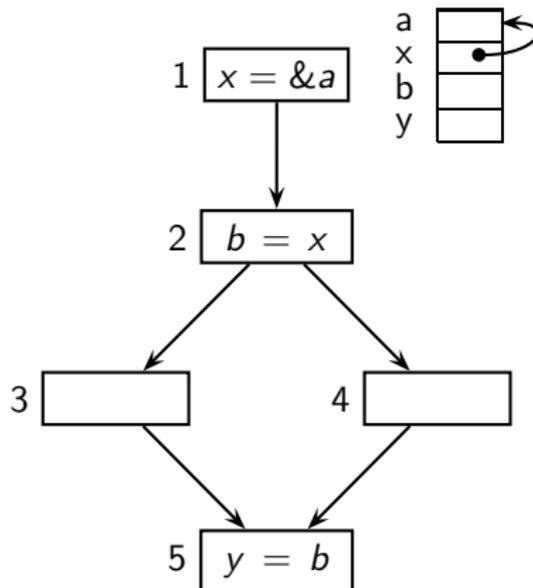
May Points-to Information



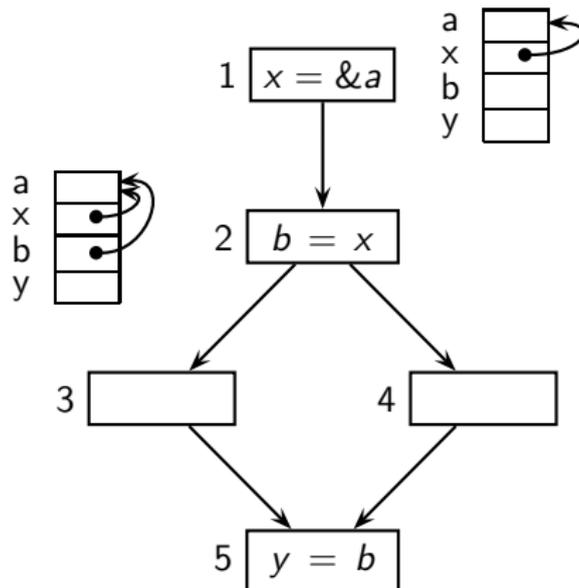
May Points-to Information



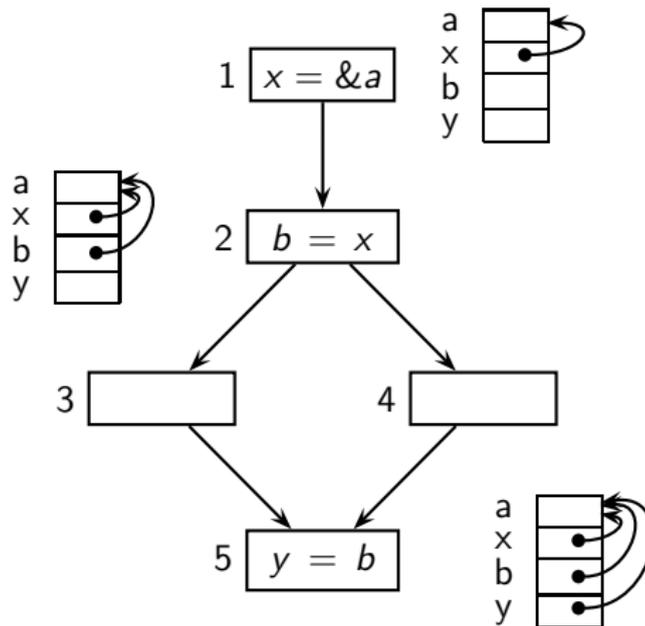
Must Alias Information



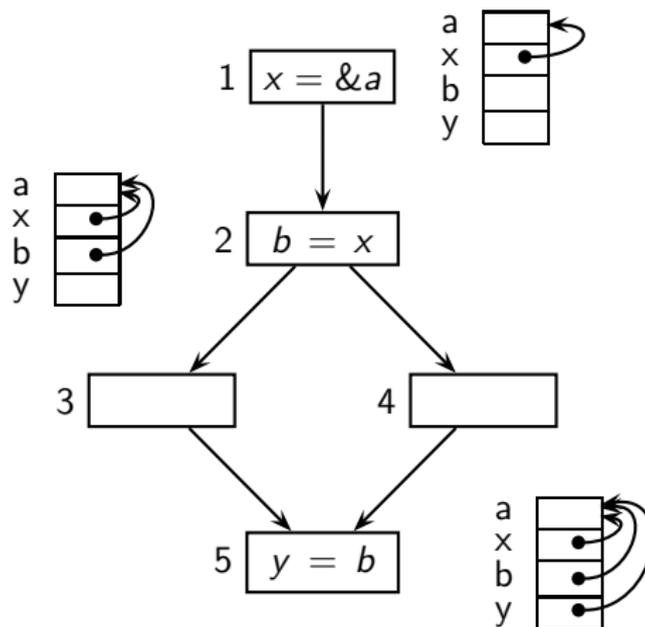
Must Alias Information



Must Alias Information



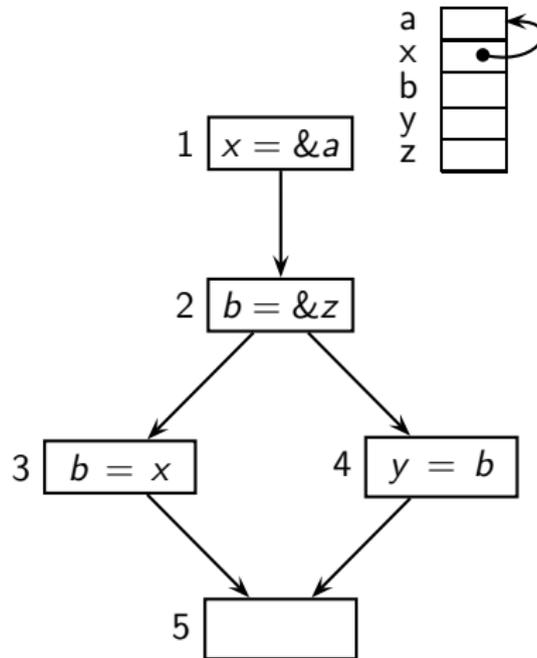
Must Alias Information



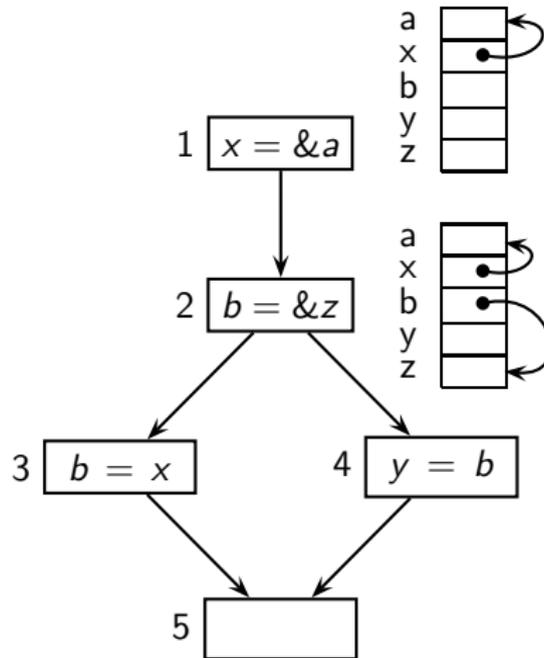
$$x \overset{\circ}{=} b \text{ and } b \overset{\circ}{=} y \Rightarrow x \overset{\circ}{=} y$$



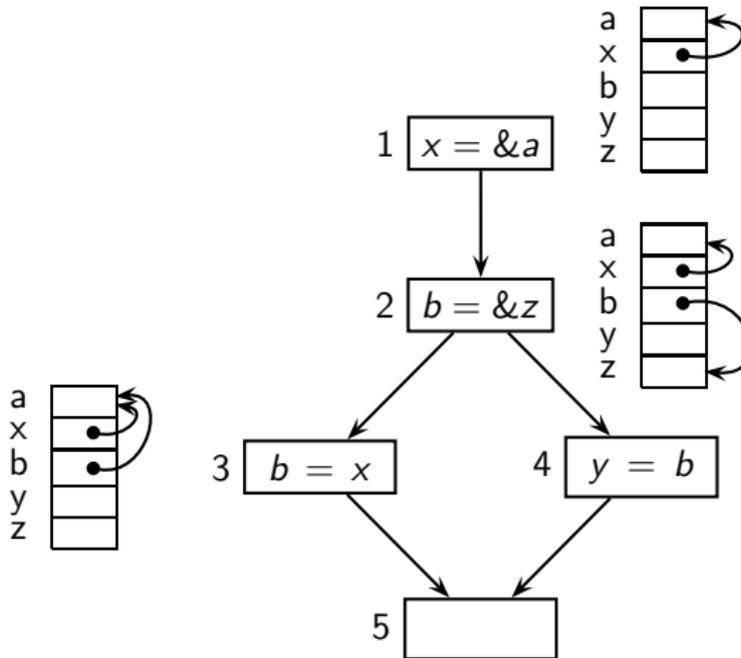
May Alias Information



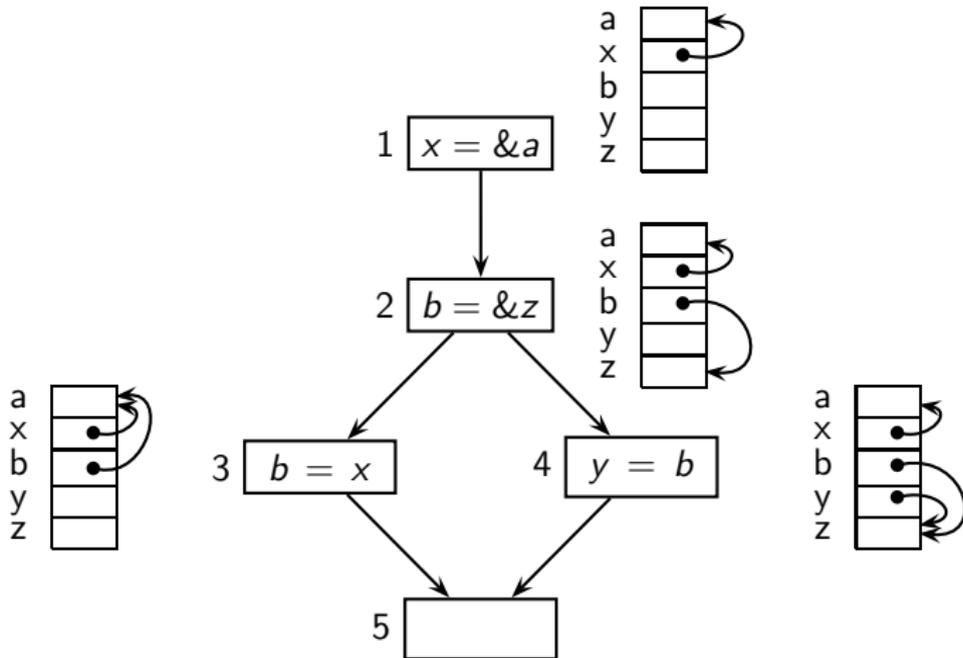
May Alias Information



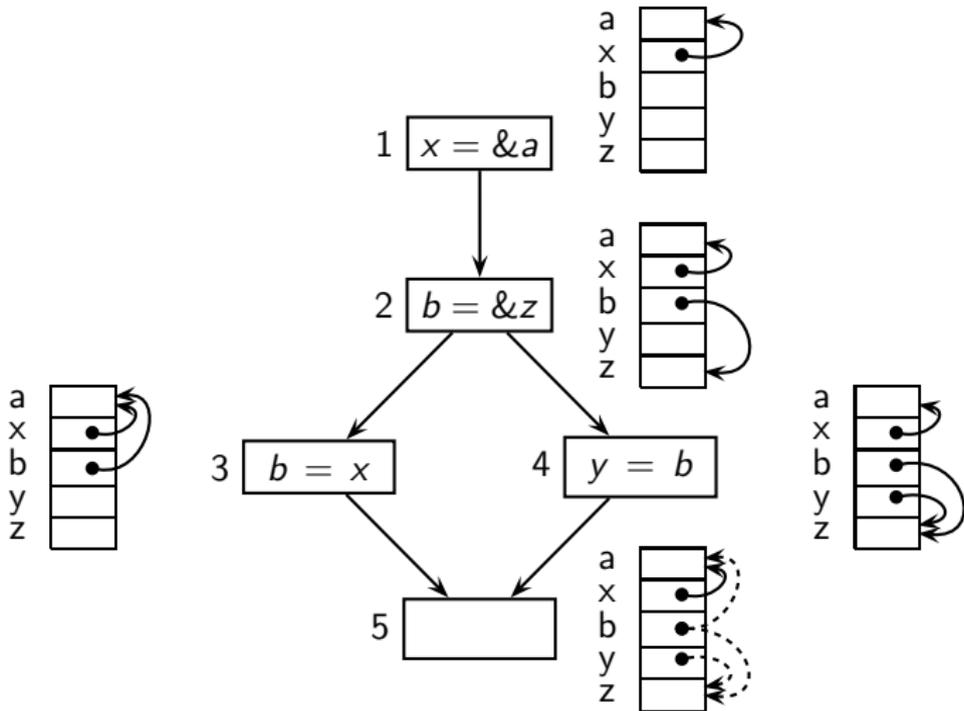
May Alias Information



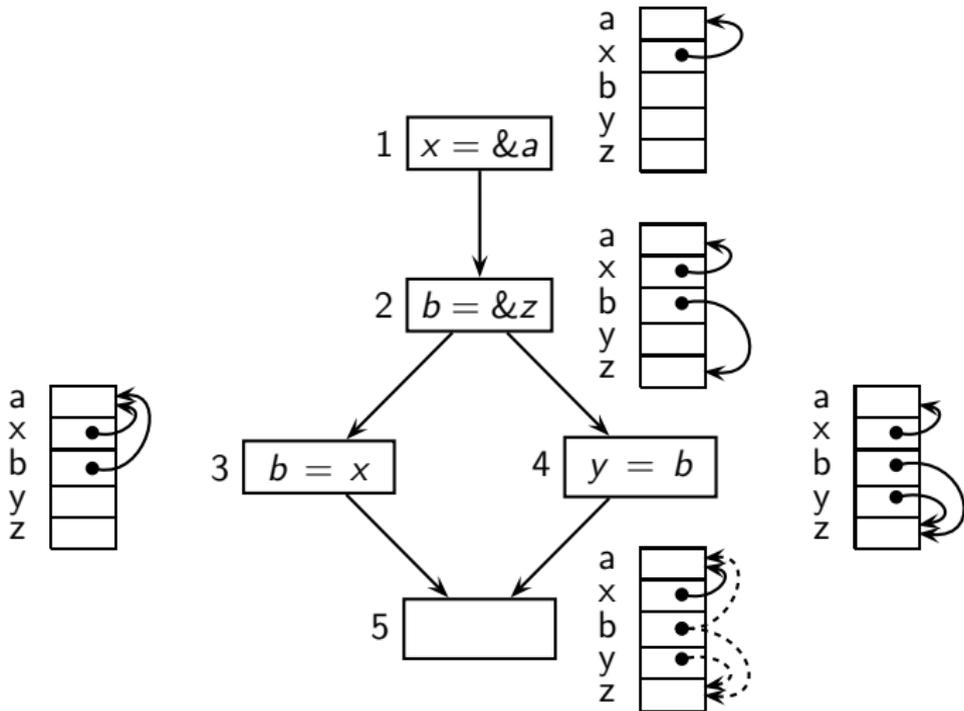
May Alias Information



May Alias Information

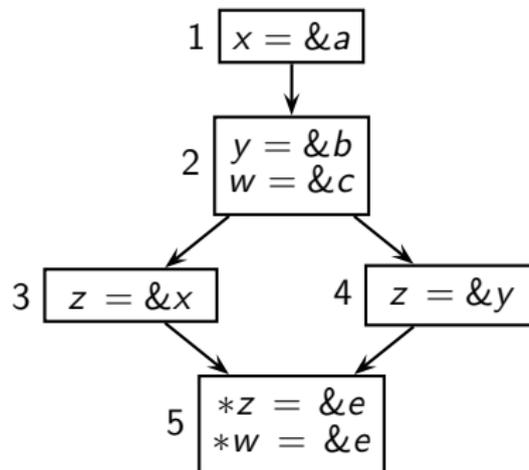


May Alias Information

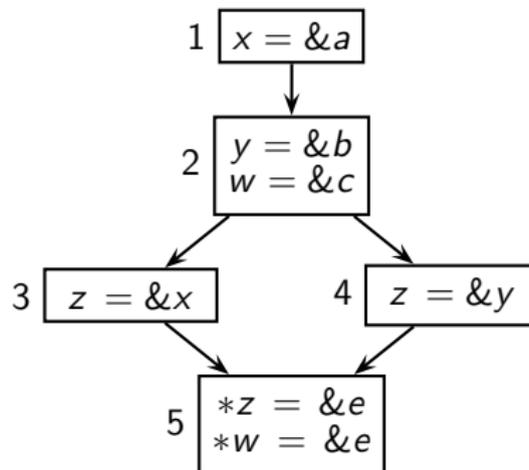


$$x \doteq b \text{ and } b \doteq y \not\Rightarrow x \doteq y$$

Strong and Weak Updates



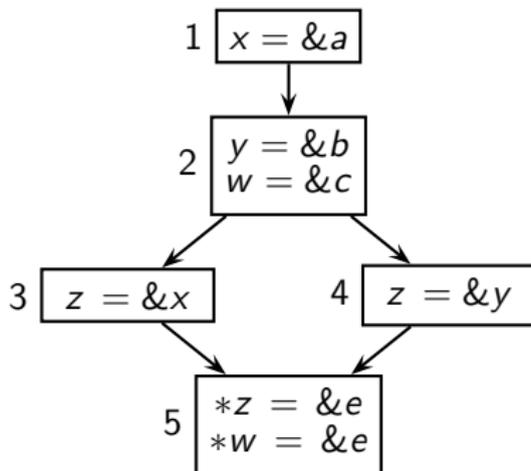
Strong and Weak Updates



Weak update: Modification of x or y due to $*z$ in block 5



Strong and Weak Updates

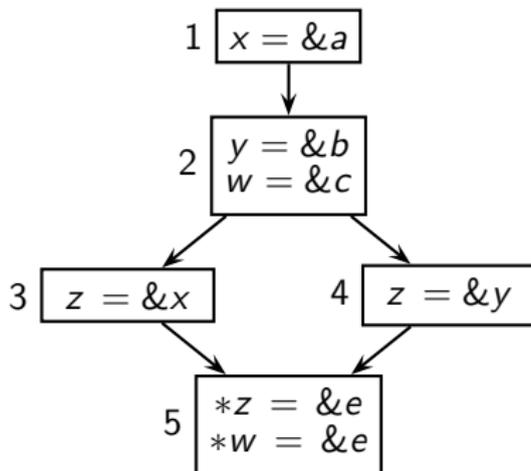


Weak update: Modification of x or y due to $*z$ in block 5

Strong update: Modification of c due to $*w$ in block 5



Strong and Weak Updates



Weak update: Modification of x or y due to $*z$ in block 5

Strong update: Modification of c due to $*w$ in block 5

How is this concept related to May/Must nature of information?



What About Heap Data?

- Compile time entities, abstract entities, or summarized entities
- Three options:
 - ▶ Represent all heap locations by a single abstract heap location
 - ▶ Represent all heap locations of a particular type by a single abstract heap location
 - ▶ Represent all heap locations allocated at a given memory allocation site by a single abstract heap location
- Summarization: Usually based on the length of pointer expression
- *Initially, we will restrict ourselves to stack and static data*
We will later introduce heap using the allocation site based abstraction



Lattice for May Points-to Analysis

Let $\mathbf{P} \subseteq \mathbb{V}\text{ar}$ be the set of pointers. Assume $\mathbb{V}\text{ar} = \{p, q\}$ and $\mathbf{P} = \{p\}$

Product View

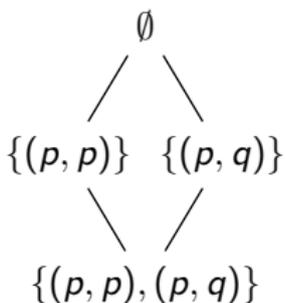
Mapping view



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Product View



Mapping view

Data flow values \subseteq $\mathbf{P} \times \mathbb{V}\text{ar}$

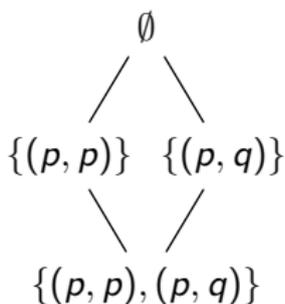
Lattice = $(2^{\mathbf{P} \times \mathbb{V}\text{ar}}, \supseteq)$



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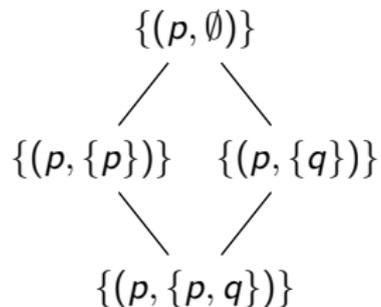
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Data flow values \in $\mathbf{P} \rightarrow 2^{\mathbb{V}\text{ar}}$

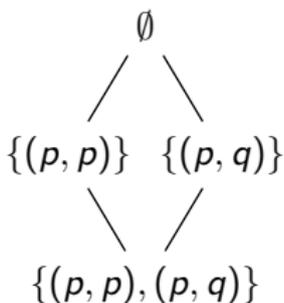
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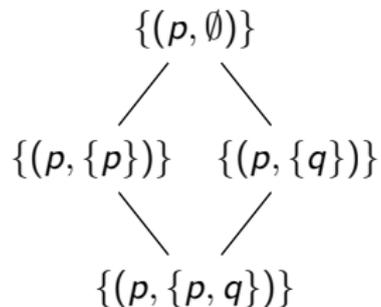


Data flow values $\subseteq \mathbf{P} \times \text{Var}$

Lattice = $(2^{\mathbf{P} \times \text{Var}}, \supseteq)$

Points-to graph as a list of directed edges

Mapping view



Data flow values $\in \mathbf{P} \rightarrow 2^{\text{Var}}$

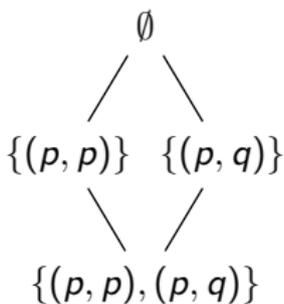
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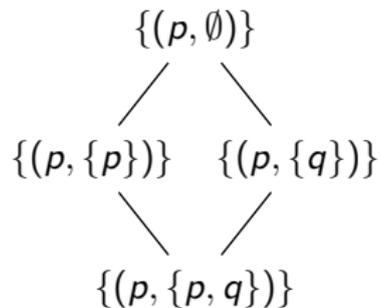


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Points-to graph as a
list of adjacency lists

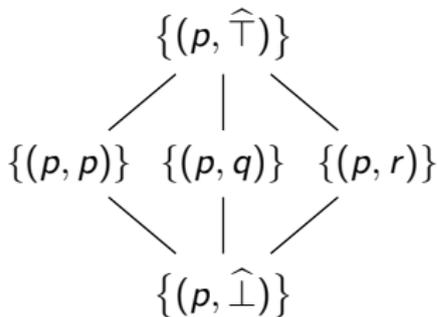


Lattice for Must Points-to Analysis

Let $\mathbf{P} \subseteq \mathbb{V}\text{ar}$ be the set of pointers. Assume $\mathbb{V}\text{ar} = \{p, q, r\}$ and $\mathbf{P} = \{p\}$

Mapping View

Set View



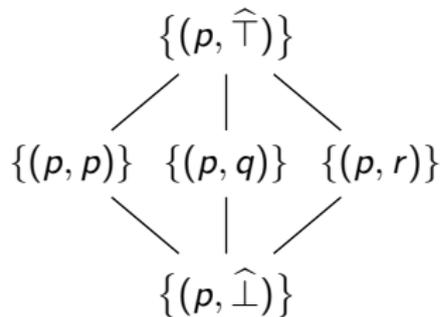
A pointer can point to at most one location



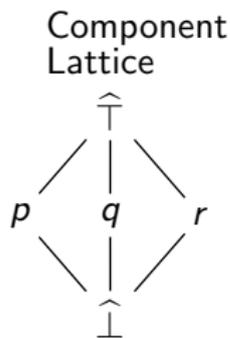
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Mapping View



Set View



Data flow values = $\mathbf{P} \rightarrow \mathbb{V}\text{ar} \cup \{\hat{\top}, \hat{\perp}\}$

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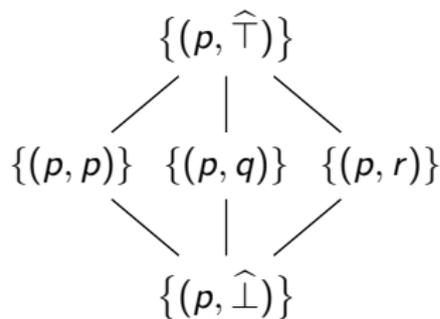
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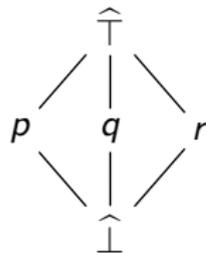
Mapping View



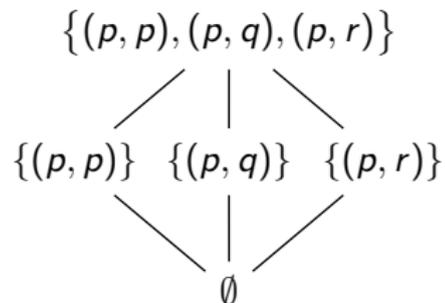
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Component Lattice



Set View



Restricted subset of $\mathbf{P} \times \text{Var}$

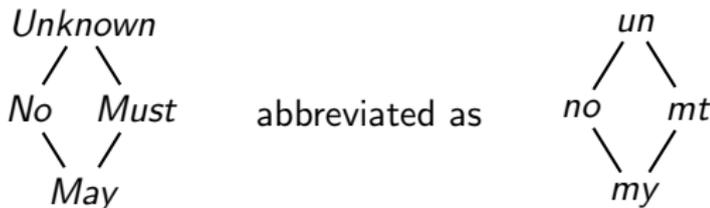
\cap can be used for \sqcap

A pointer can point to at most one location



Lattice for Combined May-Must Points-to Analysis (1)

- Consider the following abbreviation of the May-Must lattice \widehat{L}



- For $\mathbb{V}\text{ar} = \{p, q\}$, $\mathbf{P} = \{p\}$, the May-Must points-to lattice is the product

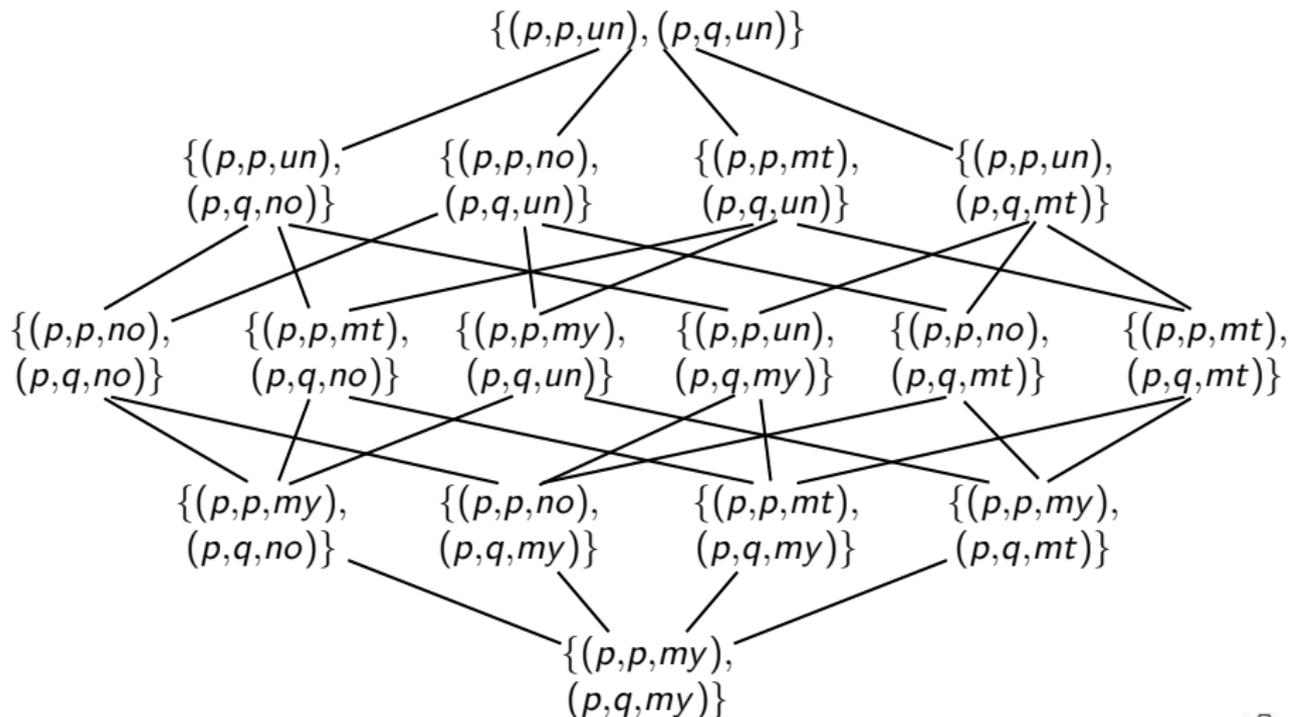
$$\mathbf{P} \times \mathbb{V}\text{ar} \times \widehat{L}$$

- ▶ Some elements are prohibited because of the semantics of *Must*
- ▶ If we have (p, p, mt) in a data flow value $X \in \mathbf{P} \times \mathbb{V}\text{ar} \times \widehat{L}$, then
 - ▶ we cannot have (p, q, un) , (p, q, mt) , or (p, q, my) in X
 - ▶ we can only have (p, q, no) in X



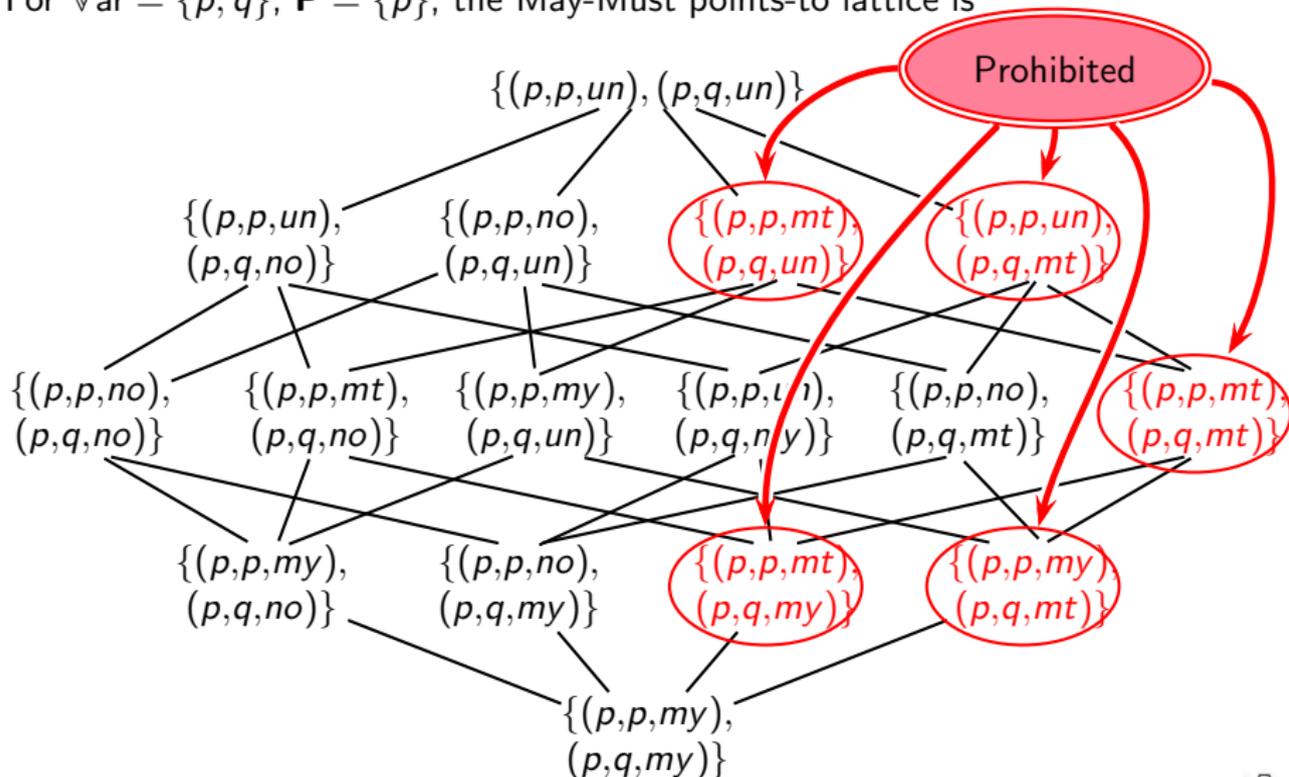
Lattice for Combined May-Must Points-to Analysis (2)

For $\text{Var} = \{p, q\}$, $\mathbf{P} = \{p\}$, the May-Must points-to lattice is



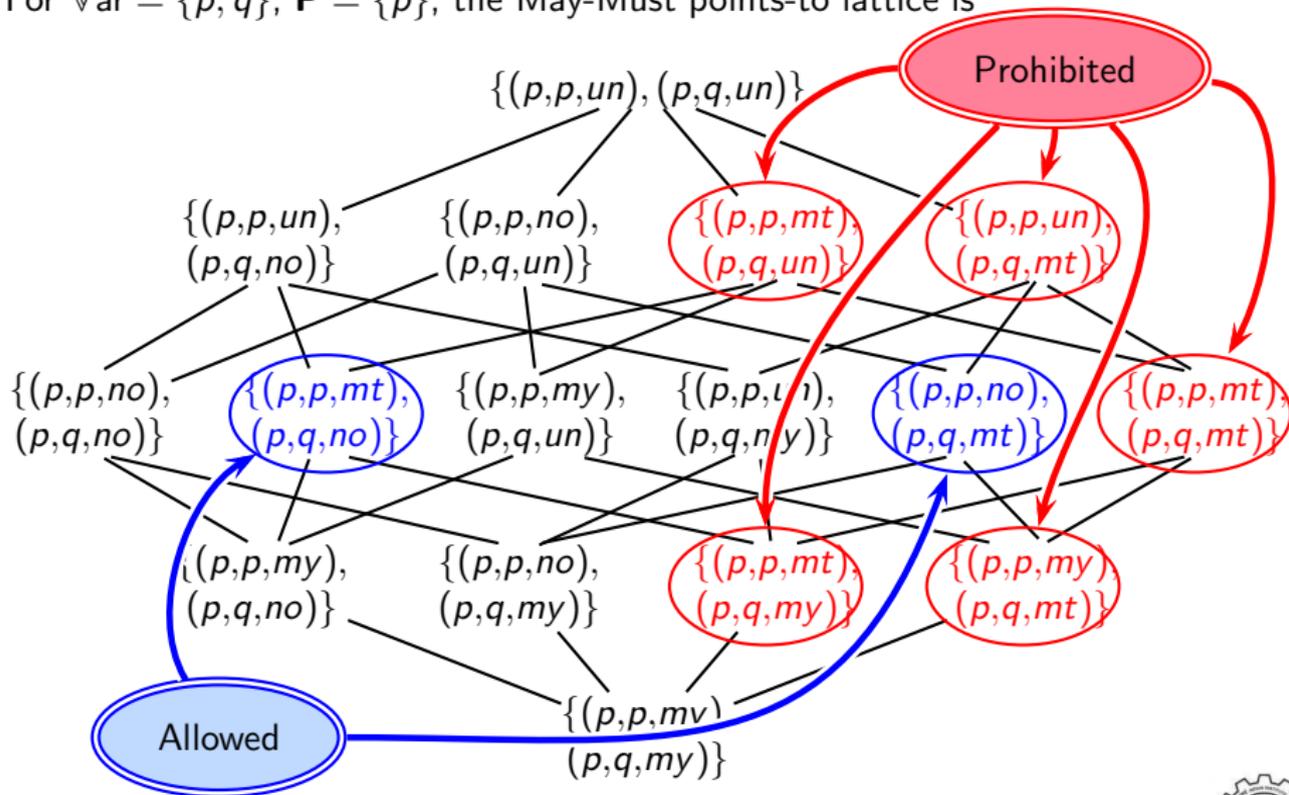
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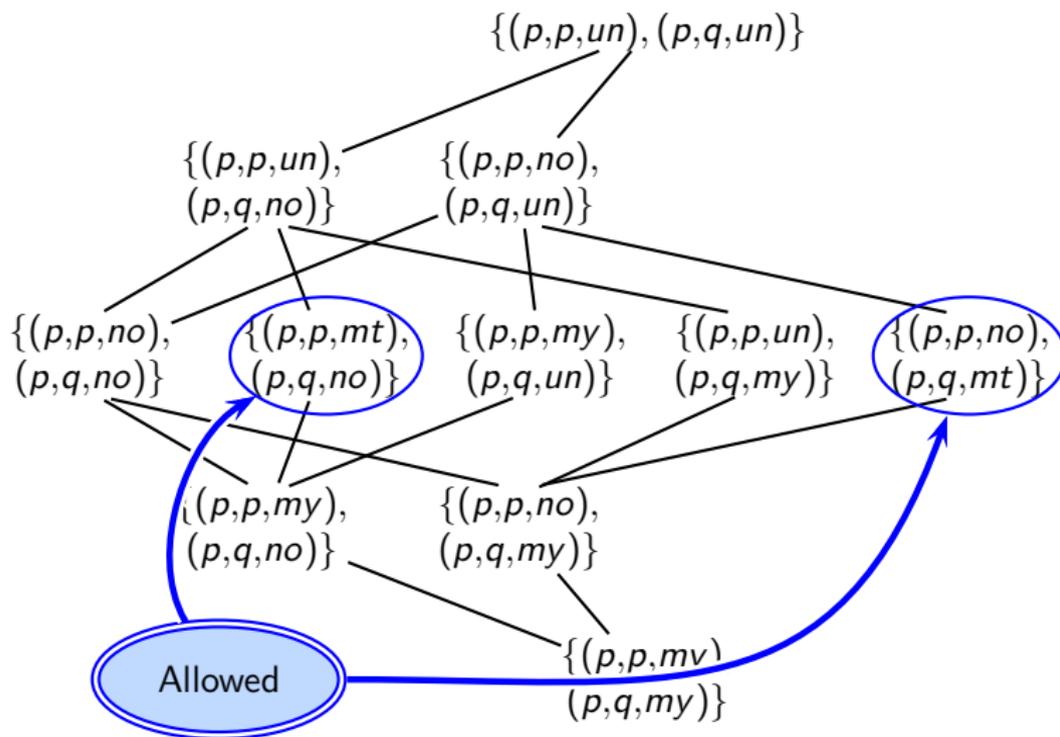
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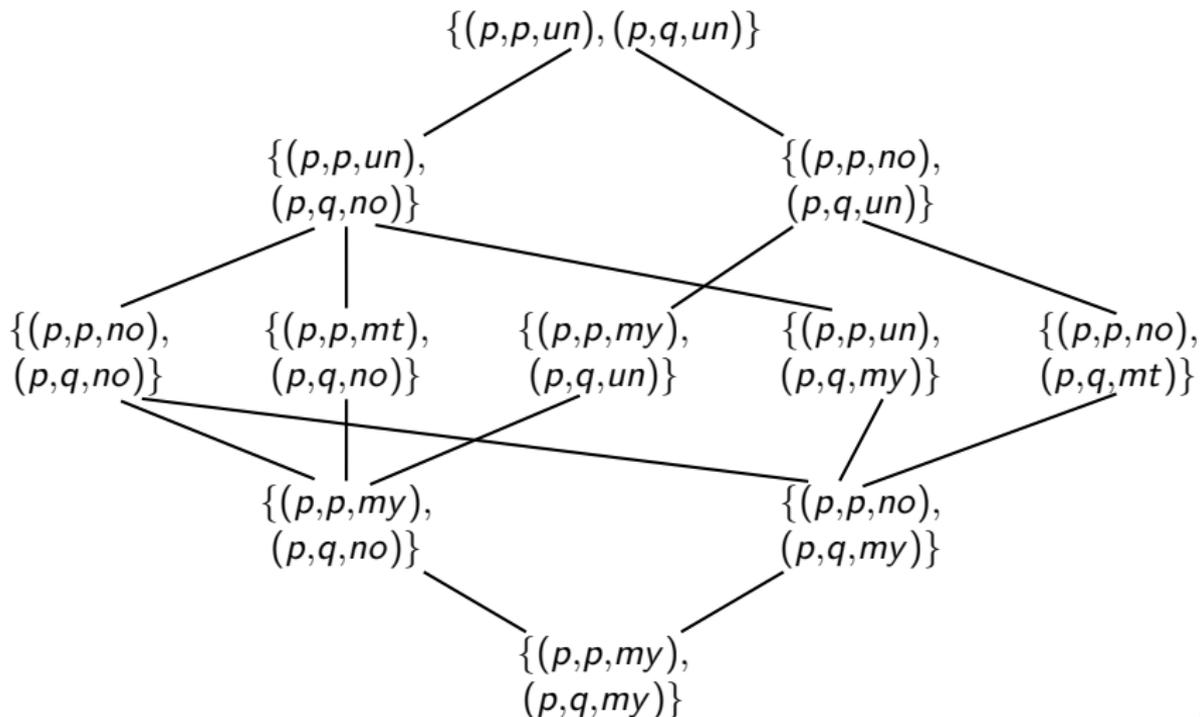
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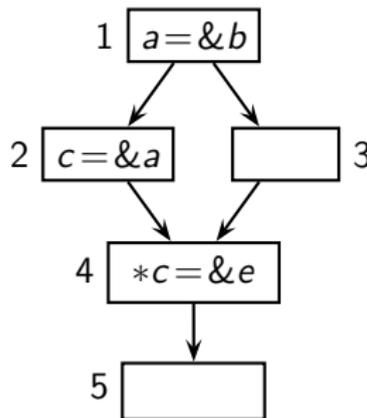
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May and Must Analysis for Killing Points-to Information (1)

May Points-to Analysis

Must Points-to Analysis

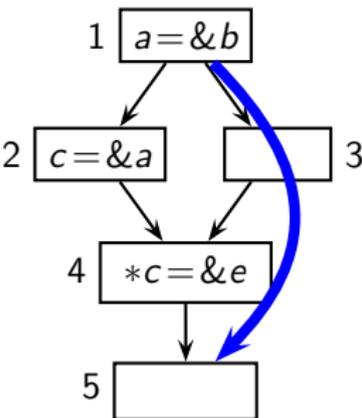


May and Must Analysis for Killing Points-to Information (1)

May Points-to Analysis

- (a, b) should be in $MayIn_5$
Holds along path 1-3-4
- Block 4 should not kill (a, b)
- Possible if pointee set of c is \emptyset
- However, $MayIn_4$ contains (c, a)

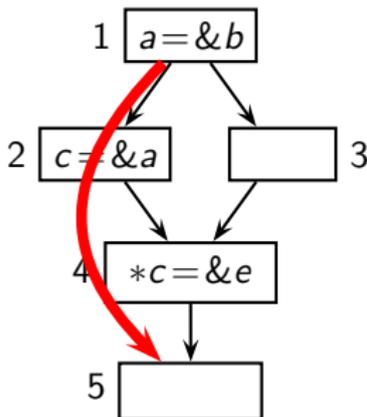
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Must Points-to Analysis

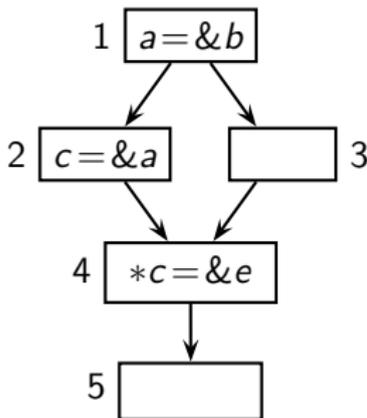
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May and Must Analysis for Killing Points-to Information (1)

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- (a, b) should be in $MayIn_5$
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- However, $MayIn_4$ contains (c, a)



Must Points-to Analysis

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Does not hold along path 1-2-4
- Block 4 should kill (a, b)
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- However, $MustIn_4$ contains (a, b)

For killing points-to information through indirection,

- **Must** points-to analysis should identify pointees of c using $MayIn_4$
- **May** points-to analysis should identify pointees of c using $MustIn_4$



May and Must Analysis for Killing Points-to Information (2)

- May Points-to analysis should remove a May points-to pair
 - ▶ only if it must be removed along all paths

Kill should remove only strong updates

⇒ should use Must Points-to information

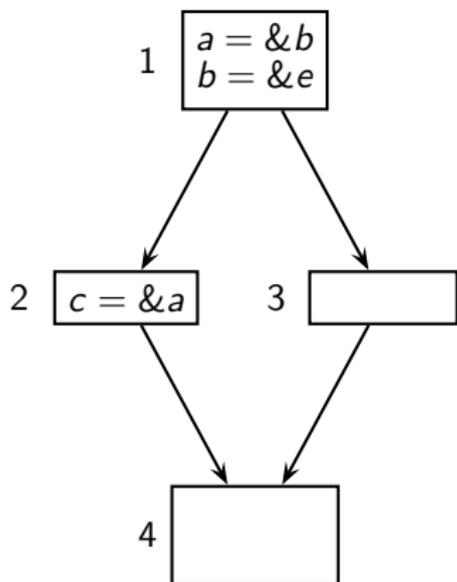
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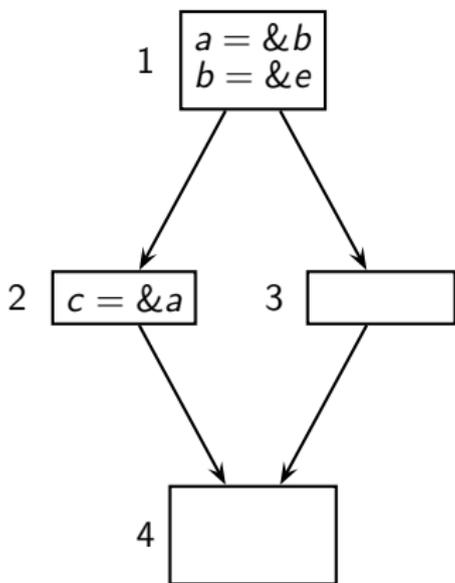
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Discovering Must Points-to Information from May Points-to Information



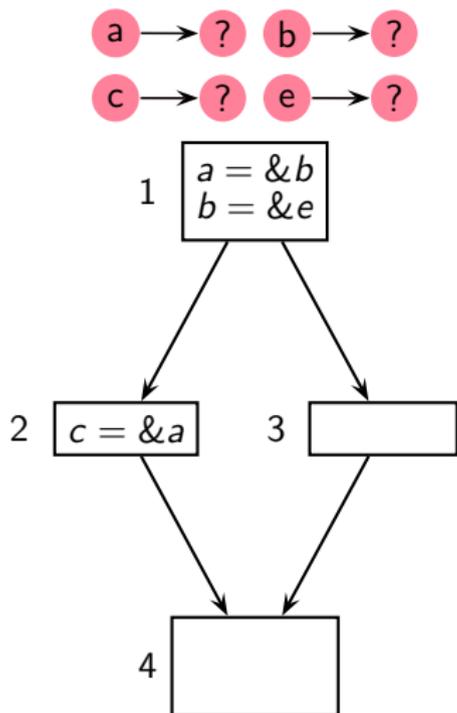
Discovering Must Points-to Information from May Points-to Information



- *Bl.* every pointer points to “?”



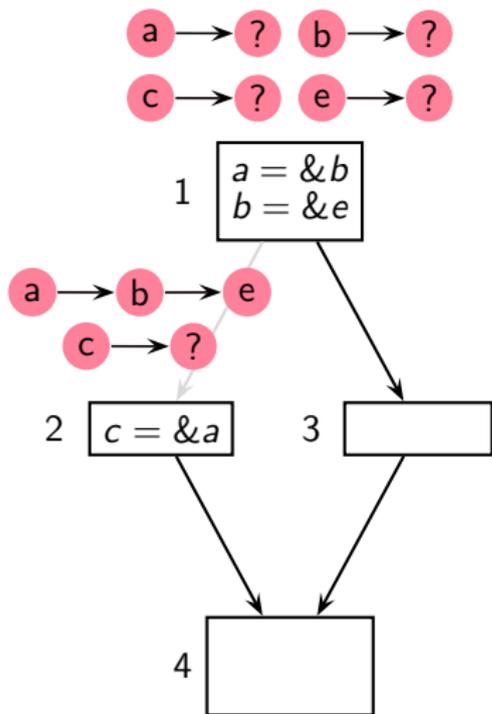
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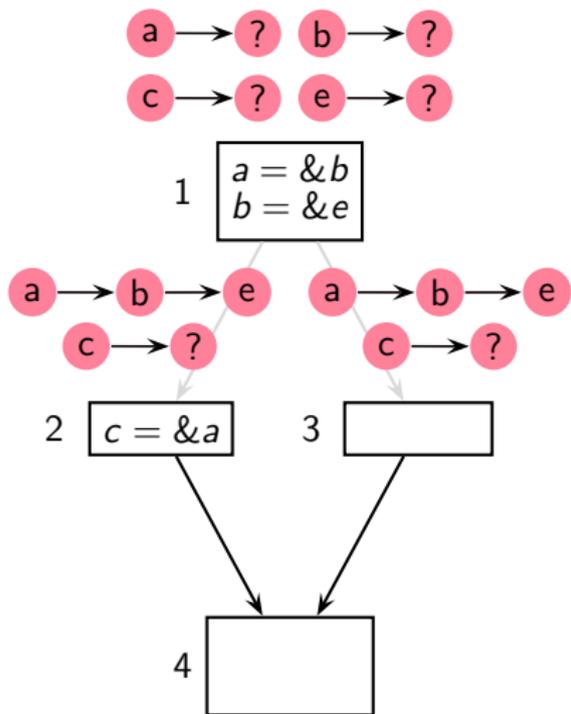
Discovering Must Points-to Information from May Points-to Information



- *Bl.* every pointer points to “?”
- Perform usual may points-to analysis



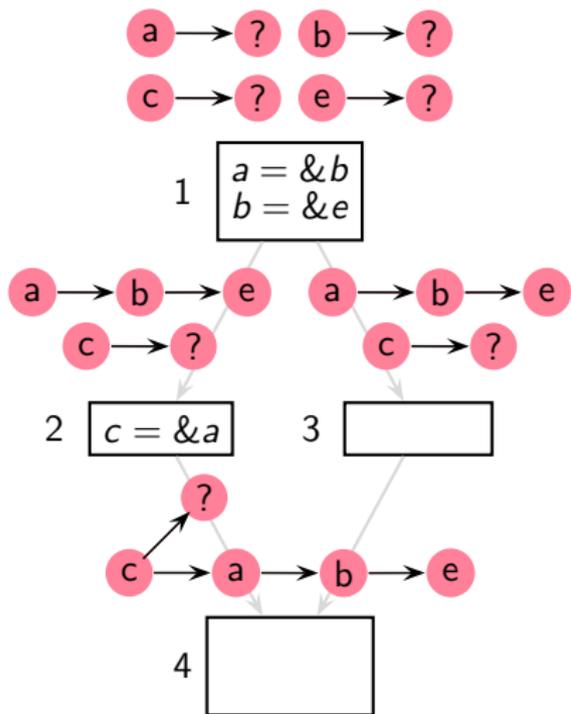
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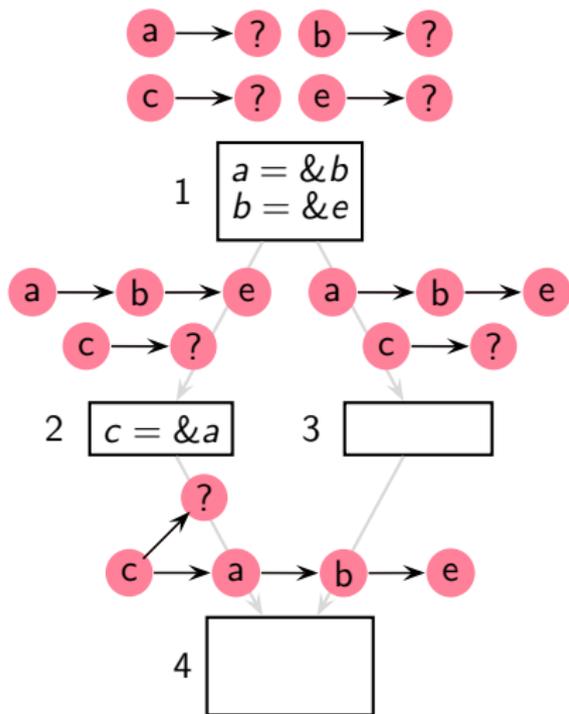
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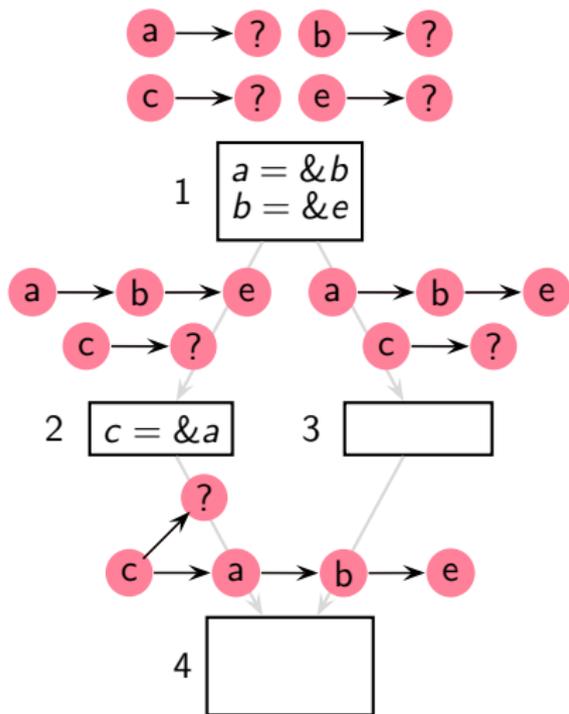
Discovering Must Points-to Information from May Points-to Information



- *Bl.* every pointer points to “?”
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- Since c has multiple pointees, it is a MAY relation



Discovering Must Points-to Information from May Points-to Information



- *Bl.* every pointer points to “?”
- Perform usual may points-to analysis
- Since c has multiple pointees, it is a MAY relation
- Since a has a single pointee, it is a MUST relation



Relevant Algebraic Operations on Relations (1)

- Let $\mathbf{P} \subseteq \text{Var}$ be the set of pointer variables
- May-points-to information: $\mathcal{A} = \langle 2^{\mathbf{P} \times \text{Var}}, \supseteq \rangle$
- Standard algebraic operations on points-to relations

Given relation $R \subseteq \mathbf{P} \times \text{Var}$ and $X \subseteq \mathbf{P}$,

- ▶ Relation *application* $R X = \{v \mid u \in X \wedge (u, v) \in R\}$
- ▶ Relation *restriction* $(R|_X) R|_X = \{(u, v) \in R \mid u \in X\}$



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- ▶ Relation *restriction* $(R|_X)$ $R|_X = \{(u, v) \in R \mid u \in X\}$
(Restrict the relation only to the pointers contained in X by removing points-to information of other pointers)



Relevant Algebraic Operations on Relations (2)

Let

$$\text{Var} = \{a, b, c, d, e, f, g, ?\}$$

$$\mathbf{P} = \{a, b, c, d, e\}$$

$$R = \{(a, b), (a, c), (b, d), (c, e), (c, g), (d, a), (e, ?)\}$$

$$X = \{a, c\}$$

Then,

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Points-to Analysis Data Flow Equations

$$Ain_n = \begin{cases} \text{Var} \times \{?\} & n \text{ is } Start_p \\ \bigcup_{p \in pred(n)} Aout_p & \text{otherwise} \end{cases}$$

$$Aout_n = \left(Ain_n - \left(Kill_n \times \text{Var} \right) \right) \cup \left(Def_n \times Pointee_n \right)$$

- $Ain/Aout$: sets of memory points-to pairs
- $Kill_n$, Def_n , and $Pointee_n$ are defined in terms of Ain_n



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Pointers whose
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Pointers that are defined (i.e. pointers in which addresses are stored)



Points-to Analysis Data Flow Equations

Pointees (i.e. locations whose addresses are stored)

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Extractor Functions for Points-to Analysis

Values defined in terms of Ain_n (denoted A)

	Def_n	$Kill_n$	$Pointee_n$
$use\ x$			
$x = \&a$			
$x = y$			
$x = *y$			
$*x = y$			
other			



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Extractor Functions for Points-to Analysis

Values defined in terms of Ain_n (denoted A)

	Def_n	$Kill_n$	$Pointee_n$
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other			

Pointees of y in Ain_n are the targets of defined pointers



Extractor Functions for Points-to Analysis

Values defined in terms of Ain_n (denoted A)

	Def_n	$Kill_n$	$Pointee_n$
$use\ x$	\emptyset	\emptyset	\emptyset
$x = \&a$	$\{x\}$	$\{x\}$	$\{a\}$
$x = y$	$\{x\}$	$\{x\}$	$A\{y\}$
$x = *y$	$\{x\}$	$\{x\}$	$A(A\{y\} \cap \mathbf{P})$
$*x = y$			
other			

Pointees of those
pointees of y in Ain_n which
are pointers



Extractor Functions for Points-to Analysis

Values defined in terms of Ain_n (denoted A)

	Def_n	$Kill_n$	$Pointee_n$
$use\ x$	\emptyset	\emptyset	\emptyset
$x = \&a$	$\{x\}$	$\{x\}$	$\{a\}$
$x = y$	$\{x\}$	$\{x\}$	$A\{y\}$
$x = *y$	$\{x\}$	$\{x\}$	$A(A\{y\} \cap \mathbf{P})$
$*x = y$	$A\{x\} \cap \mathbf{P}$	$Must(A)\{x\} \cap \mathbf{P}$	$A\{y\}$
other			

Pointees of
 x in Ain_n receive new
addresses



Extractor Functions for Points-to Analysis

Values defined in terms of Ain_n

Strong update using
must-points-to information
computed from Ain_n

	Def_n	$Kill_n$	
$use\ x$	\emptyset	\emptyset	\emptyset
$x = \&a$	$\{x\}$	$\{x\}$	$\{a\}$
$x = y$	$\{x\}$	$\{x\}$	$A\{y\}$
$x = *y$	$\{x\}$	$\{y\}$	$A(A\{y\} \cap \mathbf{P})$
$*x = y$	$A\{x\} \cap \mathbf{P}$	$Must(A)\{x\} \cap \mathbf{P}$	$A\{y\}$
other			

$$Must(R) = \bigcup_{z \in \mathbf{P}} \{z\} \times \begin{cases} \{w\} & R\{z\} = \{w\} \wedge w \neq ? \\ \emptyset & \text{otherwise} \end{cases}$$



Extractor Functions for Points-to Analysis

Values defined in terms of Ain_n

Strong update using
must-points-to information
computed from Ain_n

	Def_n	$Kill_n$	
$use\ x$	\emptyset	\emptyset	\emptyset
$x = \&a$	$\{x\}$	$\{x\}$	$\{a\}$
$x = y$	$\{x\}$	$\{x\}$	$A\{y\}$
$x = *y$	$\{x\}$	$\{y\}$	$A(A\{y\} \cap \mathbf{P})$
$*x = y$	$A\{x\} \cap \mathbf{P}$	$Must(A)\{x\} \cap \mathbf{P}$	$A\{y\}$
other			

$$Must(R) = \left(\bigcup_{z \in \mathbf{P}} \{z\} \right) \times \begin{cases} \{w\} & R\{z\} = \{w\} \wedge w \neq ? \\ \emptyset & \text{otherwise} \end{cases}$$

Find out
must-pointees of
all pointers



Extractor Functions for Points-to Analysis

Values defined in terms of Ain_n

Strong update using
must-points-to information
computed from Ain_n

	Def_n	$Kill_n$	
$use\ x$	\emptyset	\emptyset	\emptyset
$x = \&a$	$\{x\}$	$\{x\}$	$\{a\}$
$x = y$	$\{x\}$	$\{x\}$	$A\{y\}$
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other			

$$Must(R) = \bigcup_{z \in \mathbf{P}} \{z\} \times \begin{cases} \{w\} & R\{z\} = \{w\} \wedge w \neq ? \\ \emptyset & \text{otherwise} \end{cases}$$

z has a single pointee
 w in must-points-to
relation



Extractor Functions for Points-to Analysis

Values defined in terms of Ain_n

Strong update using
must-points-to information
computed from Ain_n

	Def_n	$Kill_n$	
$use\ x$	\emptyset	\emptyset	\emptyset
$x = \&a$	$\{x\}$	$\{x\}$	$\{a\}$
$x = y$	$\{x\}$	$\{x\}$	$A\{y\}$
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other			

$$Must(R) = \bigcup_{z \in \mathbf{P}} \{z\} \times \begin{cases} \{w\} & R\{z\} = \{w\} \wedge w \neq ? \\ \emptyset & \text{otherwise} \end{cases}$$

z has no pointee
in must-points-to
relation



Extractor Functions for Points-to Analysis

Values defined in terms of Ain_n (denoted A)

	Def_n	$Kill_n$	$Pointee_n$
$use\ x$	\emptyset	\emptyset	\emptyset
$x = \&a$	$\{x\}$	$\{x\}$	$\{a\}$
$x = y$	$\{x\}$	$\{x\}$	$A\{y\}$
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Extractor Functions for Points-to Analysis

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$*x = y$	$A\{x\} \cap \mathbf{P}$	$Must(A)\{x\} \cap \mathbf{P}$	$A\{y\}$
other			

$$Must(R) = \bigcup_{z \in \mathbf{P}} \{z\} \times \begin{cases} \{w\} \\ \emptyset \end{cases} \quad \begin{array}{l} R\{z\} = \{w\} \wedge w \neq ? \\ \text{otherwise} \end{array}$$

Pointees of y in Ain_n are the targets of defined pointers



Extractor Functions for Points-to Analysis

Values defined in terms of Ain_n (denoted A)

	Def_n	$Kill_n$	$Pointee_n$
$use\ x$	\emptyset	\emptyset	\emptyset
$x = \&a$	$\{x\}$	$\{x\}$	$\{a\}$
$x = y$	$\{x\}$	$\{x\}$	$A\{y\}$
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$*x = y$	$A\{x\} \cap \mathbf{P}$	$Must(A)\{x\} \cap \mathbf{P}$	$A\{y\}$
other	\emptyset	\emptyset	\emptyset

$$Must(R) = \bigcup_{z \in \mathbf{P}} \{z\} \times \begin{cases} \{w\} & R\{z\} = \{w\} \wedge w \neq ? \\ \emptyset & \text{otherwise} \end{cases}$$



Extractor Functions for Points-to Analysis

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$*x = y$	$A\{x\} \cap \mathbf{P}$	$Must(A)\{x\} \cap \mathbf{P}$	$A\{y\}$
other	\emptyset	\emptyset	\emptyset

$$Must(R) = \bigcup_{z \in \mathbf{P}} \{z\} \times \begin{cases} \{w\} & R\{z\} = \{w\} \wedge w \neq ? \\ \emptyset & \text{otherwise} \end{cases}$$



Extractor Functions for Points-to Analysis

Values defined in terms of Ain_n (denoted A)

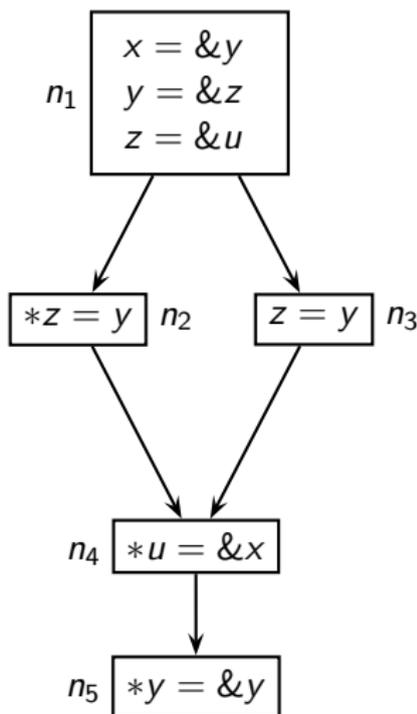
	Def_n	$Kill_n$	$Pointee_n$
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$x = \&a$	$\{x\}$	$\{x\}$	$\{a\}$
$x = y$	$\{x\}$	$\{x\}$	$A\{y\}$
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$*x = y$	$A\{x\} \cap \mathbf{P}$	$Must(A)\{x\} \cap \mathbf{P}$	$A\{y\}$
other	\emptyset	\emptyset	\emptyset

$$Must(R) = \bigcup_{z \in \mathbf{P}} \{z\} \times \begin{cases} \{w\} & R\{z\} = \{w\} \wedge w \neq ? \\ \emptyset & \text{otherwise} \end{cases}$$



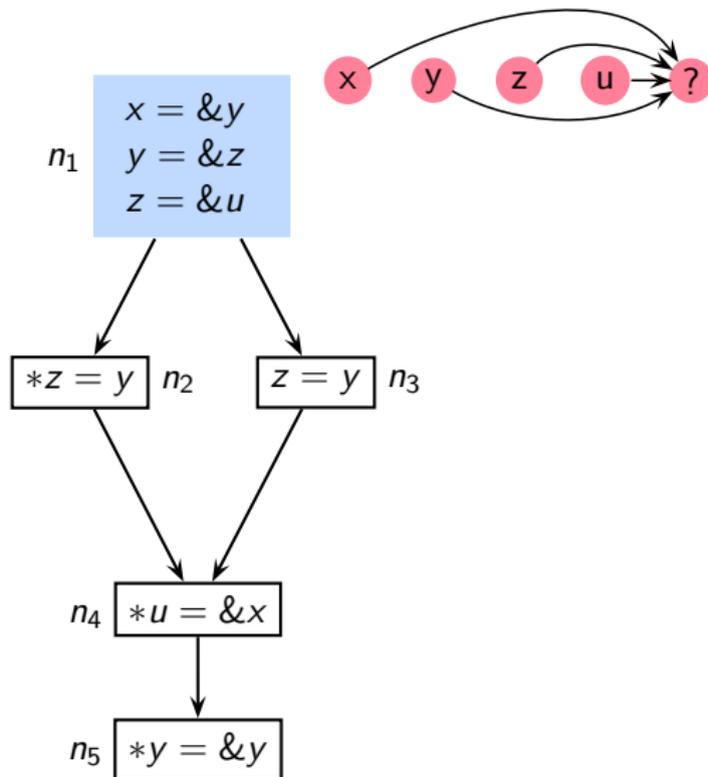
An Example of Flow Sensitive May Points-to Analysis

Assume that
the program is
type correct



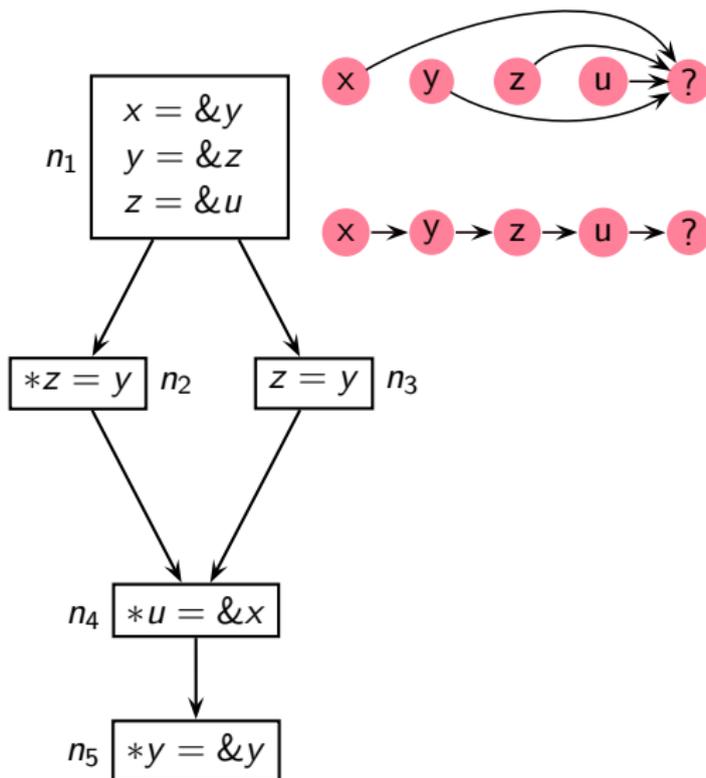
An Example of Flow Sensitive May Points-to Analysis

Assume that
the program is
type correct



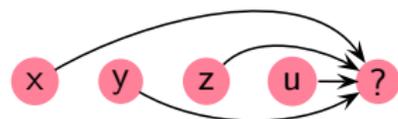
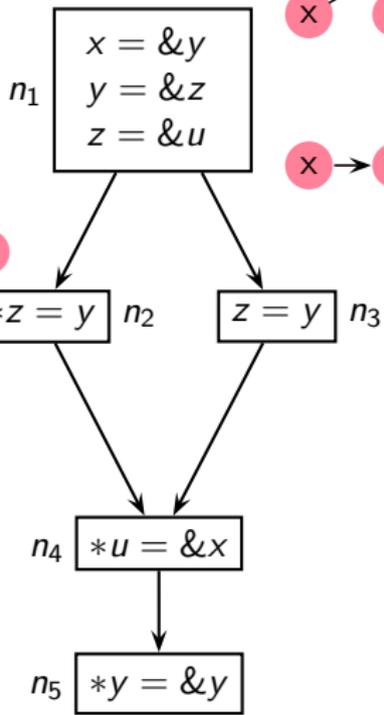
An Example of Flow Sensitive May Points-to Analysis

Assume that
the program is
type correct



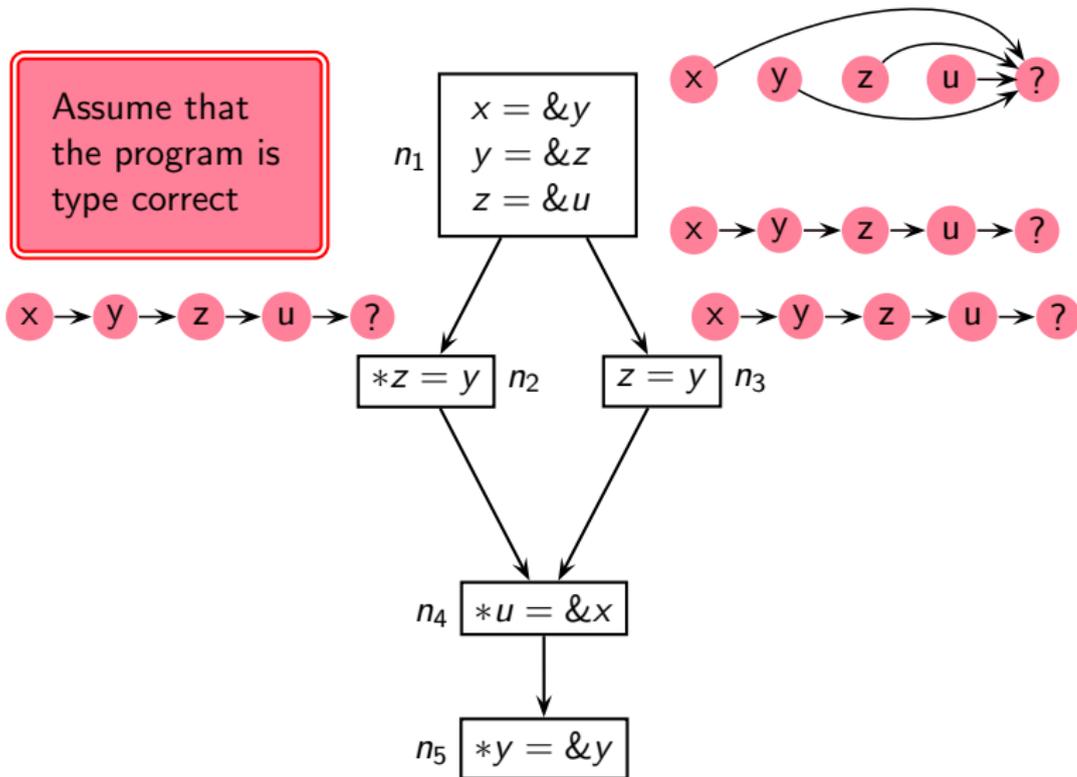
An Example of Flow Sensitive May Points-to Analysis

Assume that the program is type correct



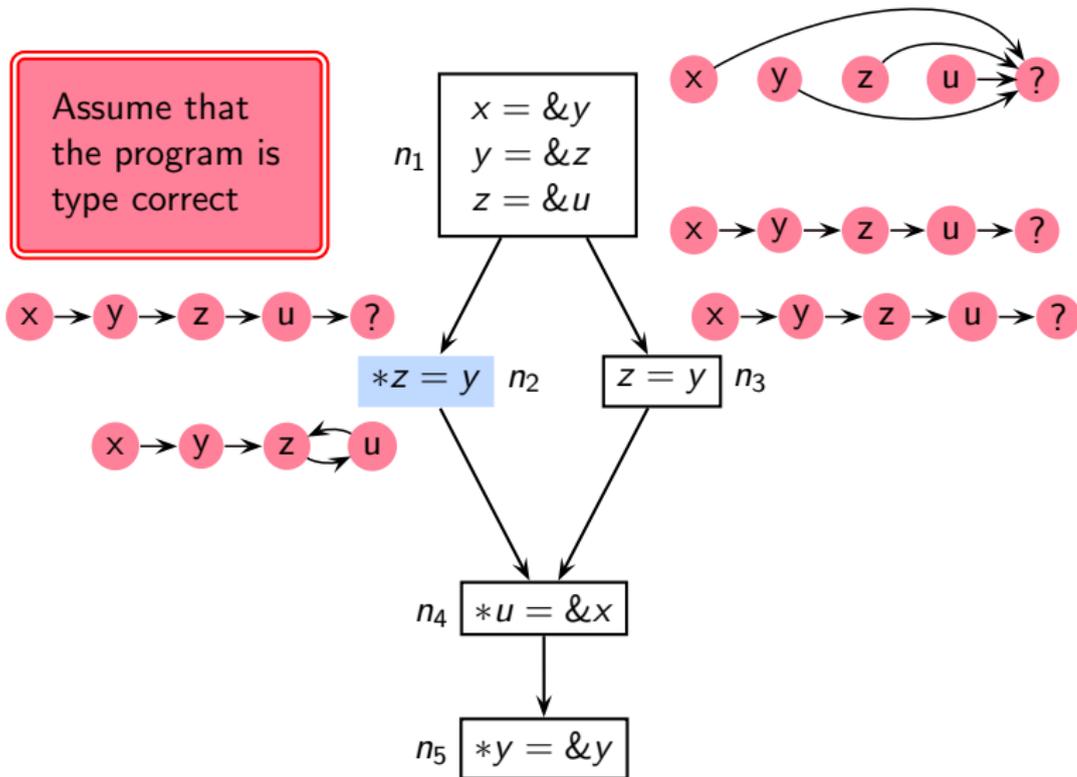
An Example of Flow Sensitive May Points-to Analysis

Assume that the program is type correct



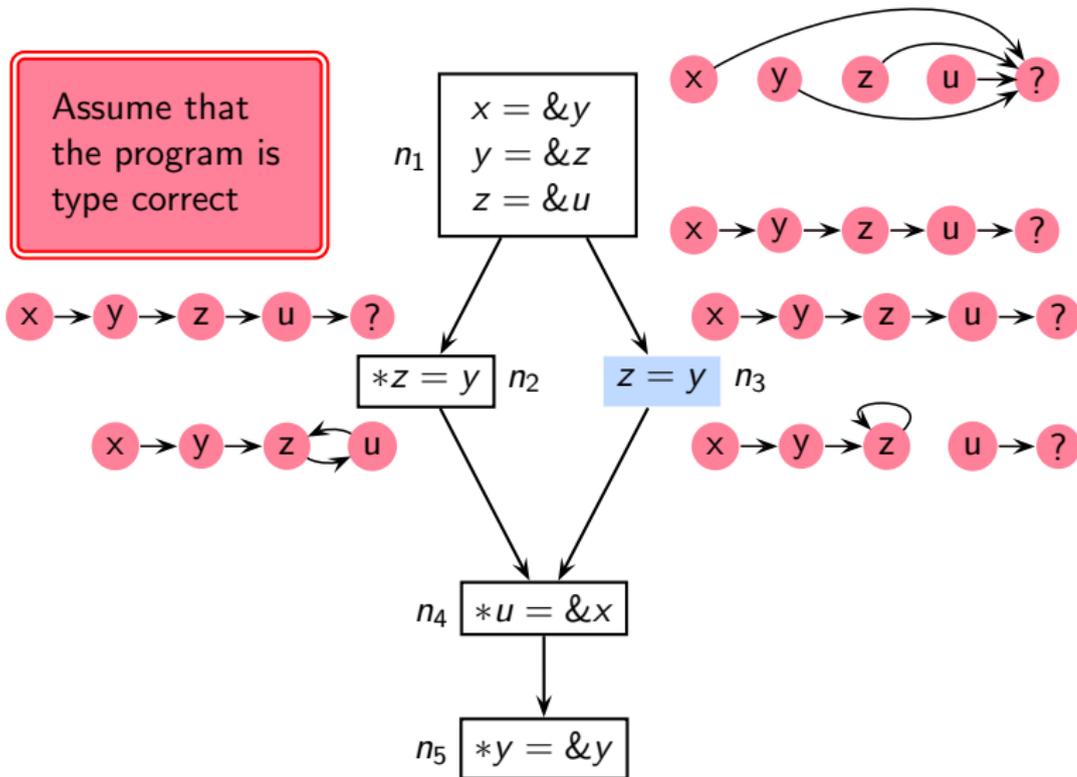
An Example of Flow Sensitive May Points-to Analysis

Assume that the program is type correct



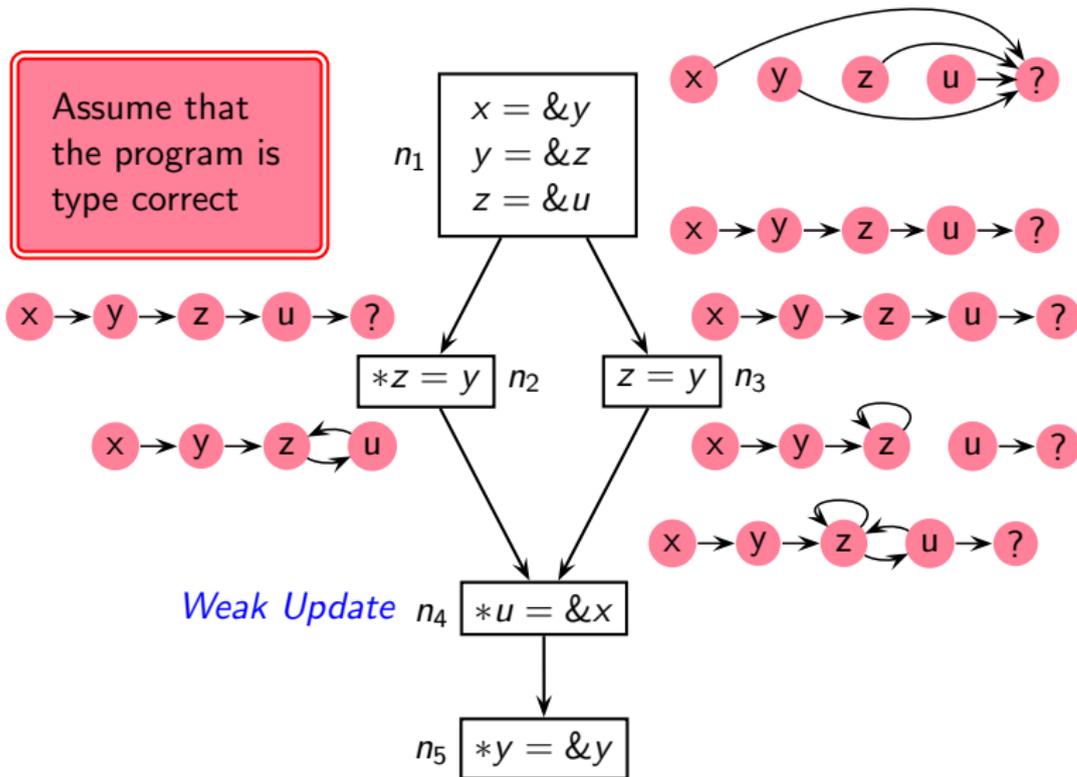
An Example of Flow Sensitive May Points-to Analysis

Assume that the program is type correct



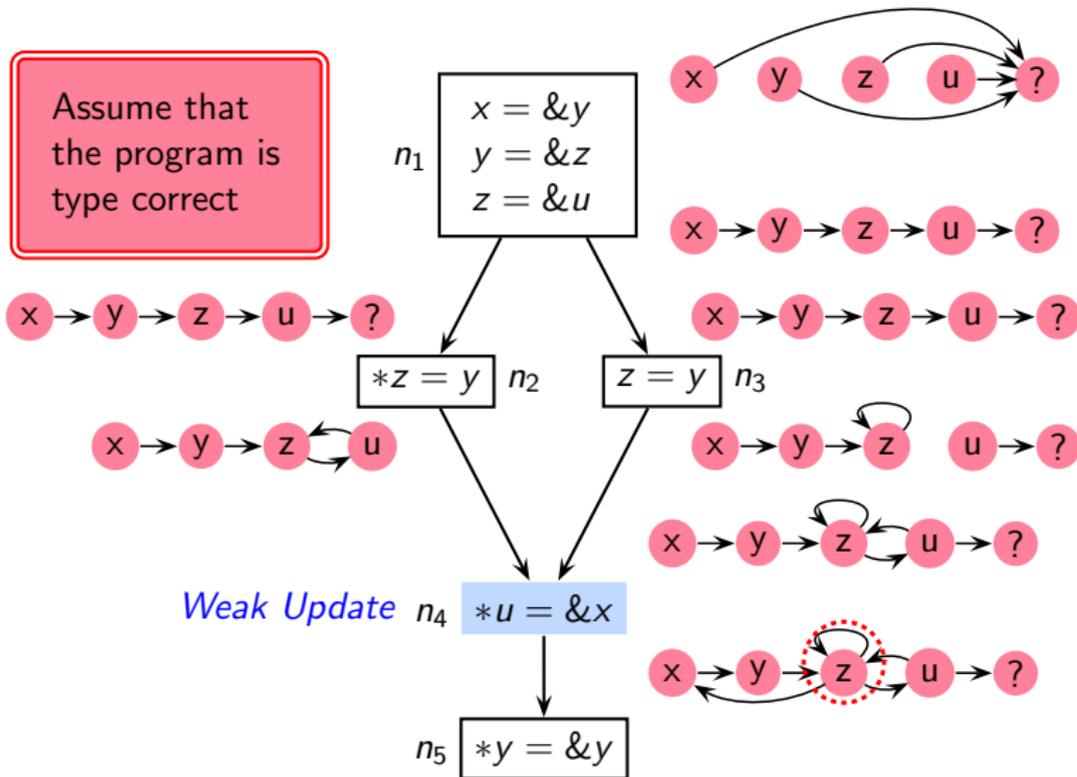
An Example of Flow Sensitive May Points-to Analysis

Assume that the program is type correct



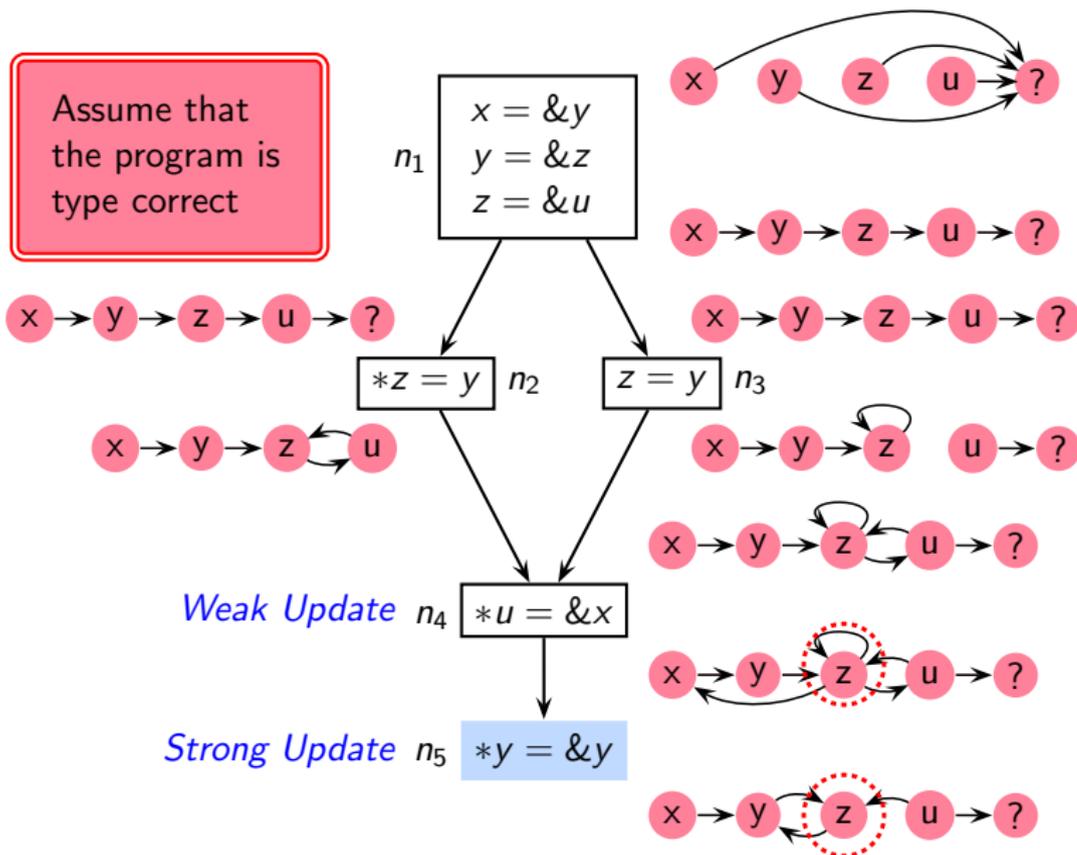
An Example of Flow Sensitive May Points-to Analysis

Assume that the program is type correct



An Example of Flow Sensitive May Points-to Analysis

Assume that the program is type correct



Tutorial Problems for Flow Sensitive Pointer Analysis (2)

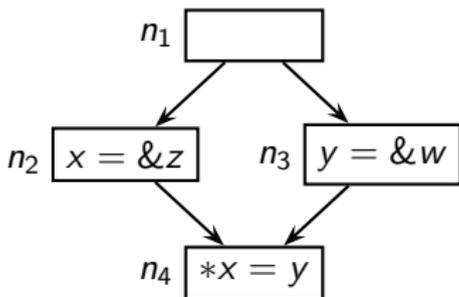
Compute May and Must points-to information

```
if (...)
    p = &x;
else
    p = &y;
x = &a;
y = &b;
*p = &c;
*y = &a;
```

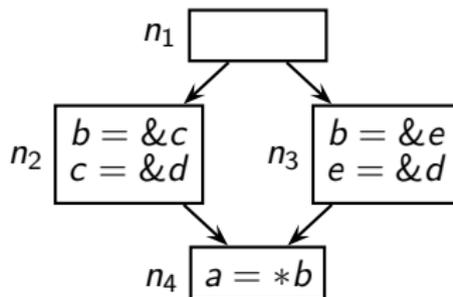


Non-Distributivity of Points-to Analysis

May Points-to

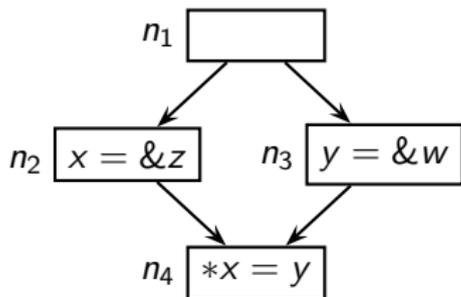


Must Points-to



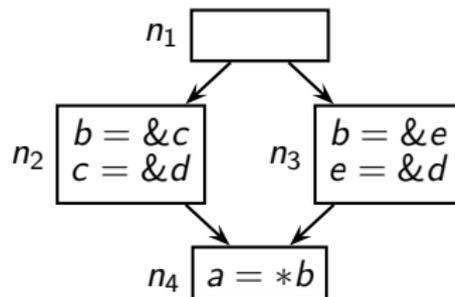
Non-Distributivity of Points-to Analysis

May Points-to



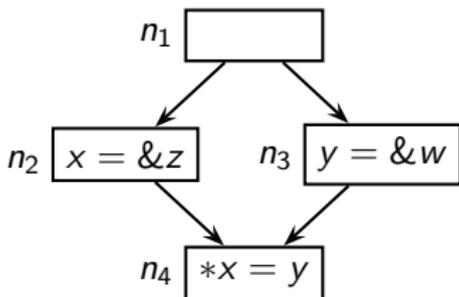
$z \rightarrow w$ is spurious

Must Points-to



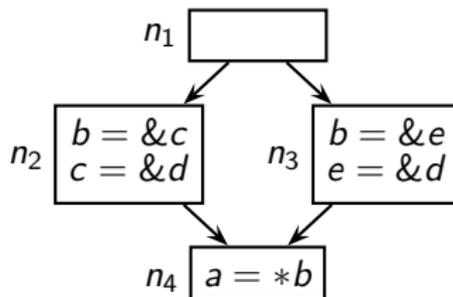
Non-Distributivity of Points-to Analysis

May Points-to



$z \rightarrow w$ is spurious

Must Points-to



$a \rightarrow d$ is missing

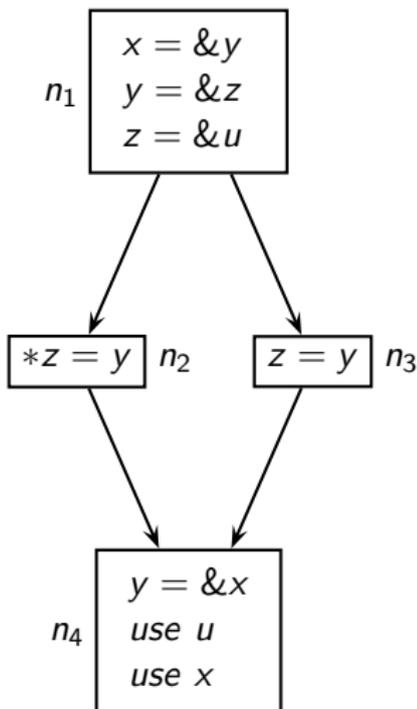


An Outline of Pointer Analysis Coverage

- The larger perspective
- Comparing Points-to and Alias information
- Flow Insensitive Points-to Analysis
- Flow Sensitive Points-to Analysis
- **Pointer Analyses: An Engineer's Landscape** **Next Topic**
- Liveness Based Points-to Analysis
- Generalizations to Heap, Arrays, Pointer Arithmetic, and Unions



An Example of Flow Insensitive May Points-to Analysis

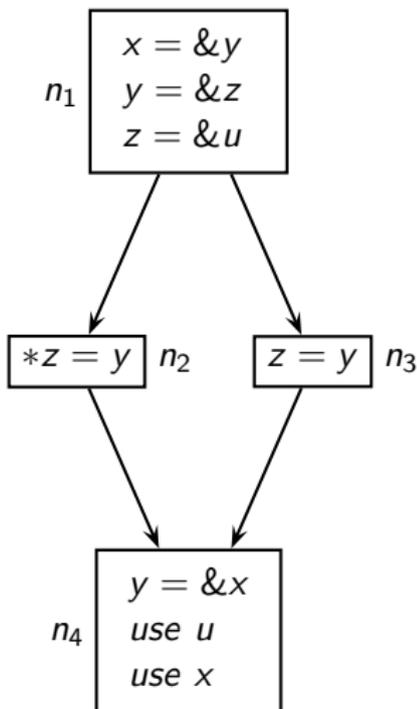


Andersen's Points-to Graph

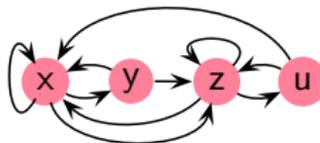
Steensgaard's Points-to Graph



An Example of Flow Insensitive May Points-to Analysis



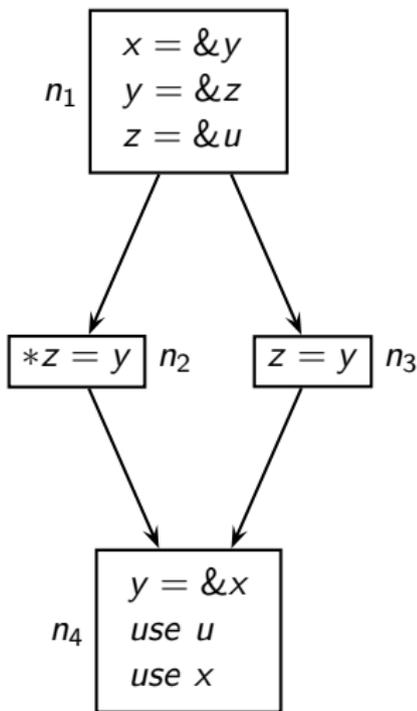
Andersen's Points-to Graph



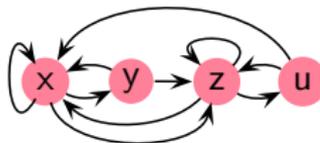
Steensgaard's Points-to Graph



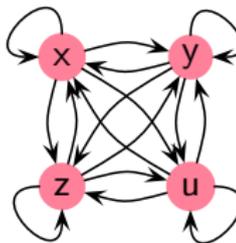
An Example of Flow Insensitive May Points-to Analysis



Andersen's Points-to Graph

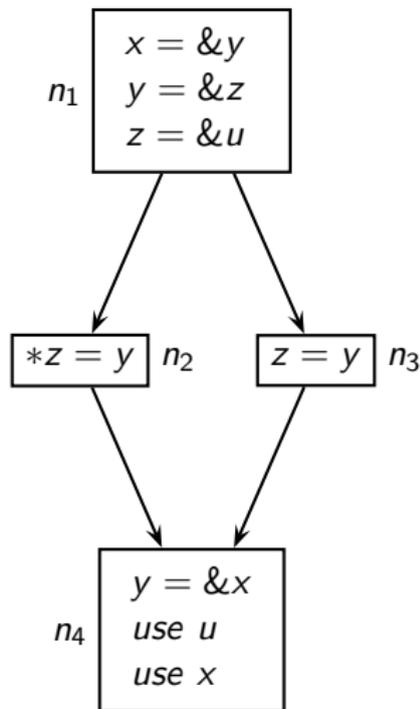


Steensgaard's Points-to Graph



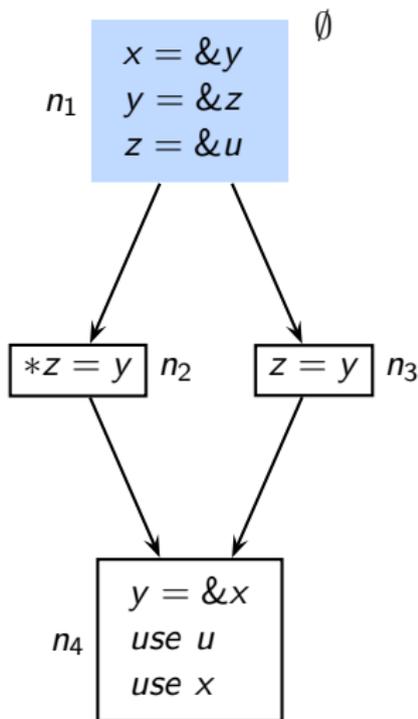
An Example of Flow Sensitive May Points-to Analysis

For simplicity,
we ignore the
BI with “?”



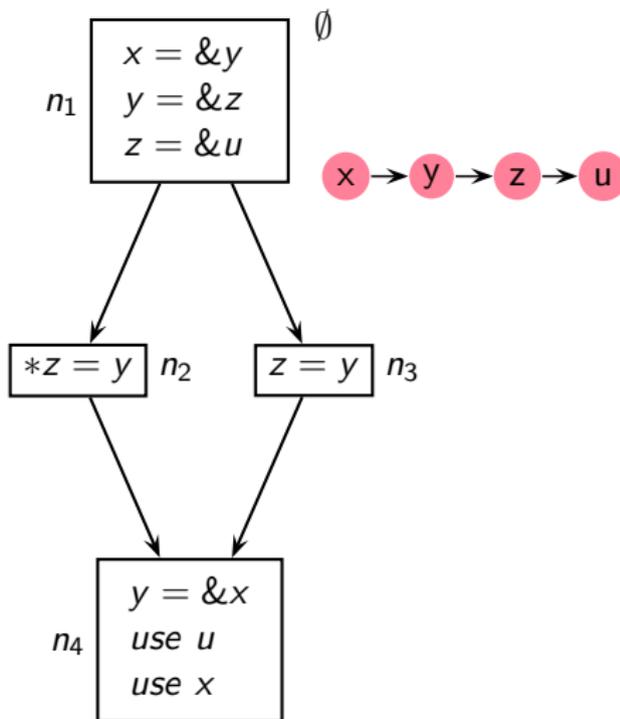
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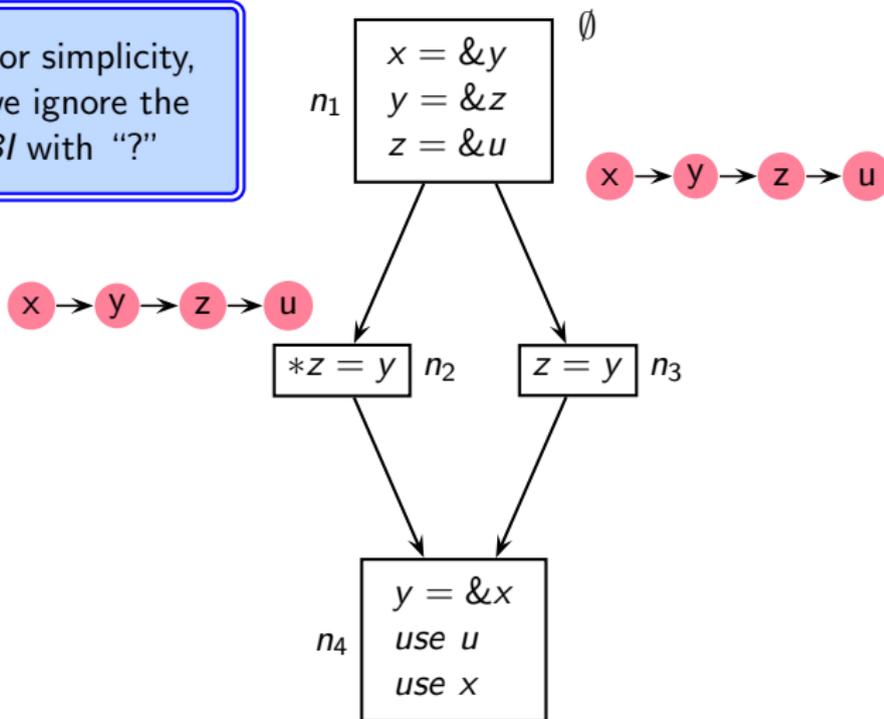
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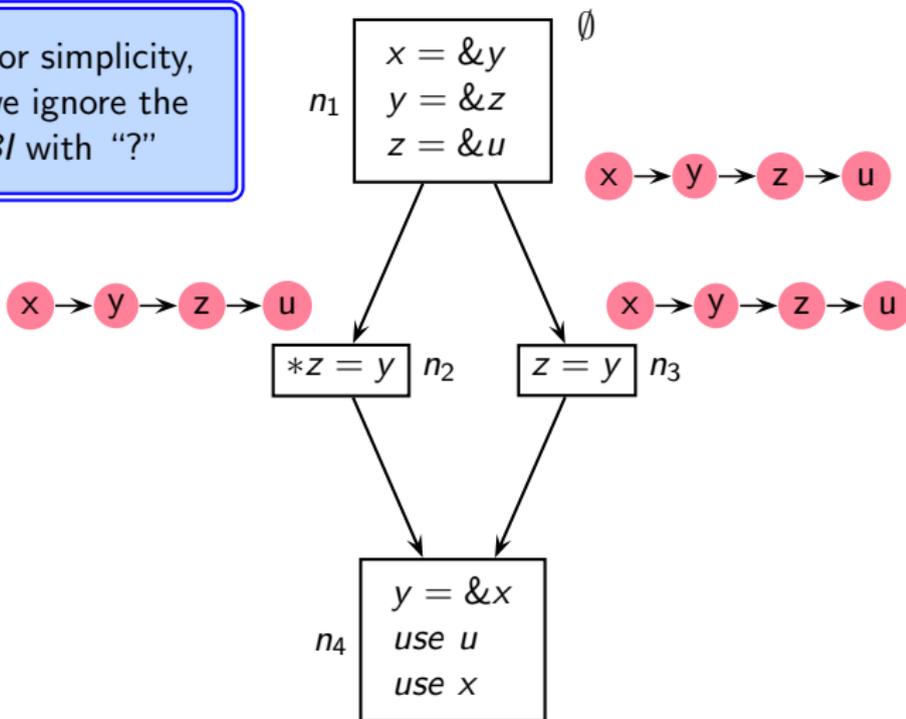
An Example of Flow Sensitive May Points-to Analysis

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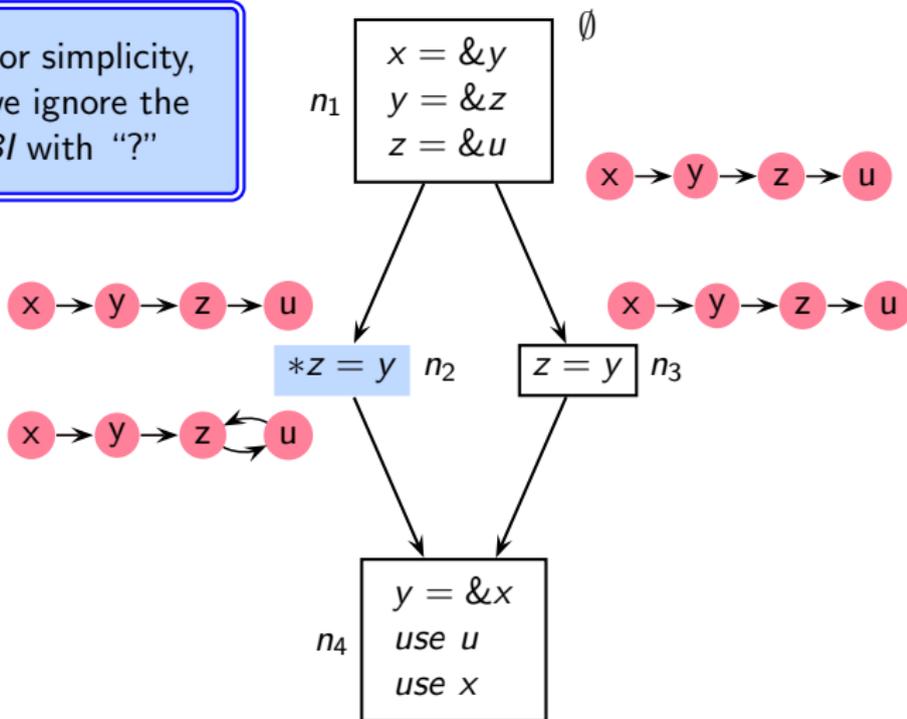
An Example of Flow Sensitive May Points-to Analysis

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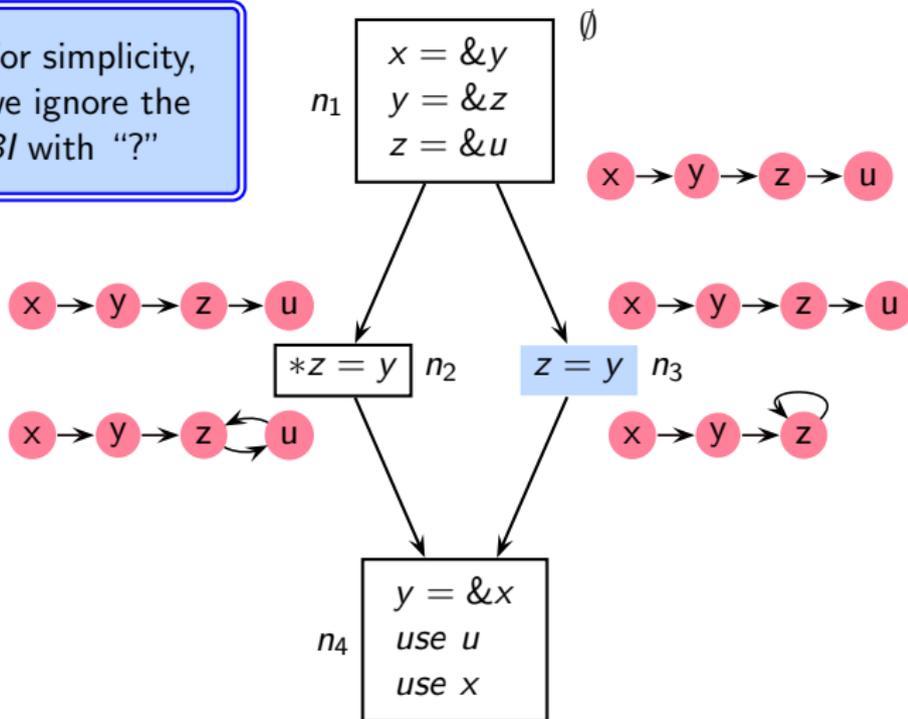
An Example of Flow Sensitive May Points-to Analysis

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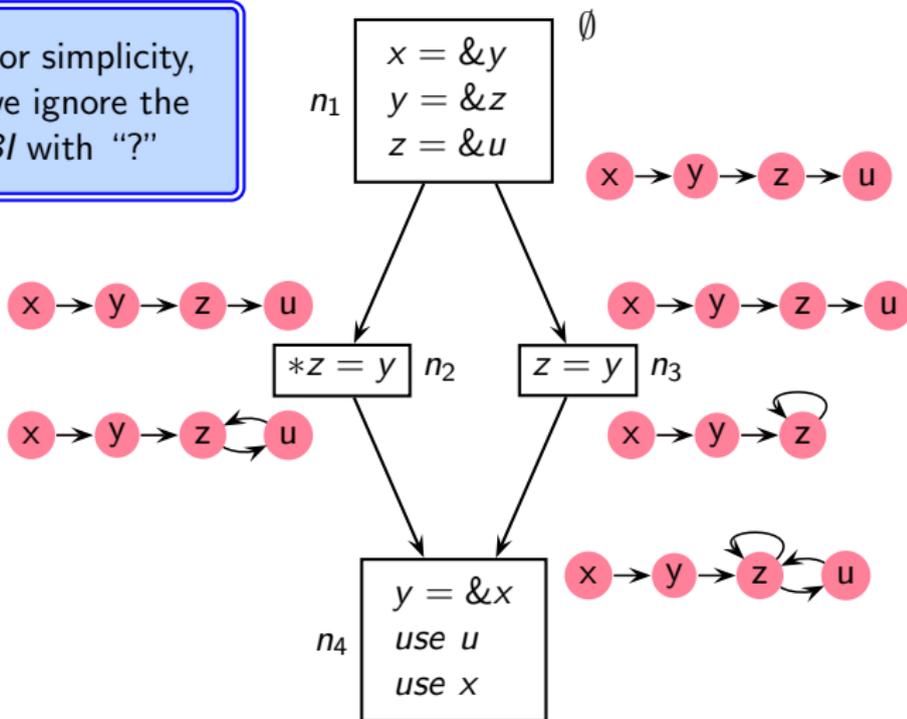
An Example of Flow Sensitive May Points-to Analysis

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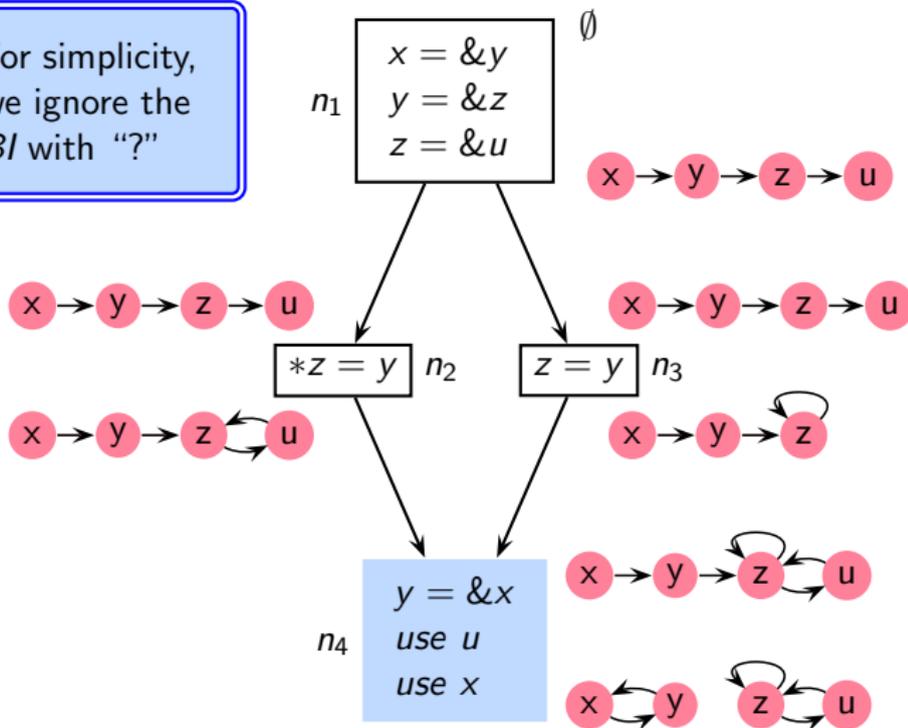
An Example of Flow Sensitive May Points-to Analysis

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BI with “?”

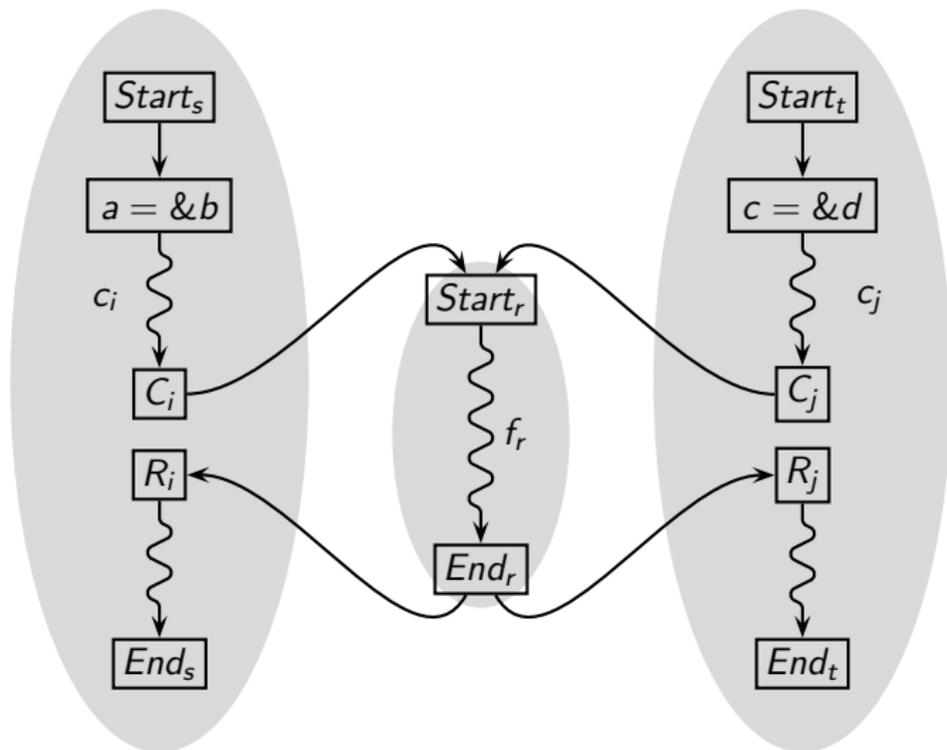


An Example of Flow Sensitive May Points-to Analysis

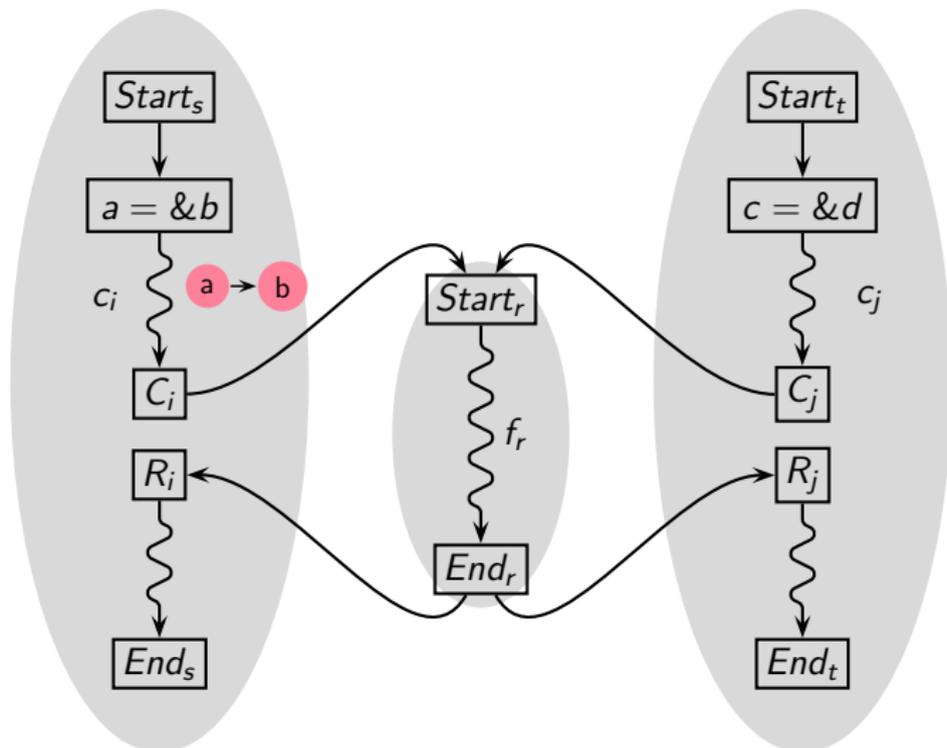
For simplicity,
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BI with “?”



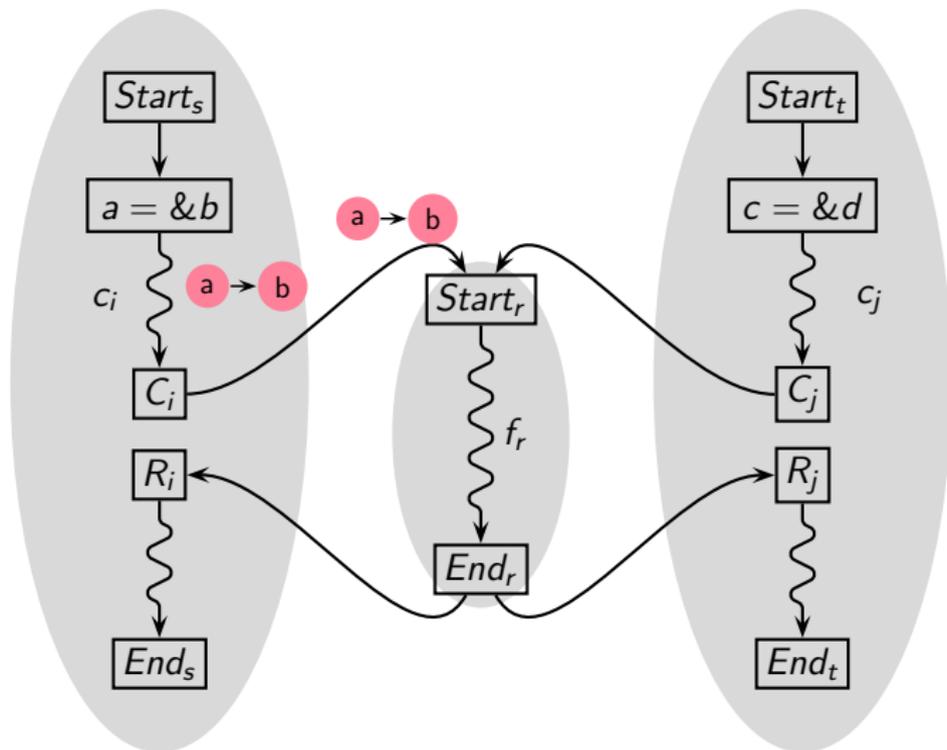
Context Sensitivity in Interprocedural Analysis



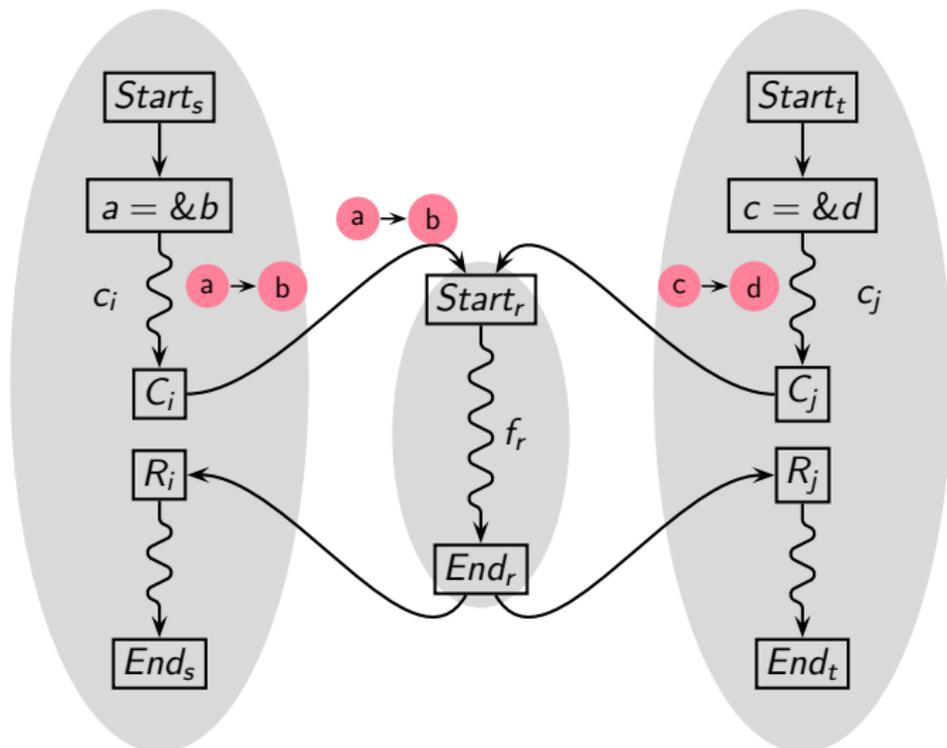
Context Sensitivity in Interprocedural Analysis



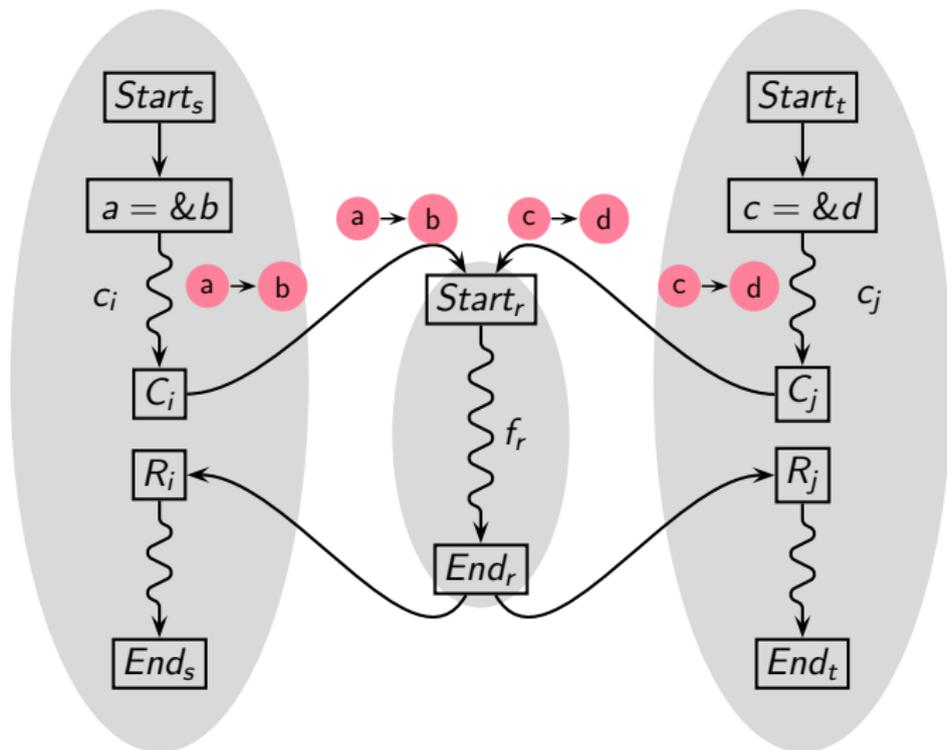
Context Sensitivity in Interprocedural Analysis



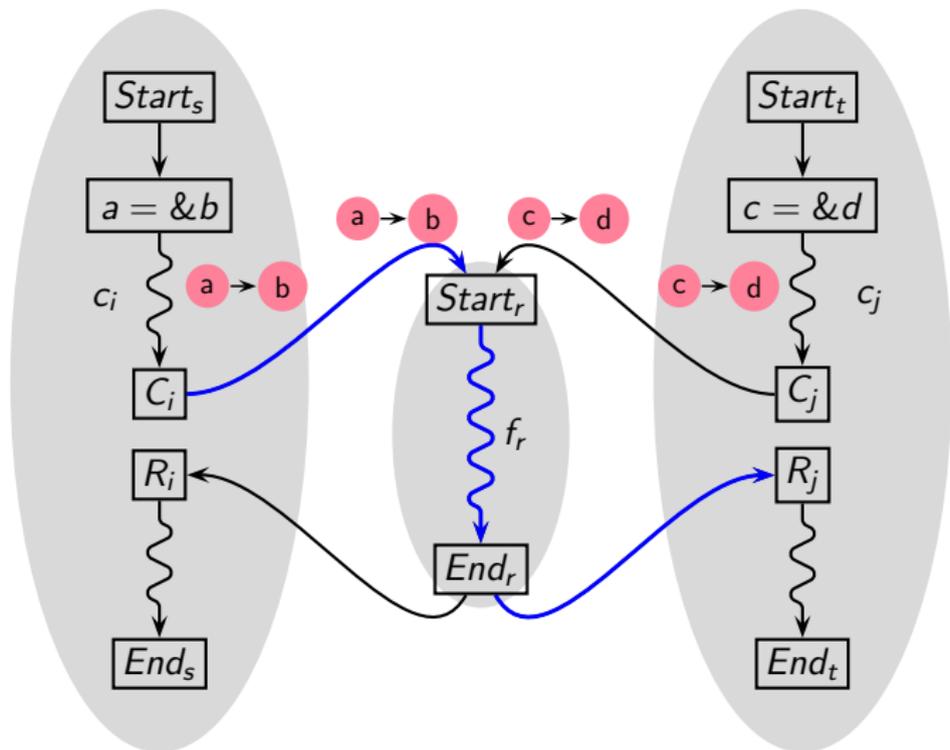
Context Sensitivity in Interprocedural Analysis



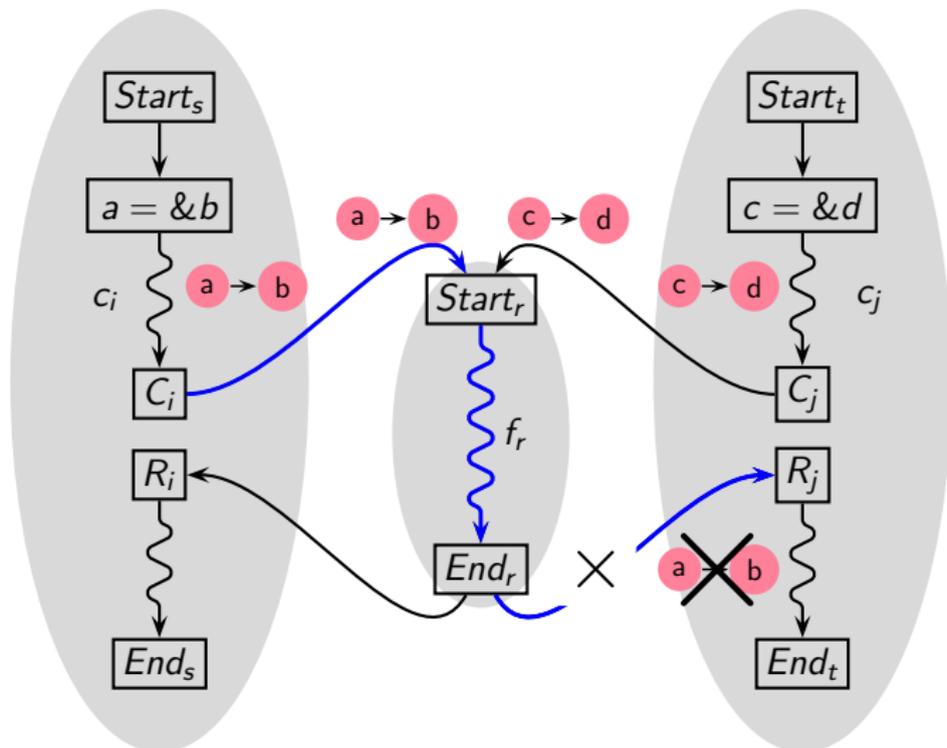
Context Sensitivity in Interprocedural Analysis



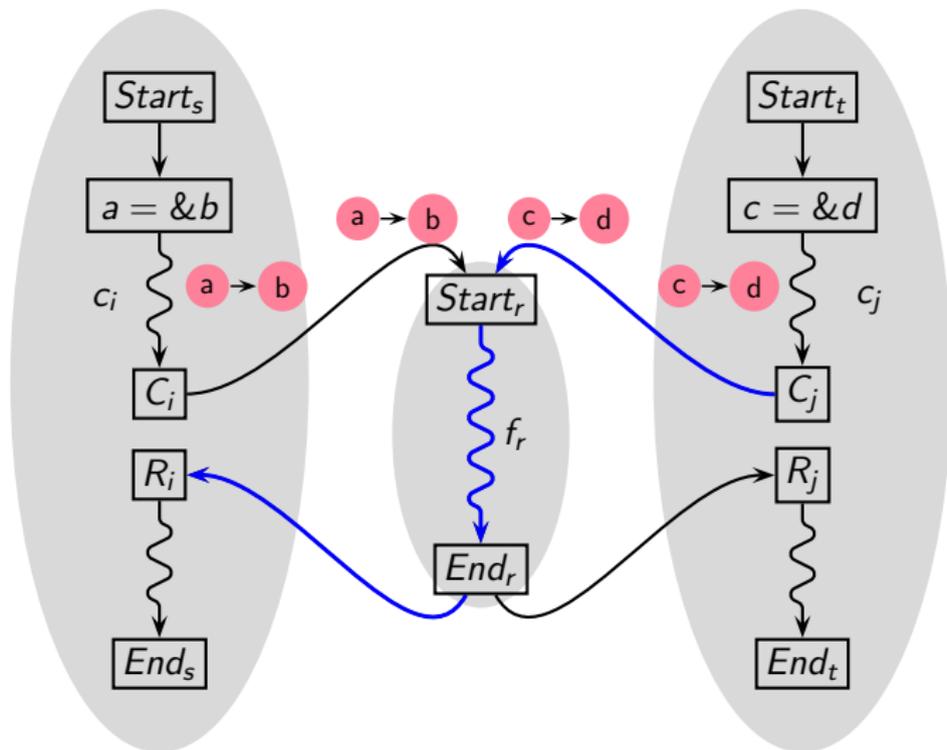
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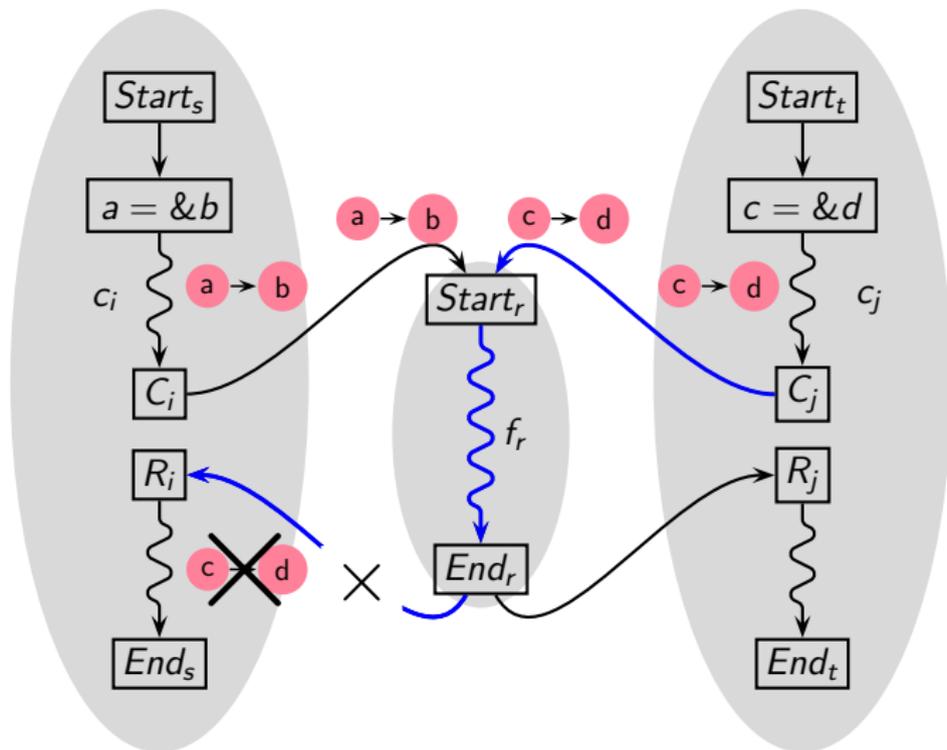
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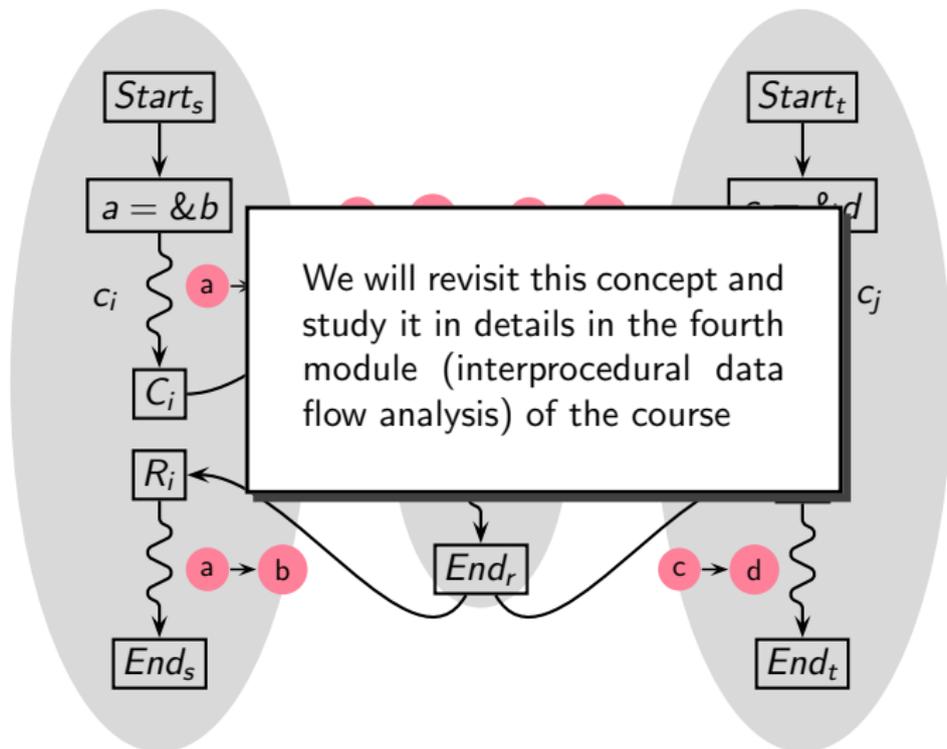
Context Sensitivity in Interprocedural Analysis



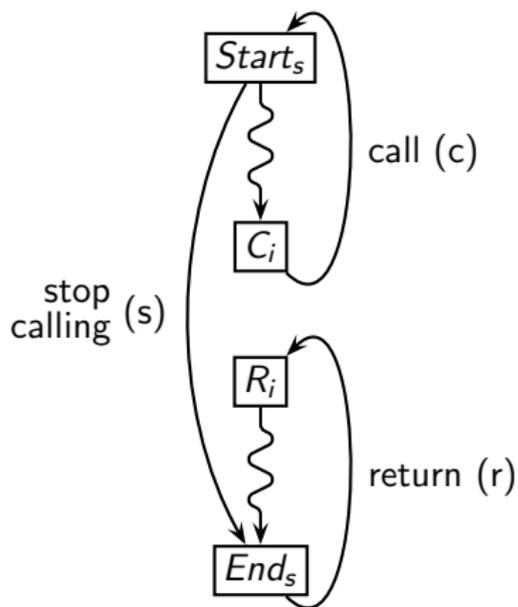
Context Sensitivity in Interprocedural Analysis



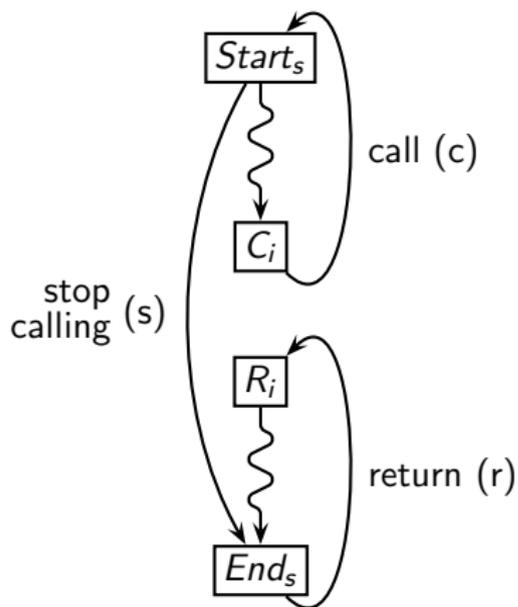
Context Sensitivity in Interprocedural Analysis



Context Sensitivity in the Presence of Recursion



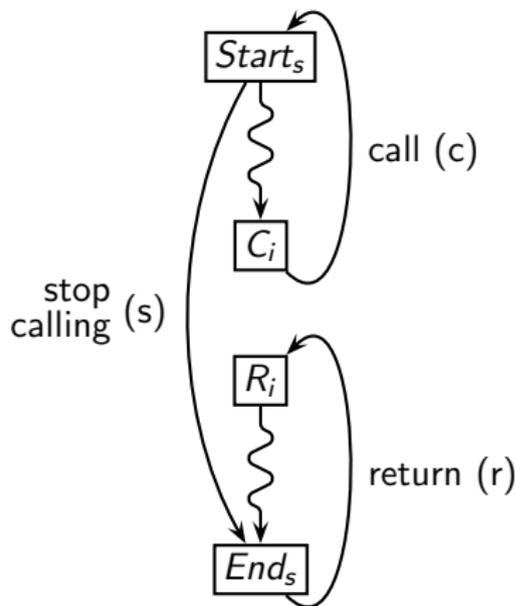
Context Sensitivity in the Presence of Recursion



- Paths from $Start_s$ to End_s should constitute a context free language $c^n sr^n$



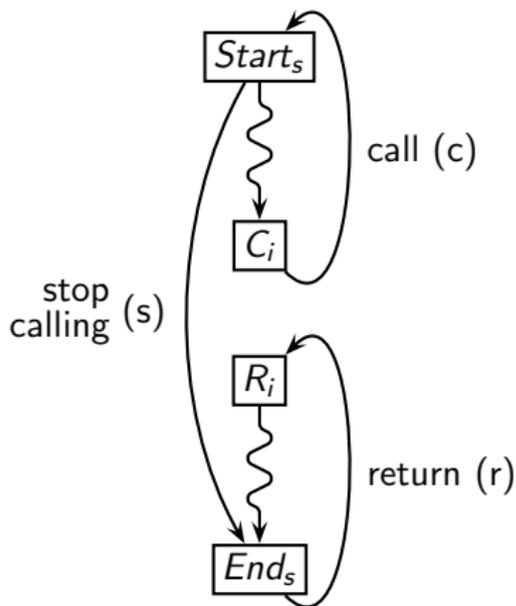
Context Sensitivity in the Presence of Recursion



- Paths from $Start_s$ to End_s should constitute a context free language $c^n sr^n$
- Many interprocedural analyses treat cycle of recursion as an SCC and approximate paths by a regular language $c^* sr^*$



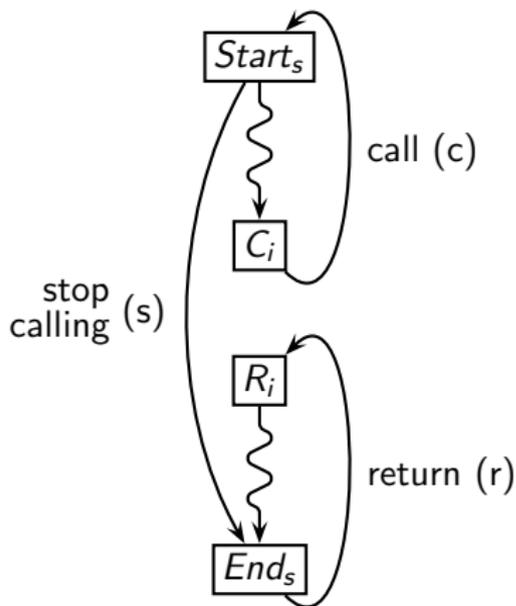
Context Sensitivity in the Presence of Recursion



- Paths from $Start_s$ to End_s should constitute a context free language $c^n sr^n$
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- We do not know any practical points-to analysis that is fully context sensitive
Most context sensitive approaches



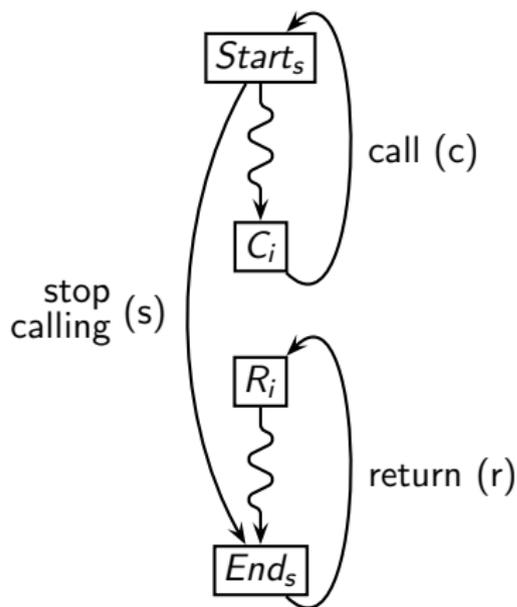
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- We do not know any practical points-to analysis that is fully context sensitive
Most context sensitive approaches
 - ▶ either **do not consider recursion**, or



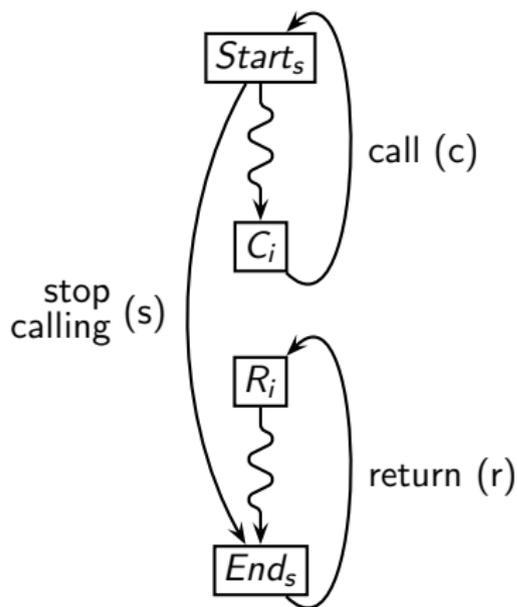
Context Sensitivity in the Presence of Recursion



- Paths from $Start_s$ to End_s should constitute a context free language $c^n sr^n$
 - Many interprocedural analyses treat cycle of recursion as an SCC and approximate paths by a regular language c^*sr^*
 - We do not know any practical points-to analysis that is fully context sensitive
- Most context sensitive approaches
- ▶ either do not consider recursion, or
 - ▶ do not consider recursive pointer manipulation (e.g. " $p = p \rightarrow n$ "), or



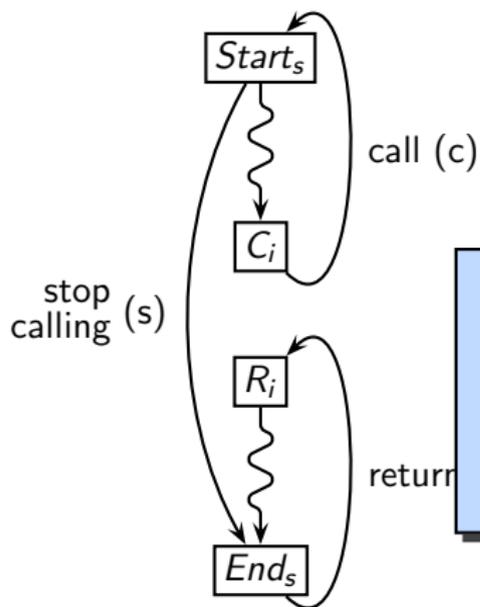
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 - ▶ are **context insensitive in recursion**



Context Sensitivity in the Presence of Recursion



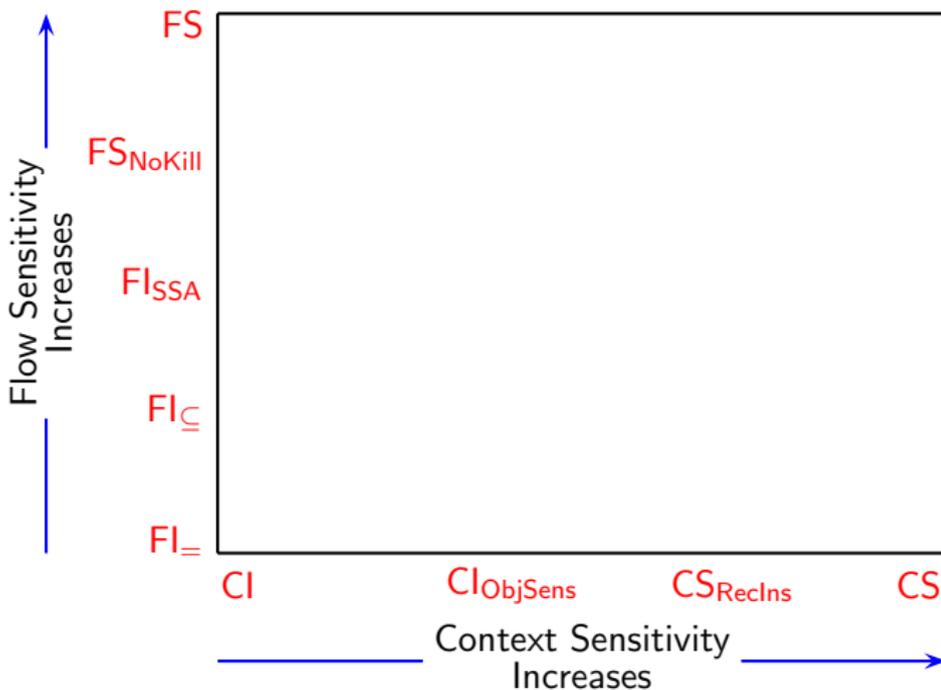
- Paths from $Start_s$ to End_s should constitute a context free language $c^n sr^n$
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We will revisit this concept and study it in details in the fourth module (interprocedural data flow analysis) of the course

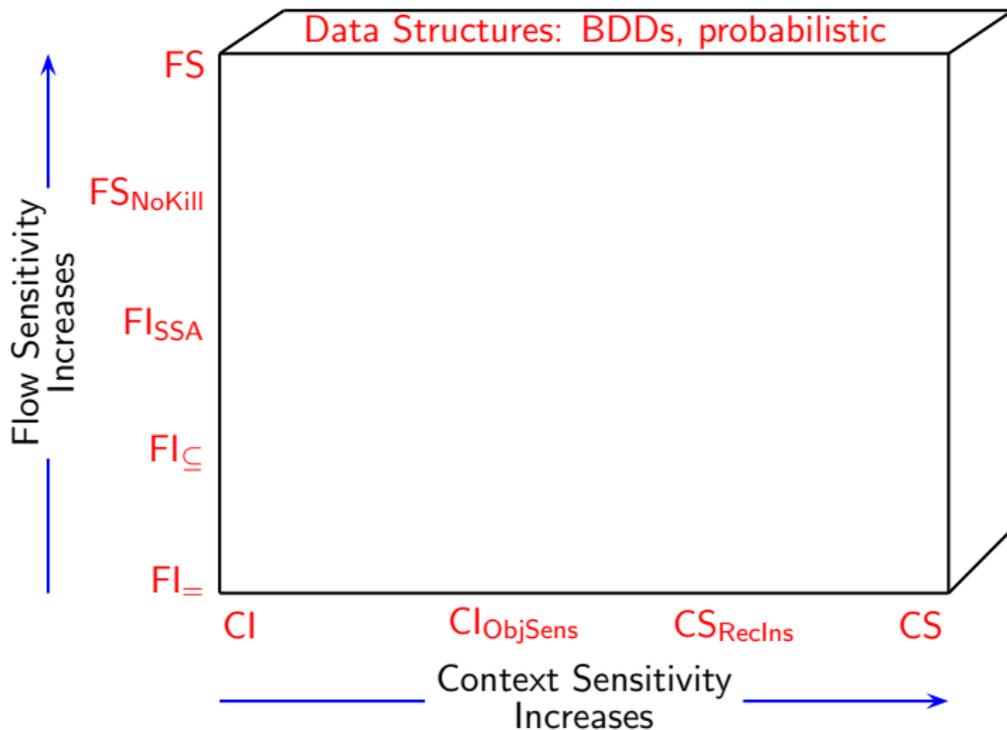
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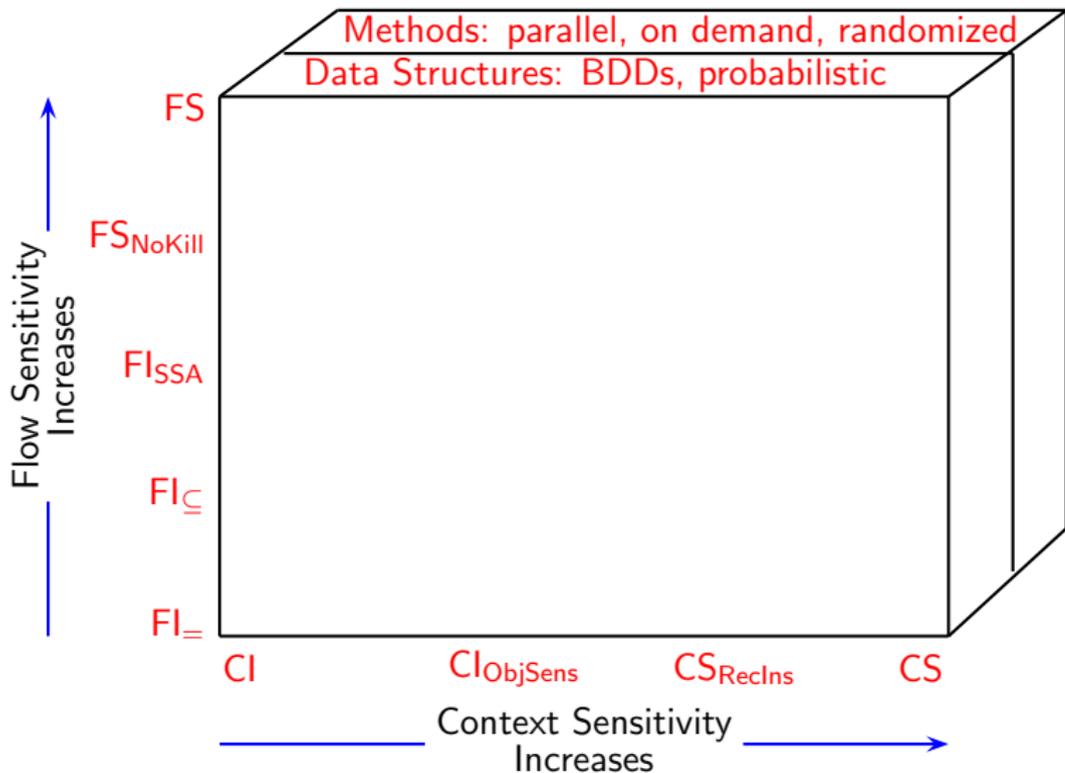
Pointer Analysis: An Engineer's Landscape



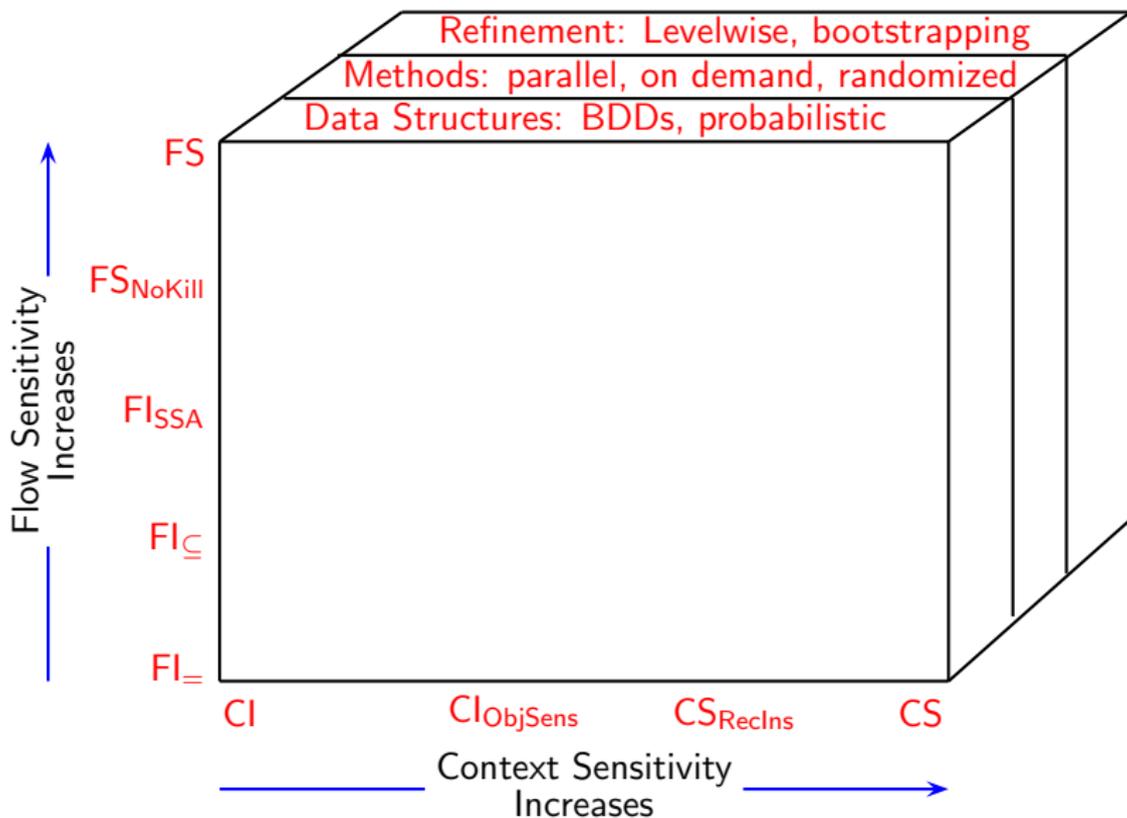
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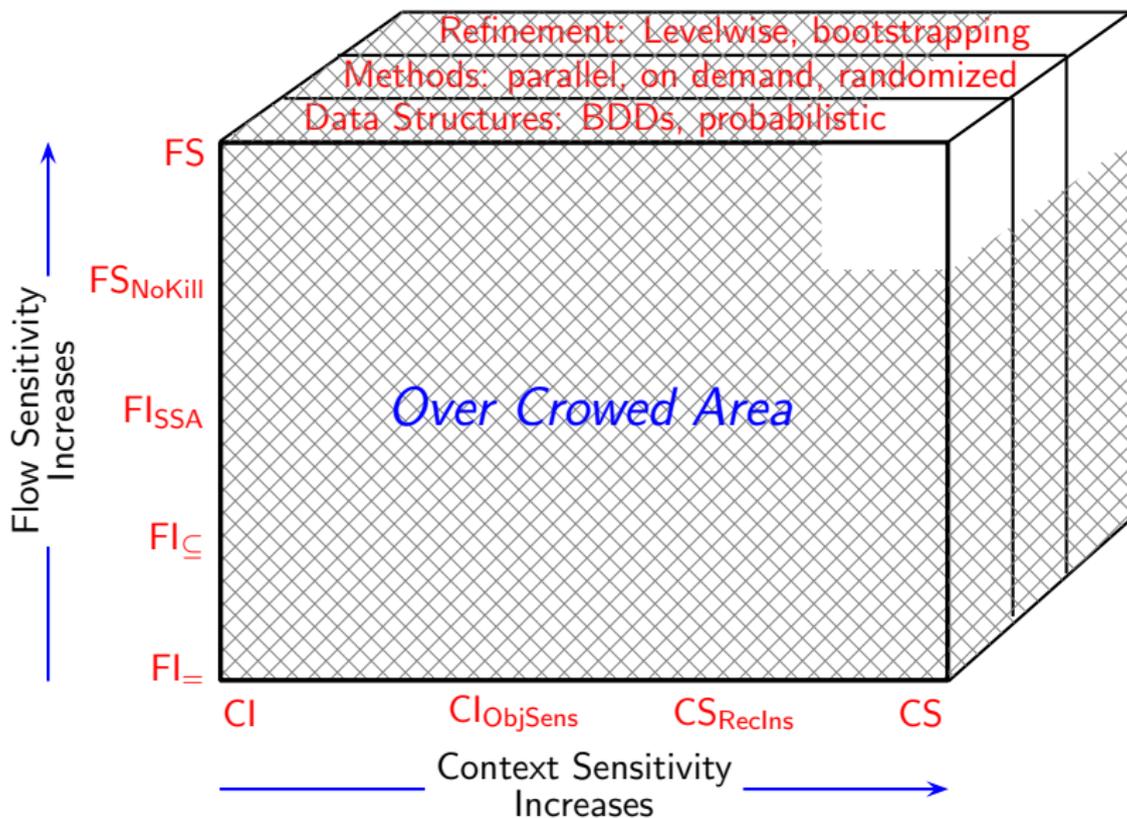
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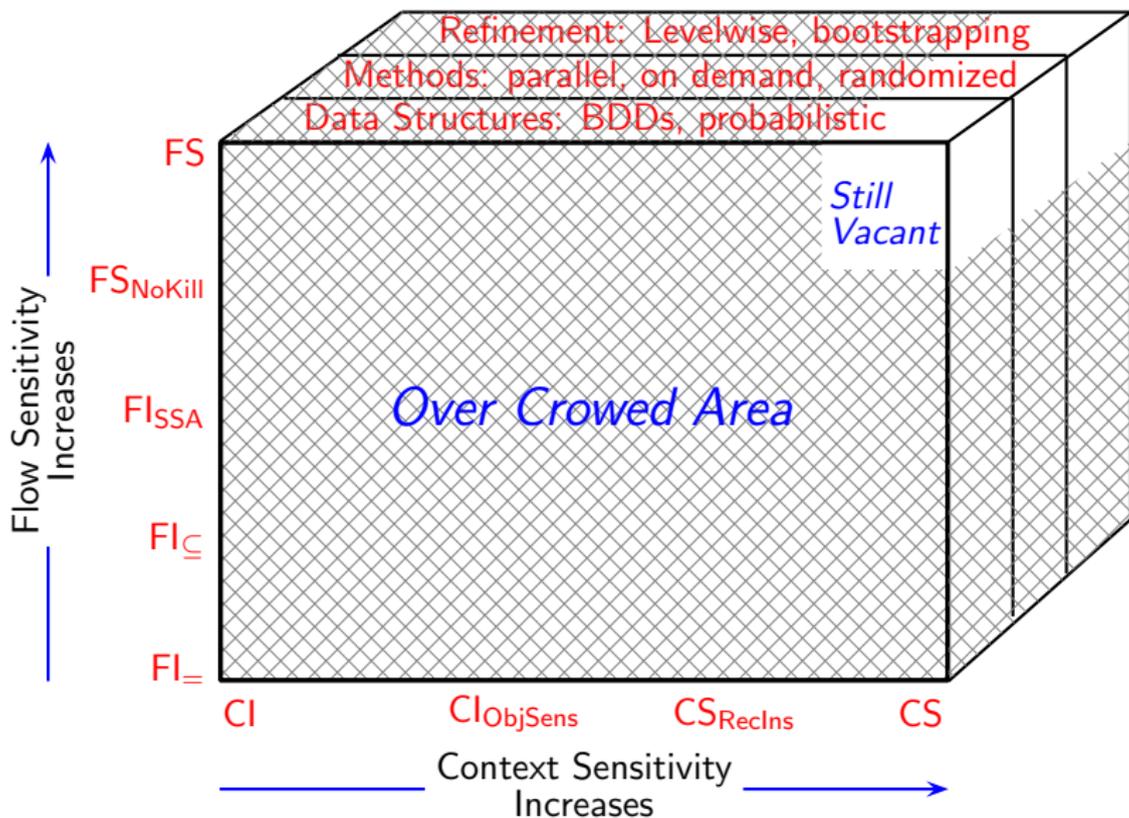
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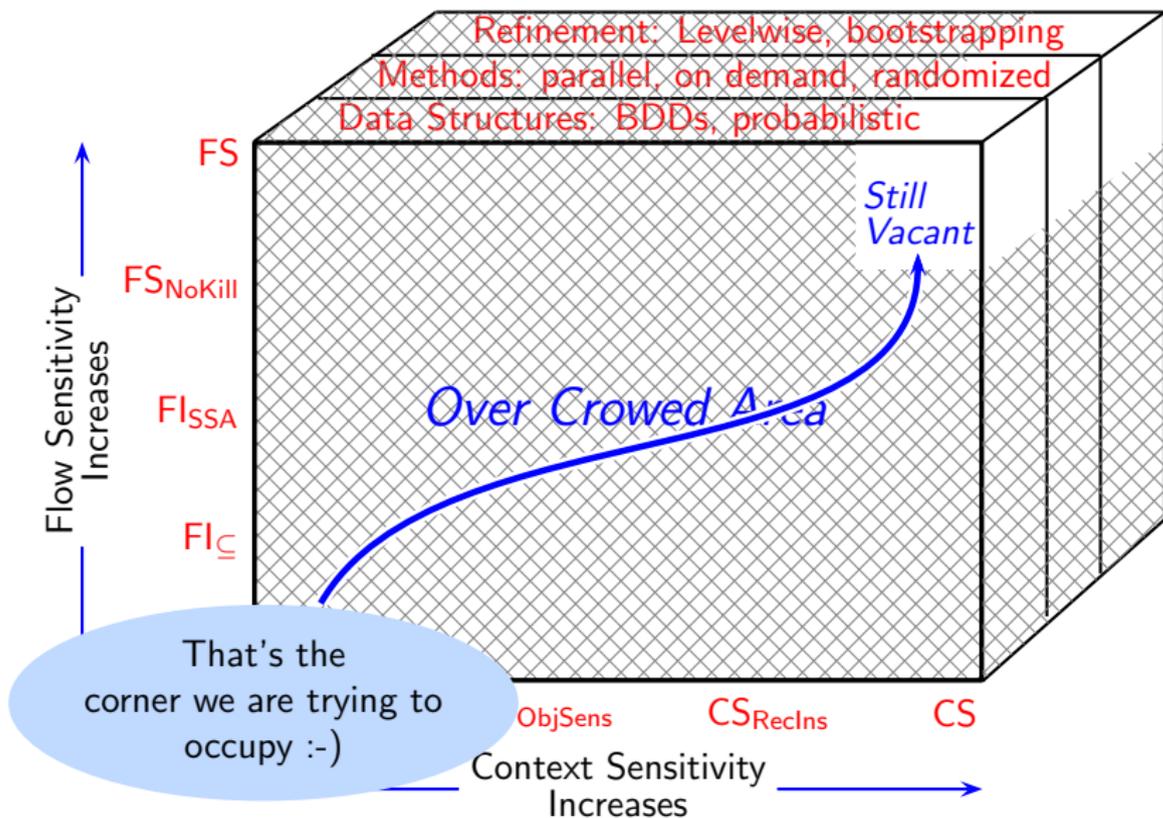
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Pointer Analysis: An Engineer's Landscape



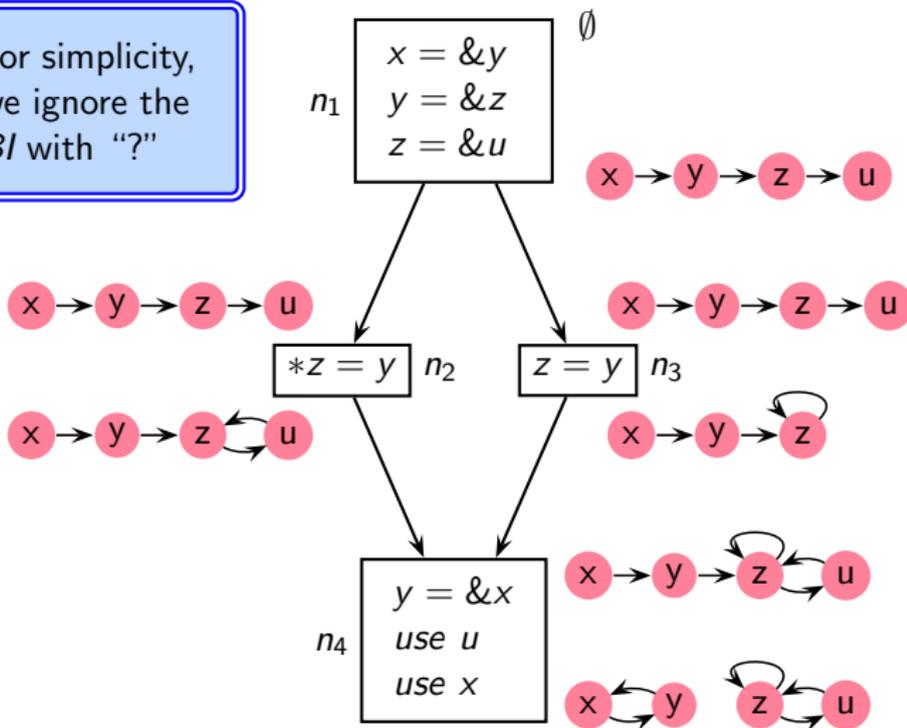
An Outline of Pointer Analysis Coverage

- The larger perspective
- Comparing Points-to and Alias information
- Flow Insensitive Points-to Analysis
- Flow Sensitive Points-to Analysis
- Pointer Analyses: An Engineer's Landscape
- Liveness Based Points-to Analysis **Next Topic**
- Generalizations to Heap, Arrays, Pointer Arithmetic, and Unions



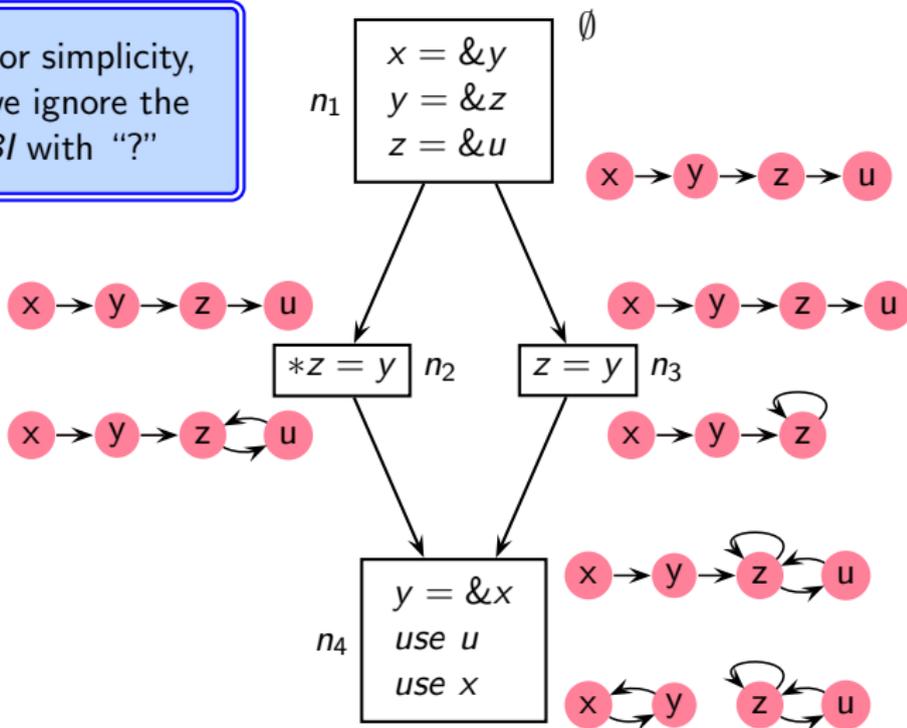
Our Motivating Example for FCPA

For simplicity,
we ignore the
BI with “?”



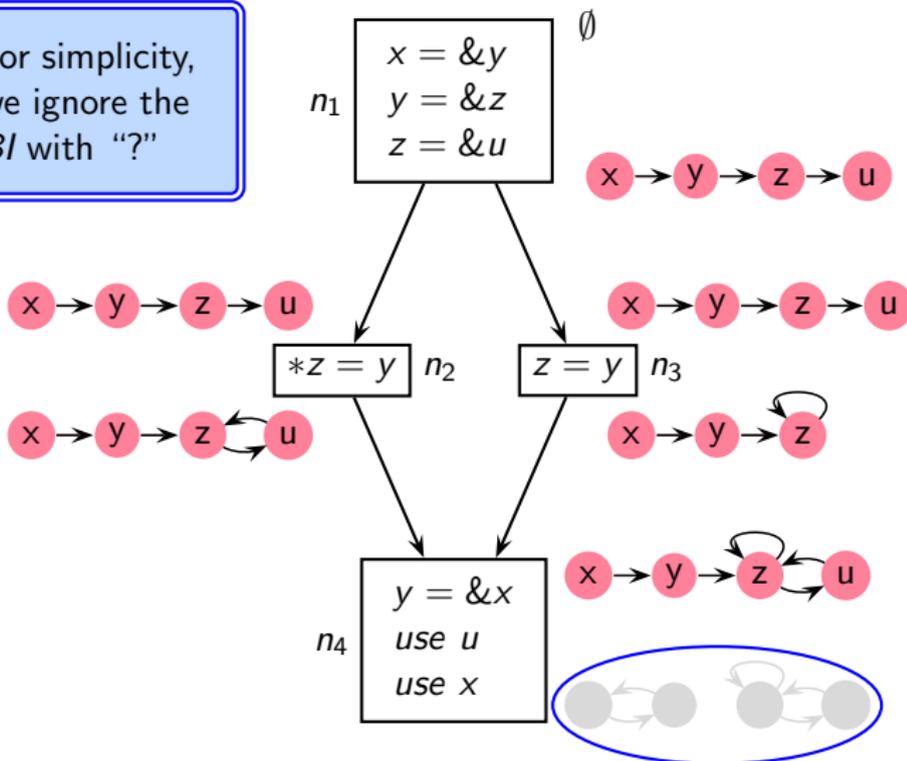
Is All This Information Useful?

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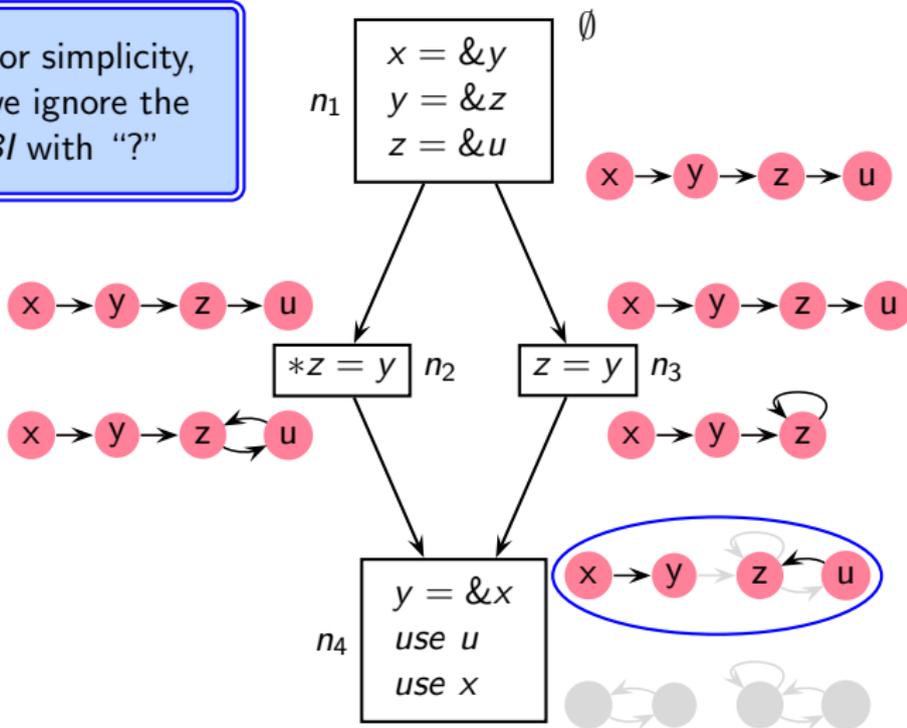
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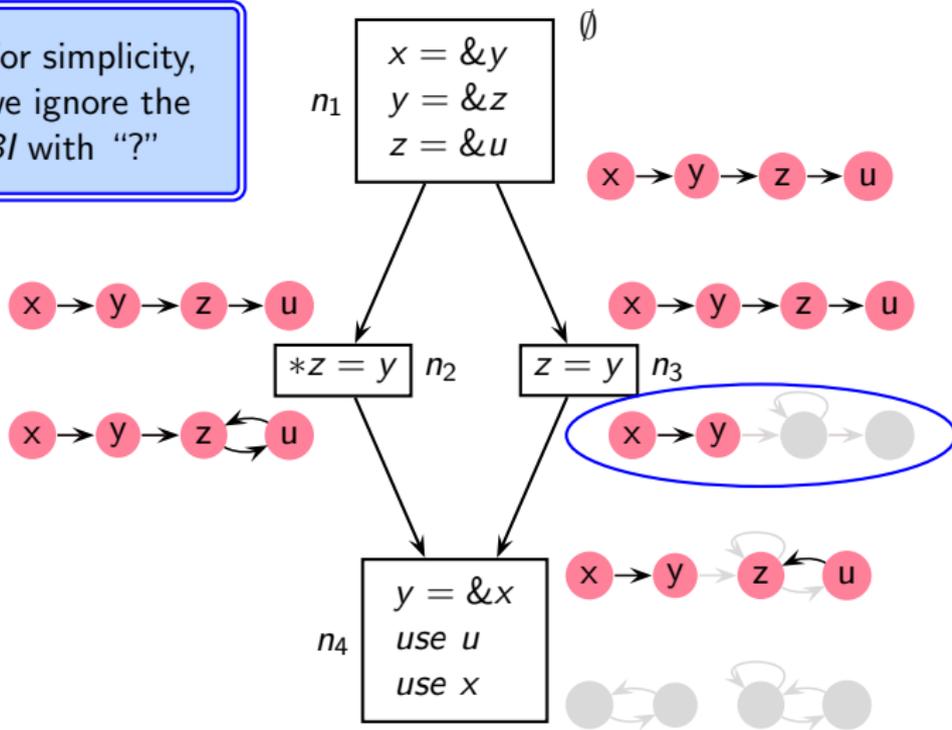
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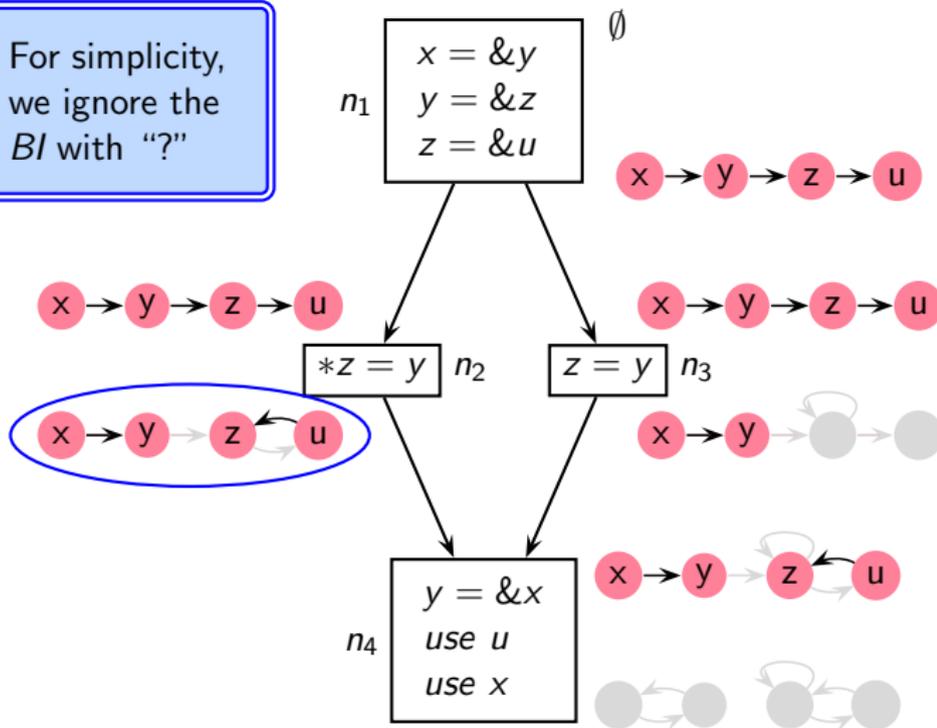
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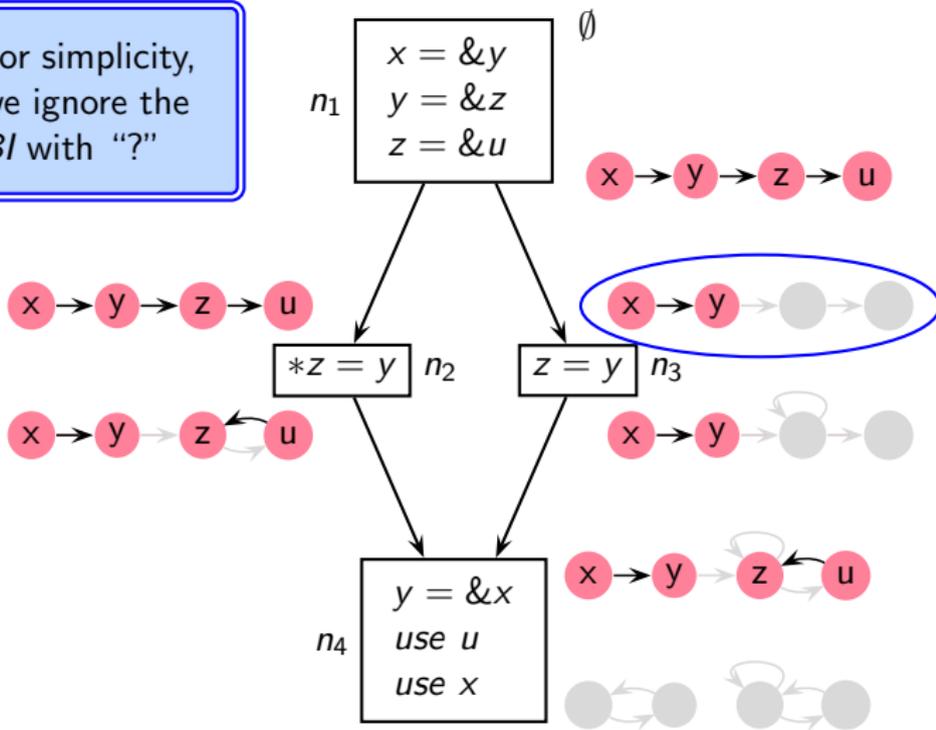
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The L and P of LFCPA

Mutual dependence of liveness and points-to information

- Define points-to information only for live pointers
- For pointer indirections, define liveness information using points-to information



The F and C of LFCPA

- Use call strings method for full flow and context sensitivity
- Use value contexts for efficient interprocedural analysis
[Khedker-Karkare-CC-2008, Padhye-Khedker-SOAP-2013]



Use of Strong Liveness

- Simple liveness considers every use of a variable as useful
- Strong liveness checks the liveness of the result before declaring the operands to be live



Use of Strong Liveness

- Simple liveness considers every use of a variable as useful
- Strong liveness checks the liveness of the result before declaring the operands to be live
- Strong liveness is more precise than simple liveness



Extractor Functions for LFCPA

Unchanged from earlier points-to analysis

Generation of strong liveness

	Def_n	$Kill_n$	$Pointee_n$	Ref_n	
				$Def_n \cap Lout_n \neq \emptyset$	otherwise
$use\ x$	\emptyset	\emptyset	\emptyset		
$x = \&a$	$\{x\}$	$\{x\}$	$\{a\}$		
$x = y$	$\{x\}$	$\{x\}$	$A\{y\}$		
$x = *y$	$\{x\}$	$\{x\}$	$A(A\{y\} \cap \mathbf{P})$		
$*x = y$	$A\{x\} \cap \mathbf{P}$	$Must(A)\{x\} \cap \mathbf{P}$	$A\{y\}$		
other	\emptyset	\emptyset	\emptyset		

- $Lin/Lout$: set of Live pointers, $Ain/Aout$: sets of mAy points-to pairs
- Ref_n , $Kill_n$, Def_n , and $Pointee_n$ are defined in terms of Ain_n



Extractor Functions for LFCPA

Unchanged from earlier points-to analysis

Generation of strong liveness

	Def_n	$Kill_n$	$Pointers_n$	Ref_n	
				$Def_n \cap Lout_n \neq \emptyset$	otherwise
<i>use x</i>	\emptyset	\emptyset	\emptyset		
$x = \&a$	$\{x\}$	$\{x\}$	$\{a\}$		
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other	\emptyset	\emptyset	\emptyset		

Pointers that become live



Extractor Functions for LFCPA

Unchanged from earlier points-to analysis

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$*x = y$	$A\{x\} \cap \mathbf{P}$	$Must(A)\{x\} \cap \mathbf{P}$	$A\{y\}$		
other	\emptyset	\emptyset	\emptyset		

Defined pointers must be live at the exit for the read pointers to become live



Extractor Functions for LFCPA

Unchanged from earlier points-to analysis

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$*x = y$	$A\{x\} \cap \mathbf{P}$	$Must(A)\{x\} \cap \mathbf{P}$	$A\{y\}$		
other	\emptyset	\emptyset	\emptyset		

Some pointers
are unconditionally
live



Extractor Functions for LFCPA

Unchanged from earlier points-to analysis

Generation of strong liveness

	Def_n	$Kill_n$	$Pointee_n$	Ref_n	
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<i>use x</i>	\emptyset	\emptyset	\emptyset	$\{x\}$	$\{x\}$
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other	\emptyset	\emptyset	\emptyset		

x is
unconditionally
live



Extractor Functions for LFCPA

Unchanged from earlier points-to analysis

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	Def_n	$Kill_n$	$Pointee_n$	Ref_n	
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$x = \&a$	$\{x\}$	$\{x\}$	$\{a\}$	\emptyset	\emptyset
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y is live
if defined pointers
are live



Extractor Functions for LFCPA

Unchanged from earlier points-to analysis

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	Def_n	$Kill_n$	$Pointee_n$	Ref_n	
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$x = *y$	$\{x\}$	$\{x\}$	$A(A\{y\} \cap \mathbf{P})$	$\{y\} \cup A\{y\} \cap \mathbf{P}$	
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y and its
pointees in Ain_n are
live if defined pointers
are live



Extractor Functions for LFCPA

Unchanged from earlier points-to analysis

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$x = y$	$\{x\}$	$\{x\}$	$A\{y\}$	$\{y\}$	\emptyset
$x = *y$	$\{x\}$	$\{x\}$	$A(A\{y\} \cap \mathbf{P})$	$\{y\} \cup A\{y\} \cap \mathbf{P}$	\emptyset
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Extractor Functions for LFCPA

Unchanged from earlier points-to analysis

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$x = y$	$\{x\}$	$\{x\}$	$A\{y\}$	$\{y\}$	\emptyset
$x = *y$	$\{x\}$	$\{x\}$	$A(A\{y\} \cap \mathbf{P})$	$\{y\} \cup A\{y\} \cap \mathbf{P}$	\emptyset
$*x = y$	$A\{x\} \cap \mathbf{P}$	$Must(A)\{x\} \cap \mathbf{P}$	$A\{y\}$	$\{x, y\}$	
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y is live
if defined pointers
are live



Extractor Functions for LFCPA

Unchanged from earlier points-to analysis

Generation of strong liveness

	Def_n	$Kill_n$	$Pointee_n$	Ref_n	
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x is
unconditionally
live



Extractor Functions for LFCPA

Unchanged from earlier points-to analysis

Generation of strong liveness

	Def_n	$Kill_n$	$Pointee_n$	Ref_n	
				$Def_n \cap Lout_n \neq \emptyset$	otherwise
$use\ x$	\emptyset	\emptyset	\emptyset	$\{x\}$	$\{x\}$
$x = \&a$	$\{x\}$	$\{x\}$	$\{a\}$	\emptyset	\emptyset
$x = y$	$\{x\}$	$\{x\}$	$A\{y\}$	$\{y\}$	\emptyset
$x = *y$	$\{x\}$	$\{x\}$	$A(A\{y\} \cap \mathbf{P})$	$\{y\} \cup A\{y\} \cap \mathbf{P}$	\emptyset
$*x = y$	$A\{x\} \cap \mathbf{P}$	$Must(A)\{x\} \cap \mathbf{P}$	$A\{y\}$	$\{x, y\}$	$\{x\}$
other	\emptyset	\emptyset	\emptyset	\emptyset	\emptyset



Deriving *Must* Points-to for LFCPA

For $*x = y$, unless the pointees of x are known

- points-to propagation should be blocked
- liveness propagation should be blocked

to ensure monotonicity

$$Must(R) = \bigcup_{x \in \mathbf{P}} \{x\} \times \begin{cases} \text{Var} & R\{x\} = \emptyset \vee R\{x\} = \{?\} \\ \{y\} & R\{x\} = \{y\} \wedge y \neq ? \\ \emptyset & \text{otherwise} \end{cases}$$



LFCPA Data Flow Equations

$$Lout_n = \begin{cases} \emptyset & n \text{ is } End_p \\ \bigcup_{s \in succ(n)} Lin_s & \text{otherwise} \end{cases}$$

$$Lin_n = (Lout_n - Kill_n) \cup Ref_n$$

$$Ain_n = \begin{cases} Lin_n \times \{?\} & n \text{ is } Start_p \\ \left(\bigcup_{p \in pred(n)} Aout_p \right) \Big|_{Lin_n} & \text{otherwise} \end{cases}$$

$$Aout_n = \left((Ain_n - (Kill_n \times \mathbb{V}ar)) \cup (Def_n \times Pointee_n) \right) \Big|_{Lout_n}$$

- *Lin/Lout*: set of Live pointers
- *Ain/Aout*: definitions remain unchanged except for restriction to liveness



LFCPA Data Flow Equations

$$\begin{aligned}
 Lout_n &= \begin{cases} \emptyset & n \text{ is } \text{Exit}_p \\ \bigcup_{s \in \text{succ}(n)} Lin_s & \text{otherwise} \end{cases} \\
 Lin_n &= (Lout_n - Kill_n) \cup Ref_n \\
 Ain_n &= \begin{cases} Lin_n \times \{?\} & n \text{ is } \text{Start}_p \\ \left(\bigcup_{p \in \text{pred}(n)} Aout_p \right) \Big|_{Lin_n} & \text{otherwise} \end{cases} \\
 Aout_n &= \left((Ain_n - (Kill_n \times \text{Var})) \cup (Def_n \times \text{Pointee}_n) \right) \Big|_{Lout_n}
 \end{aligned}$$

$Kill_n$ defined in terms of Ain_n

- $Lin/Lout$: set of Live pointers
- $Ain/Aout$: definitions remain unchanged except for restriction to liveness



LFCPA Data Flow Equations

$$Lout_n = \begin{cases} \emptyset & n \text{ is } End_p \\ \bigcup_{s \in succ(n)} Lin_s & \text{otherwise} \end{cases}$$

$$Lin_n = (Lout_n - Kill_n) \cup Ref_n$$

Ref_n defined
in terms of Ain_n
and $Lout_n$

$$Ain_n = \begin{cases} Lin_n \times \{?\} & n \text{ is } Start_p \\ \left(\bigcup_{p \in pred(n)} Aout_p \right) \Big|_{Lin_n} & \text{otherwise} \end{cases}$$

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LFCPA Data Flow Equations

$$Lout_n = \begin{cases} \emptyset & n \text{ is } End_p \\ \bigcup_{s \in succ(n)} Lin_s & \text{otherwise} \end{cases}$$

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$$Aout_n = \left((Ain_n - (Kill_n \times \mathbb{V}ar)) \cup (Def_n \times Pointee_n) \right) \Big|_{Lout_n}$$

Ain_n and $Aout_n$
 are restricted to
 Lin_n and $Lout_n$

n is $Start_p$

Lin_n

$Lout_n$

- $Lin/Lout$: set of Live pointers
- $Ain/Aout$: definitions remain unchanged except for restriction to liveness



LFCPA Data Flow Equations

$$Lout_n = \begin{cases} \emptyset & n \text{ is } End_p \\ \bigcup_{s \in succ(n)} Lin_s & \text{otherwise} \end{cases}$$

$$Lin_n = (Lout_n - Kill_n) \cup Ref_n$$

$$Ain_n = \begin{cases} Lin_n \times \{?\} & n \text{ is } Start_p \\ \left(\bigcup_{p \in pred(n)} Aout_p \right) \Big|_{Lin_n} & \text{otherwise} \end{cases}$$

$$Aout_n = \left((Ain_n - (Kill_n \times \mathbb{V}ar)) \cup (Def_n \times Pointee_n) \right) \Big|_{Lout_n}$$

BI
restricted to
live pointers

- *Lin/Lout*: set of Live pointers
- *Ain/Aout*: definitions remain unchanged except for restriction to liveness



LFCPA Data Flow Equations

$$Lout_n = \begin{cases} \emptyset & n \text{ is } End_p \\ \bigcup_{s \in succ(n)} Lin_s & \text{otherwise} \end{cases}$$

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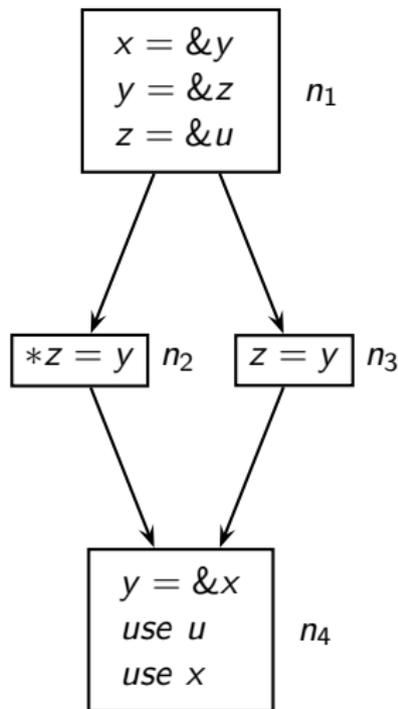


Motivating Example Revisited

- For convenience, we show complete sweeps of liveness and points-to analysis repeatedly
- This is not required by the computation
- The data flow equations define a single fixed point computation

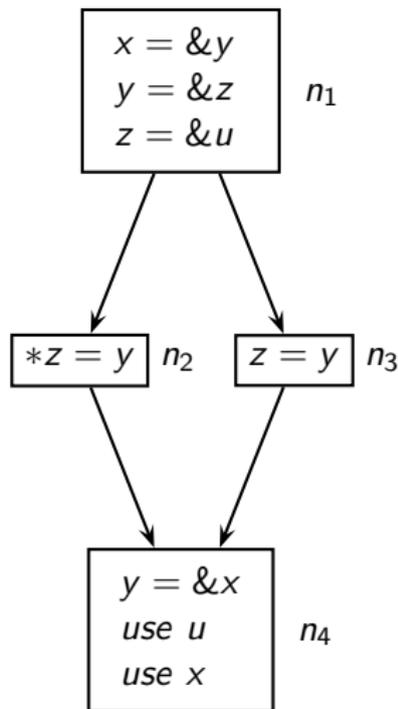


First Round of Liveness Analysis and Points-to Analysis

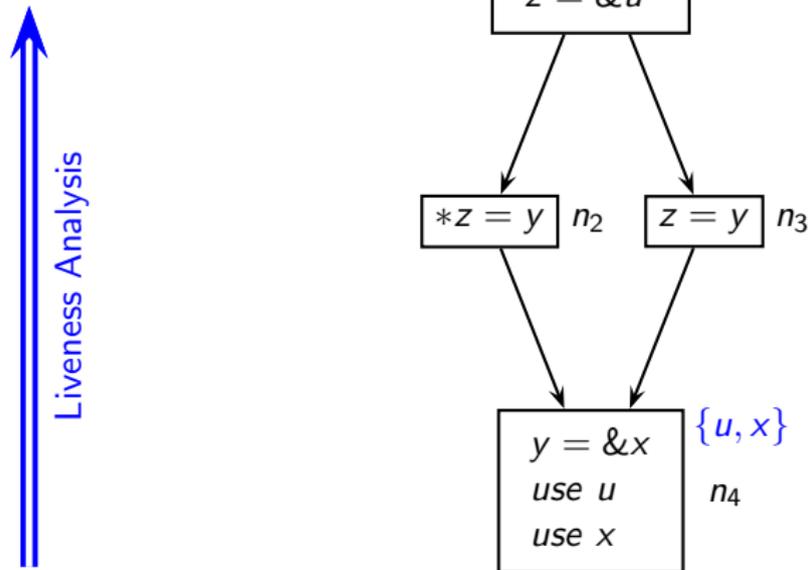


First Round of Liveness Analysis and Points-to Analysis

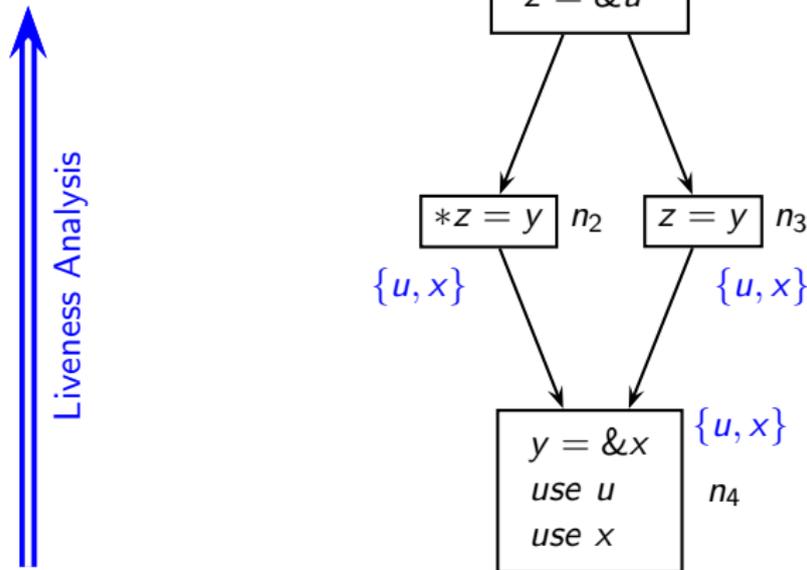
↑
Liveness Analysis



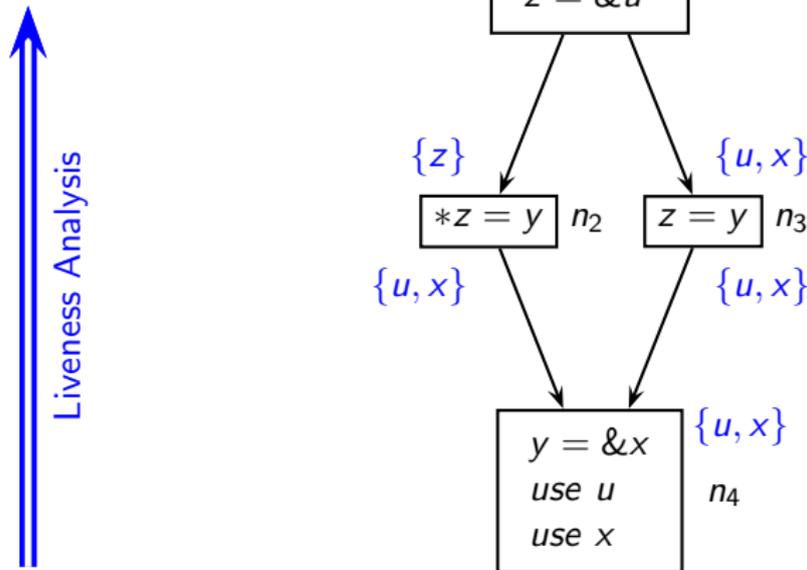
First Round of Liveness Analysis and Points-to Analysis



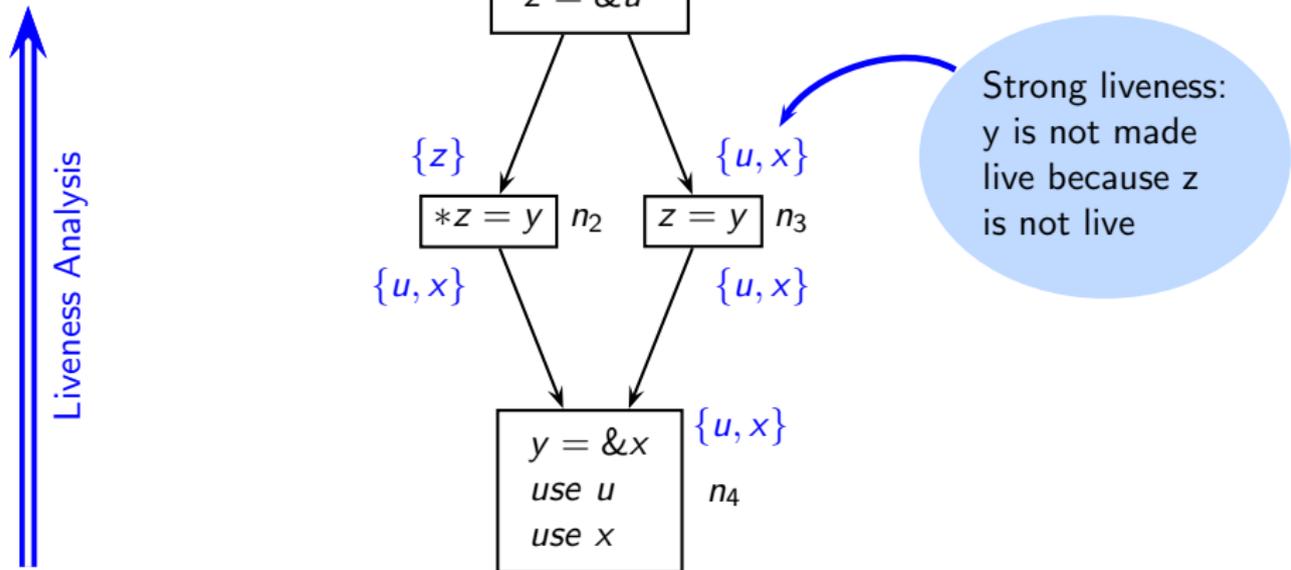
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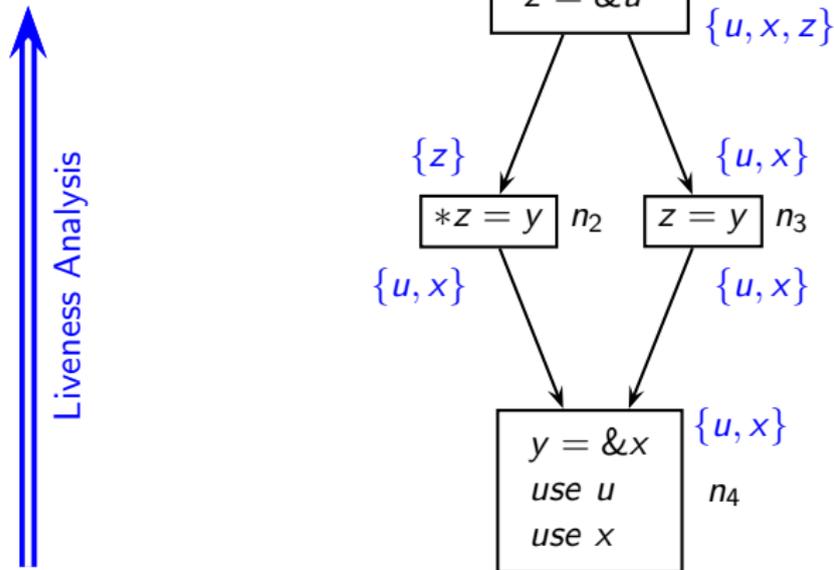
First Round of Liveness Analysis and Points-to Analysis



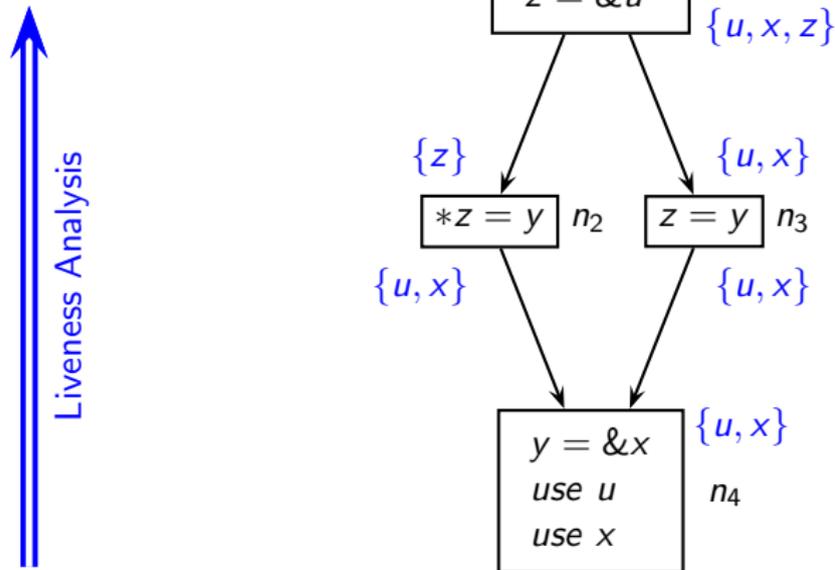
First Round of Liveness Analysis and Points-to Analysis



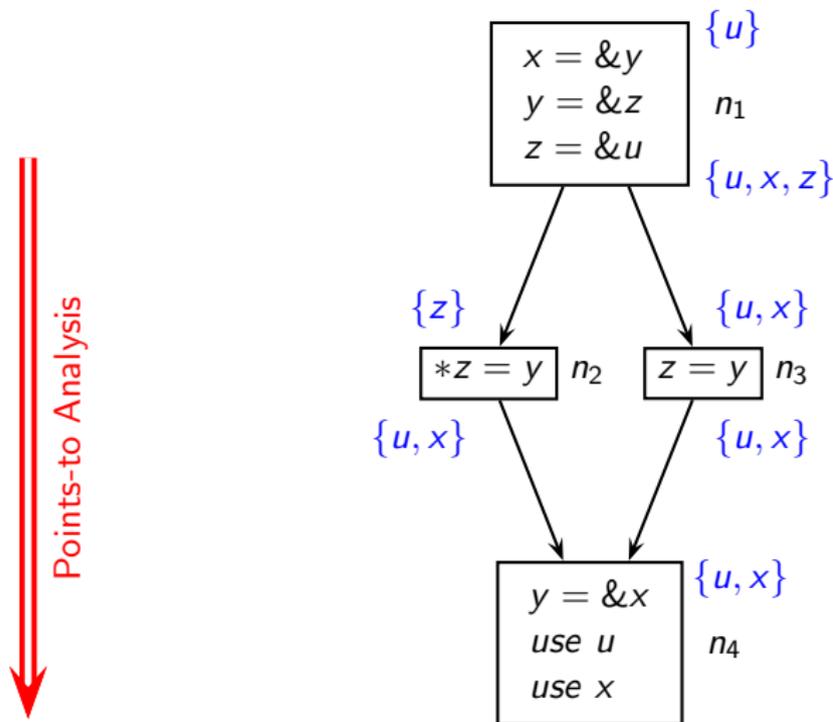
First Round of Liveness Analysis and Points-to Analysis



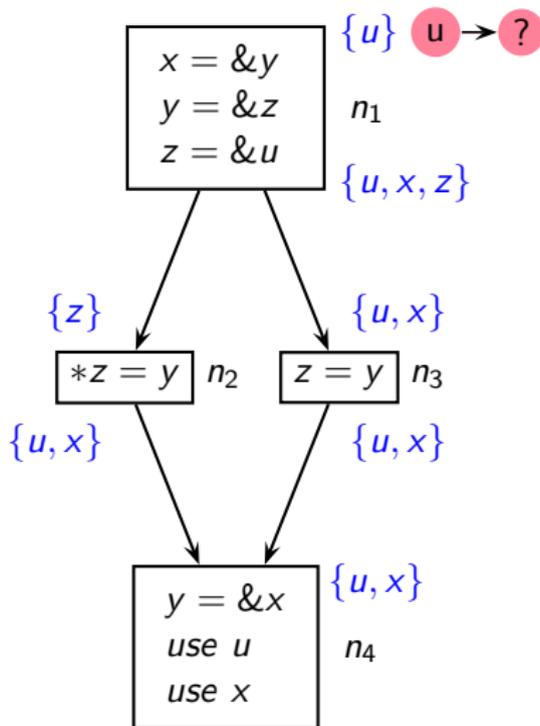
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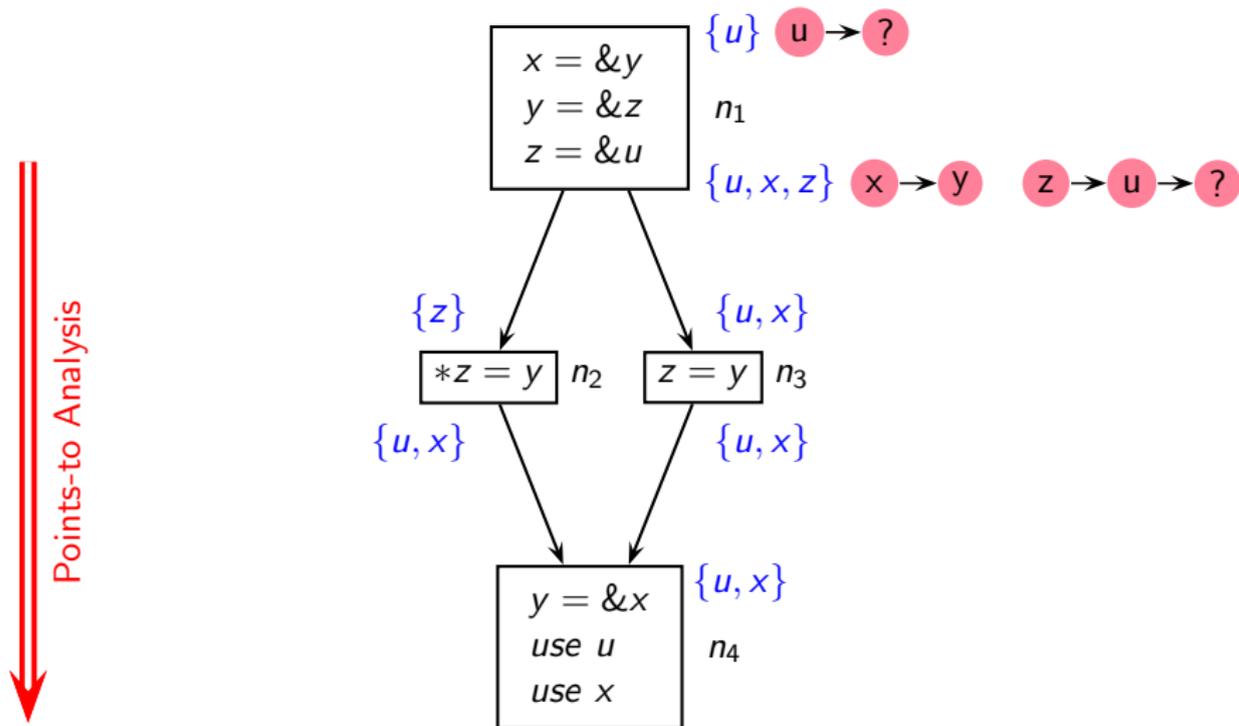
First Round of Liveness Analysis and Points-to Analysis



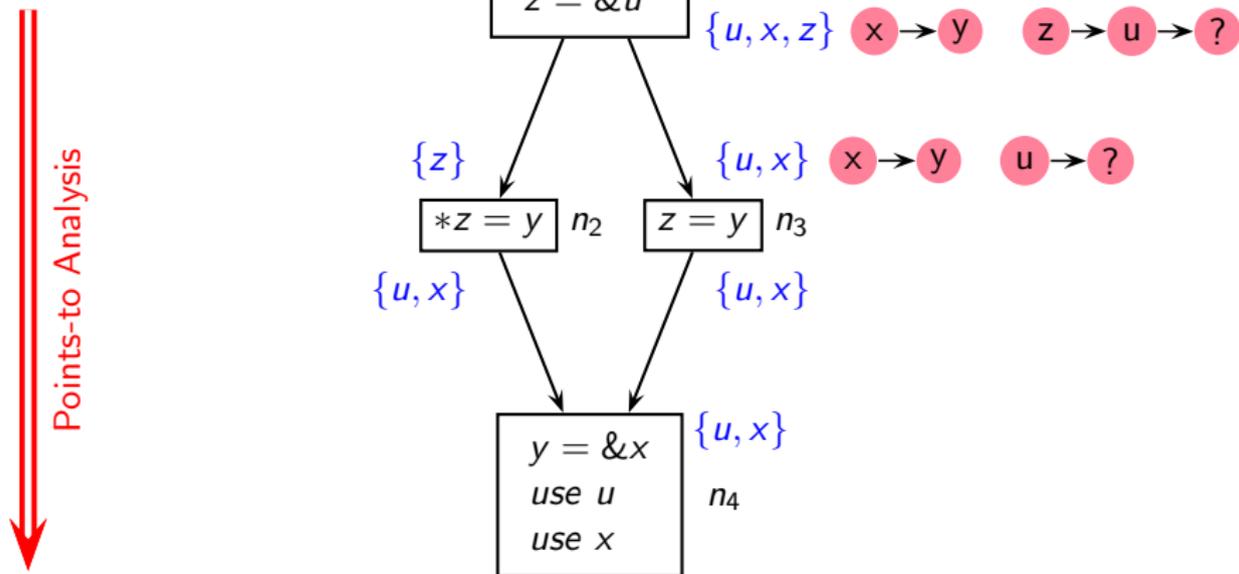
First Round of Liveness Analysis and Points-to Analysis



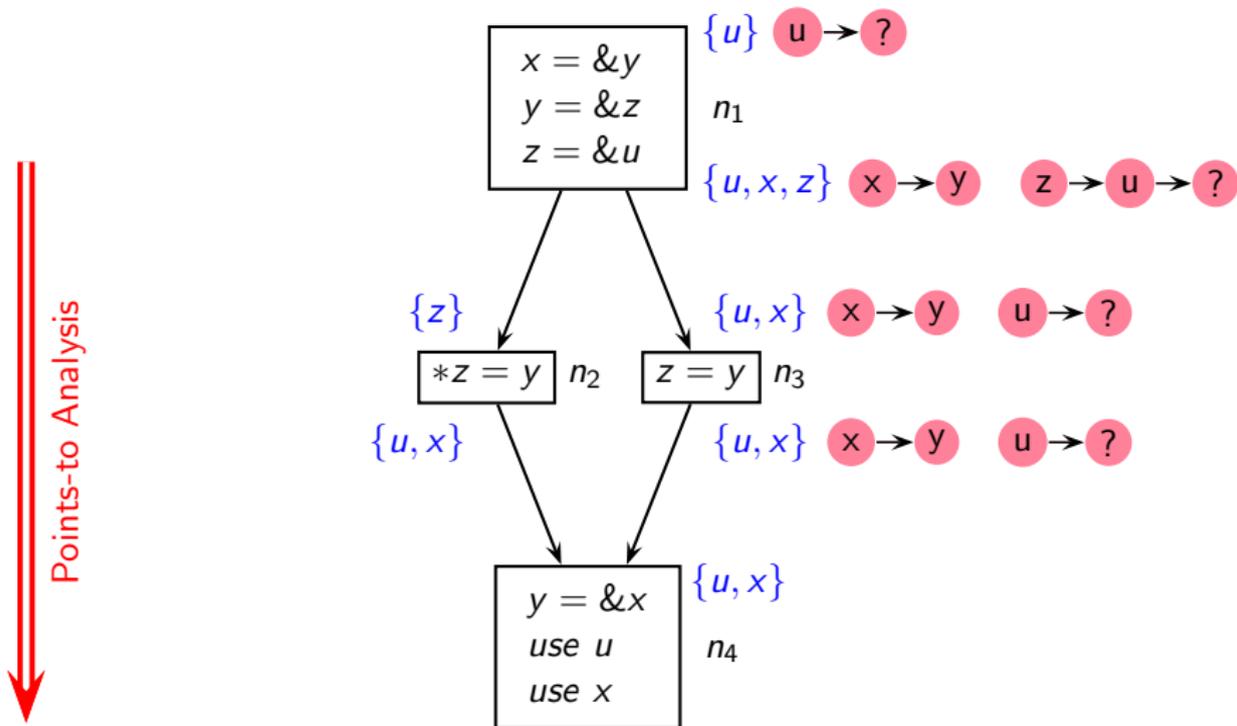
First Round of Liveness Analysis and Points-to Analysis



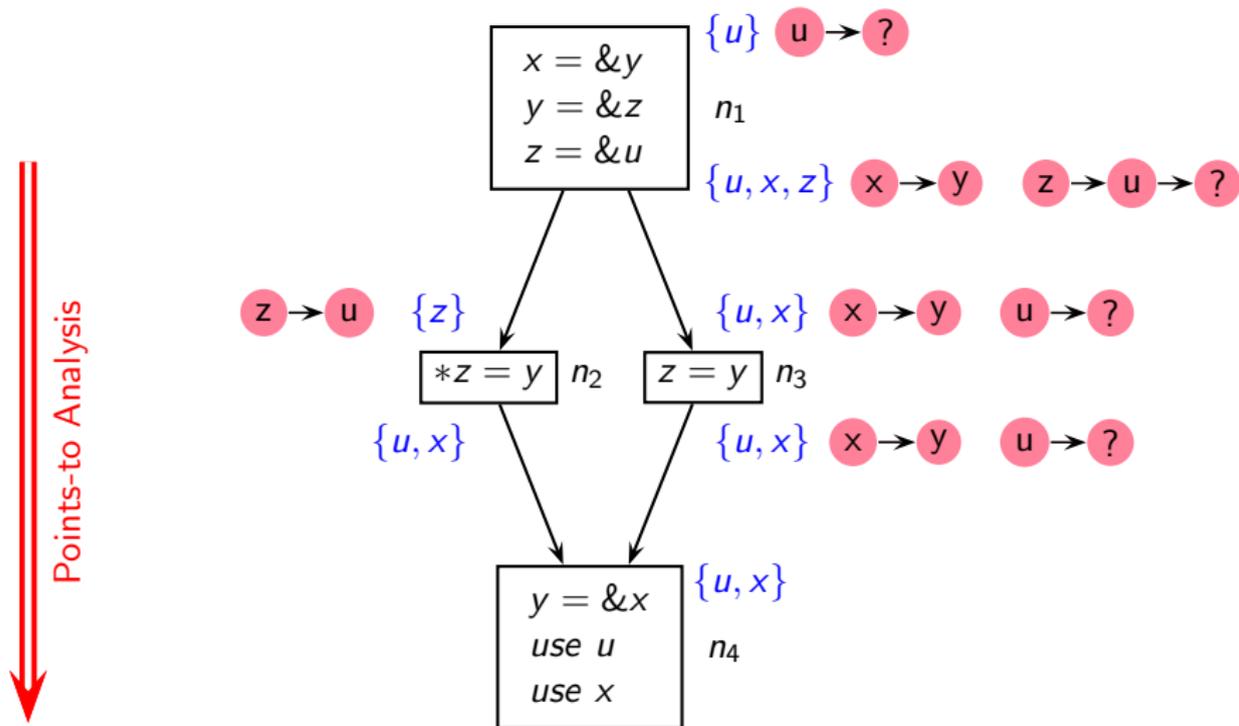
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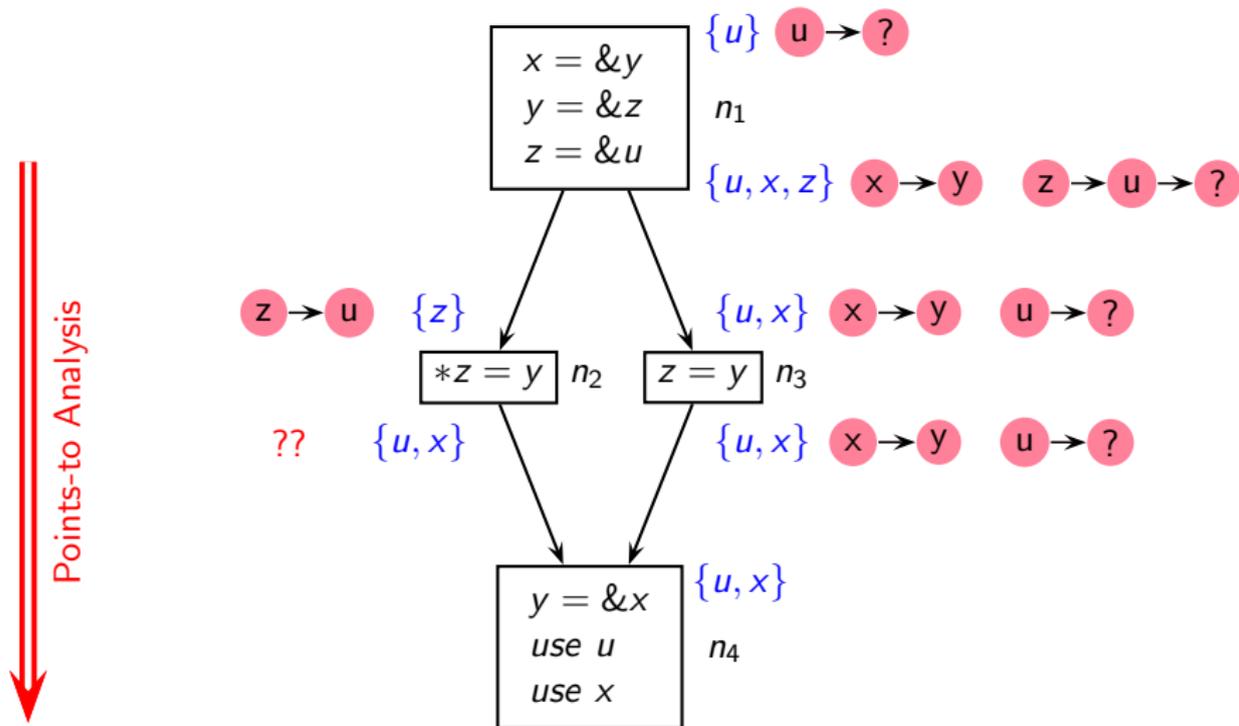
First Round of Liveness Analysis and Points-to Analysis



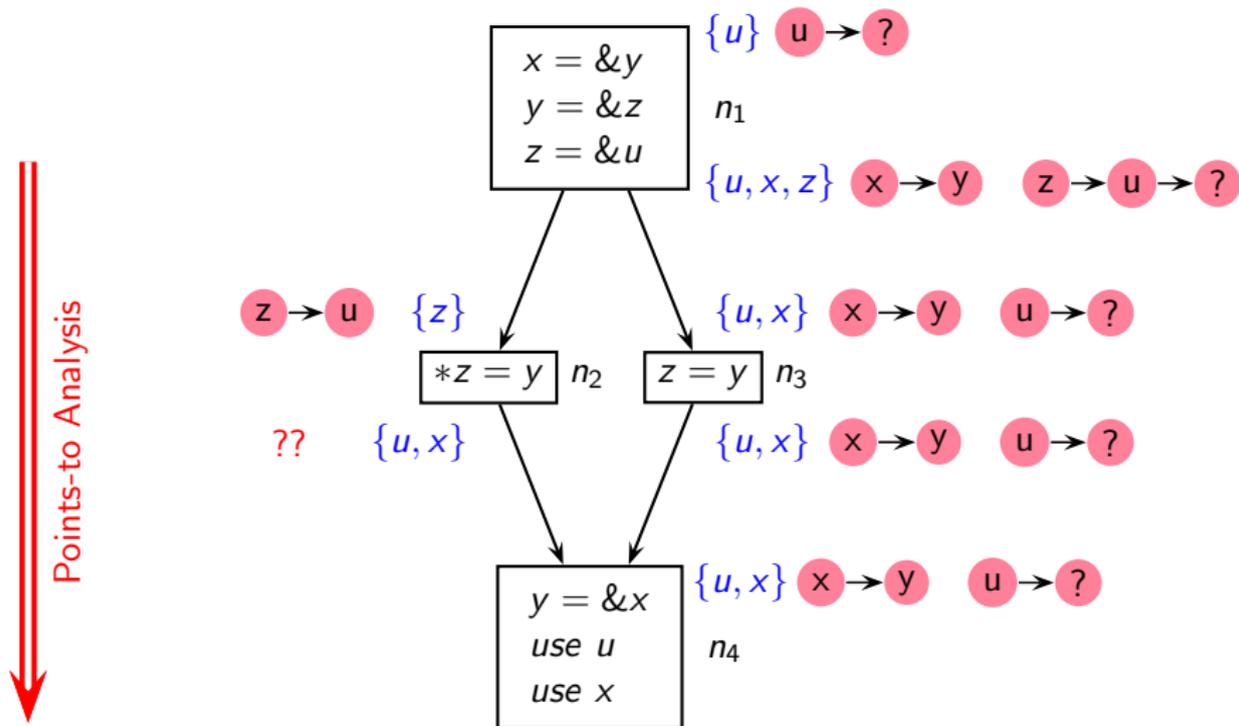
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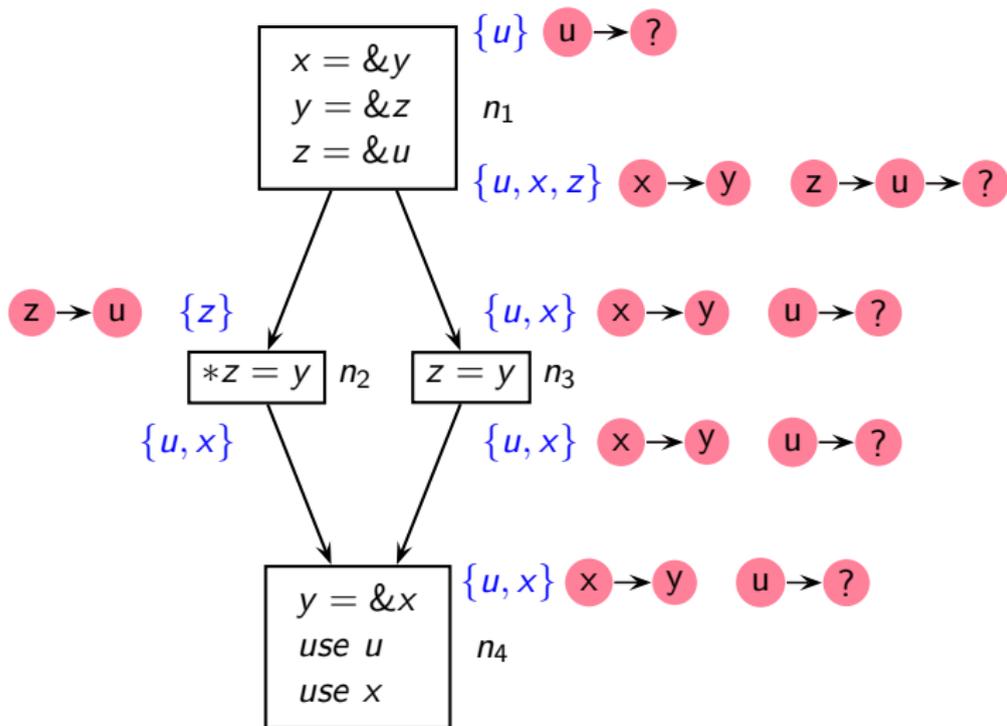
First Round of Liveness Analysis and Points-to Analysis



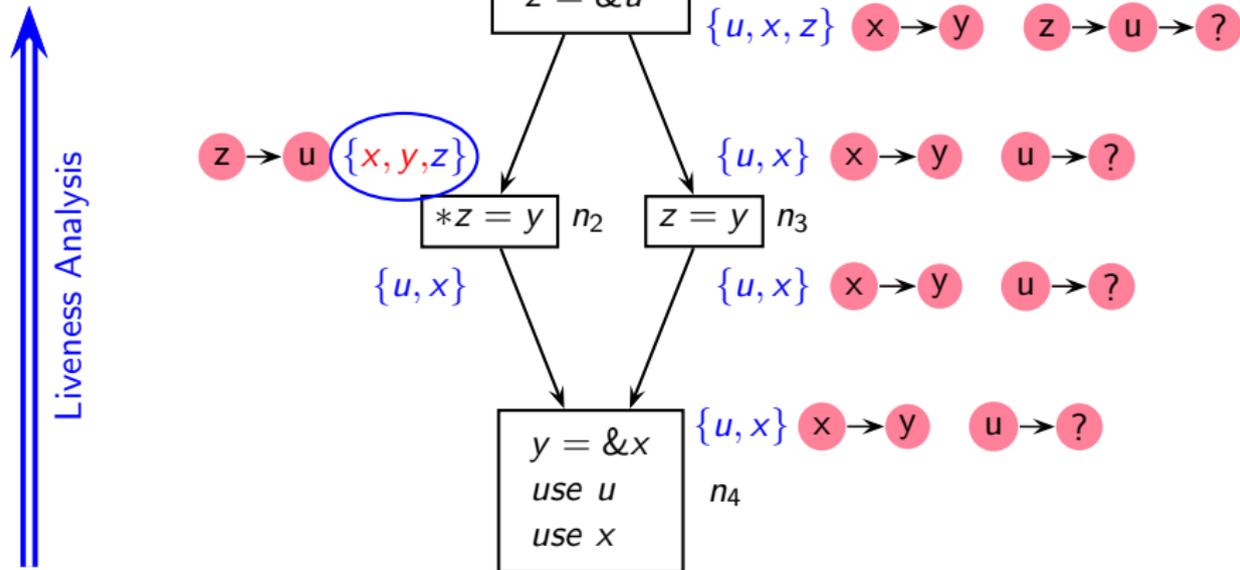
First Round of Liveness Analysis and Points-to Analysis



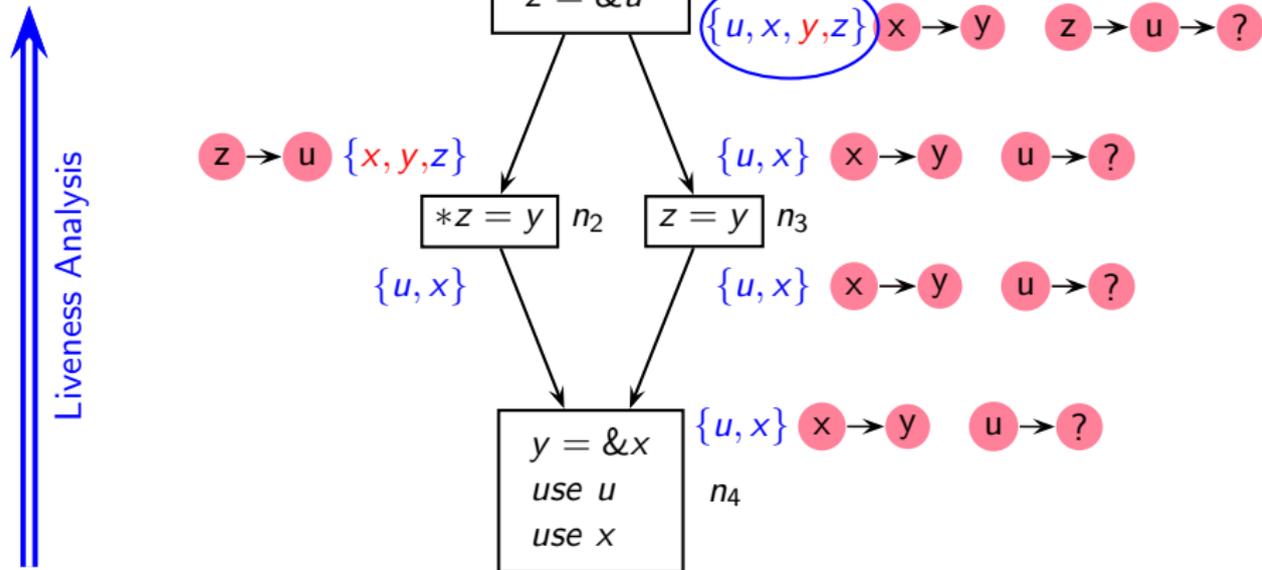
Second Round of Liveness Analysis and Points-to Analysis



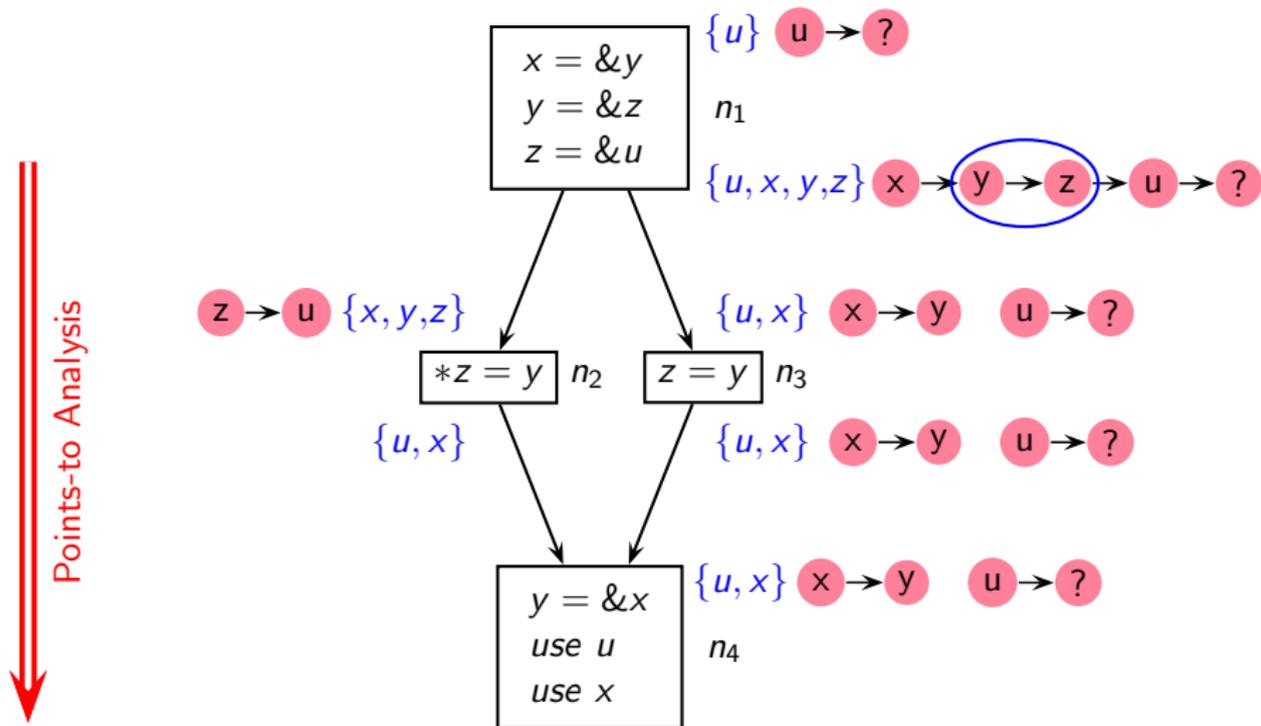
Second Round of Liveness Analysis and Points-to Analysis



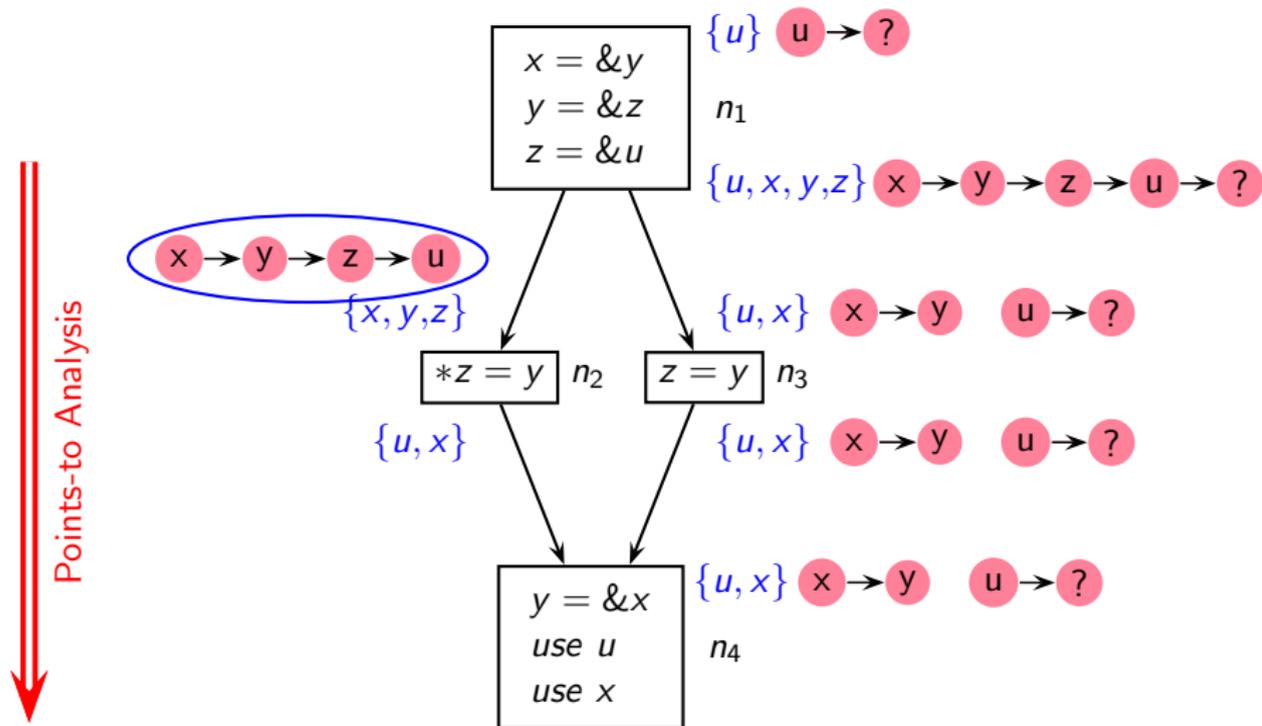
Second Round of Liveness Analysis and Points-to Analysis



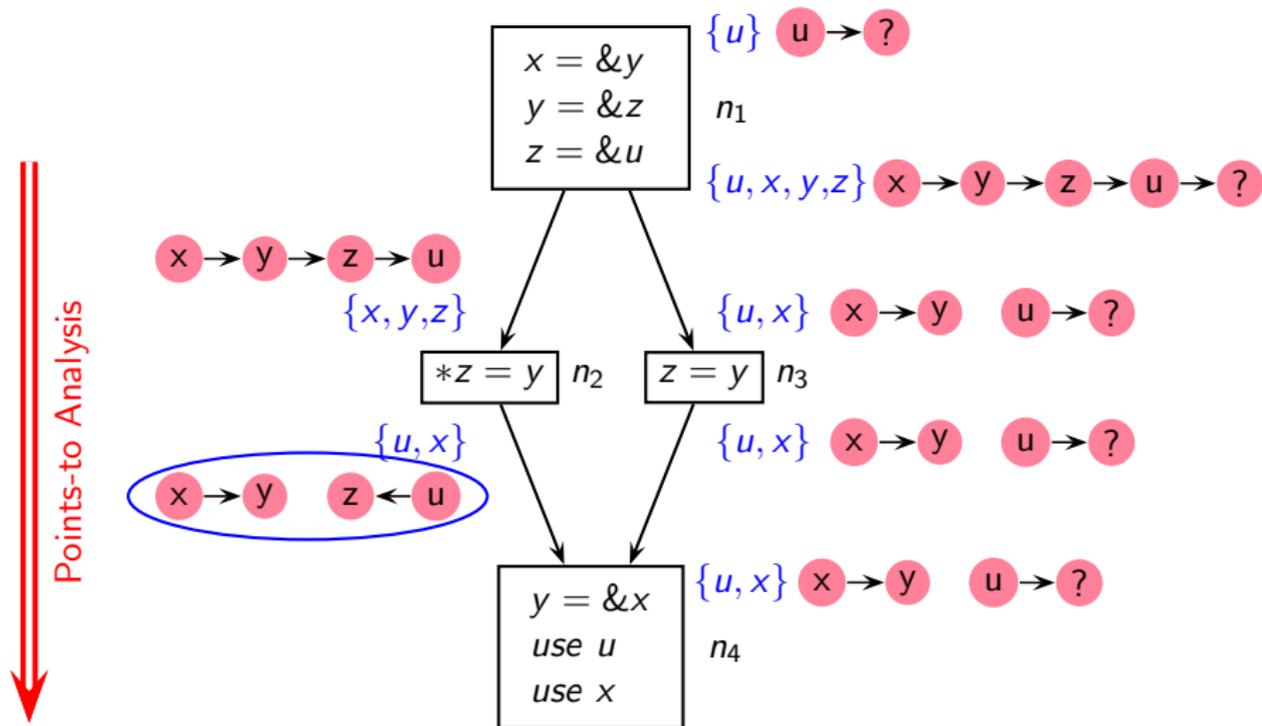
Second Round of Liveness Analysis and Points-to Analysis



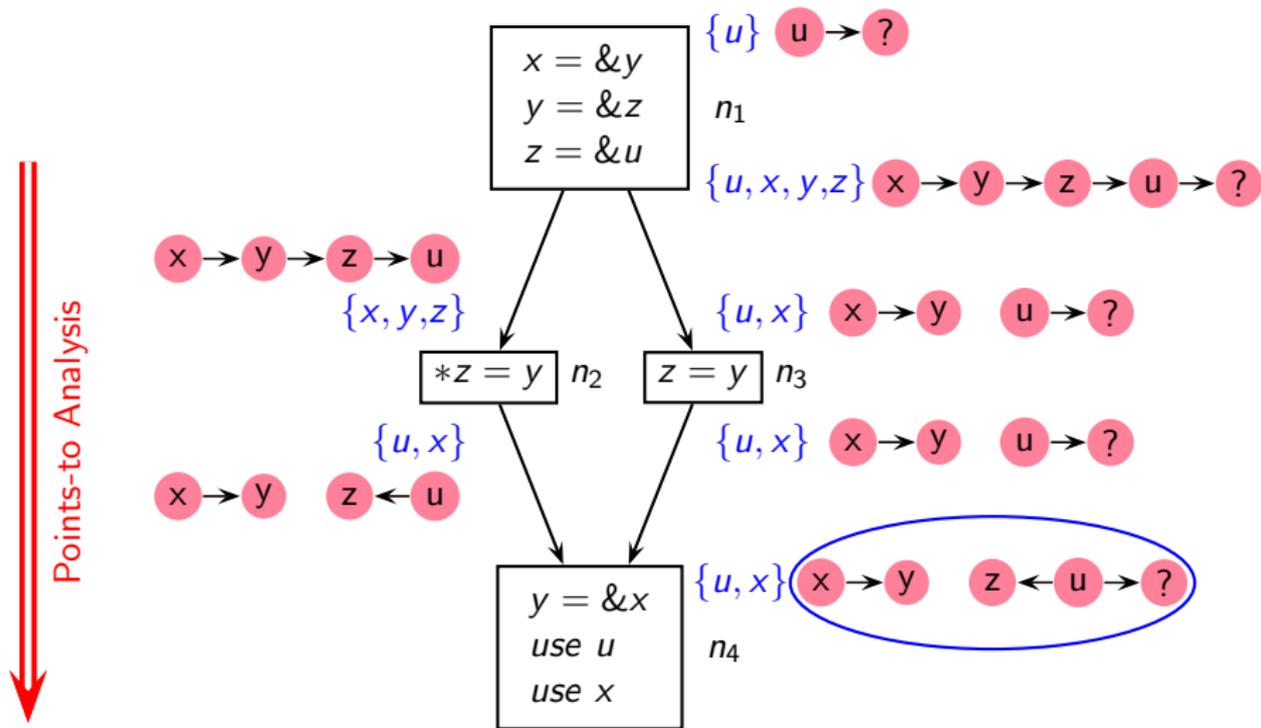
Second Round of Liveness Analysis and Points-to Analysis



Second Round of Liveness Analysis and Points-to Analysis



Second Round of Liveness Analysis and Points-to Analysis



LFCPA Implementation

- LTO framework of GCC 4.6.0
- Naive prototype implementation
(Points-to sets implemented using linked lists)
- Implemented FCPA without liveness for comparison
- Comparison with GCC's flow and context insensitive method
- SPEC 2006 benchmarks



Analysis Time

Program	kLoC	Call Sites	Time in milliseconds			
			L-FCPA		FCPA	GPTA
			Liveness	Points-to		
lbm	0.9	33	0.55	0.52	1.9	5.2
mcf	1.6	29	1.04	0.62	9.5	3.4
libquantum	2.6	258	2.0	1.8	5.6	4.8
bzip2	3.7	233	4.5	4.8	28.1	30.2
parser	7.7	1123	1.2×10^3	145.6	4.3×10^5	422.12
sjeng	10.5	678	858.2	99.0	3.2×10^4	38.1
hmmer	20.6	1292	90.0	62.9	2.9×10^5	246.3
h264ref	36.0	1992	2.2×10^5	2.0×10^5	?	4.3×10^3

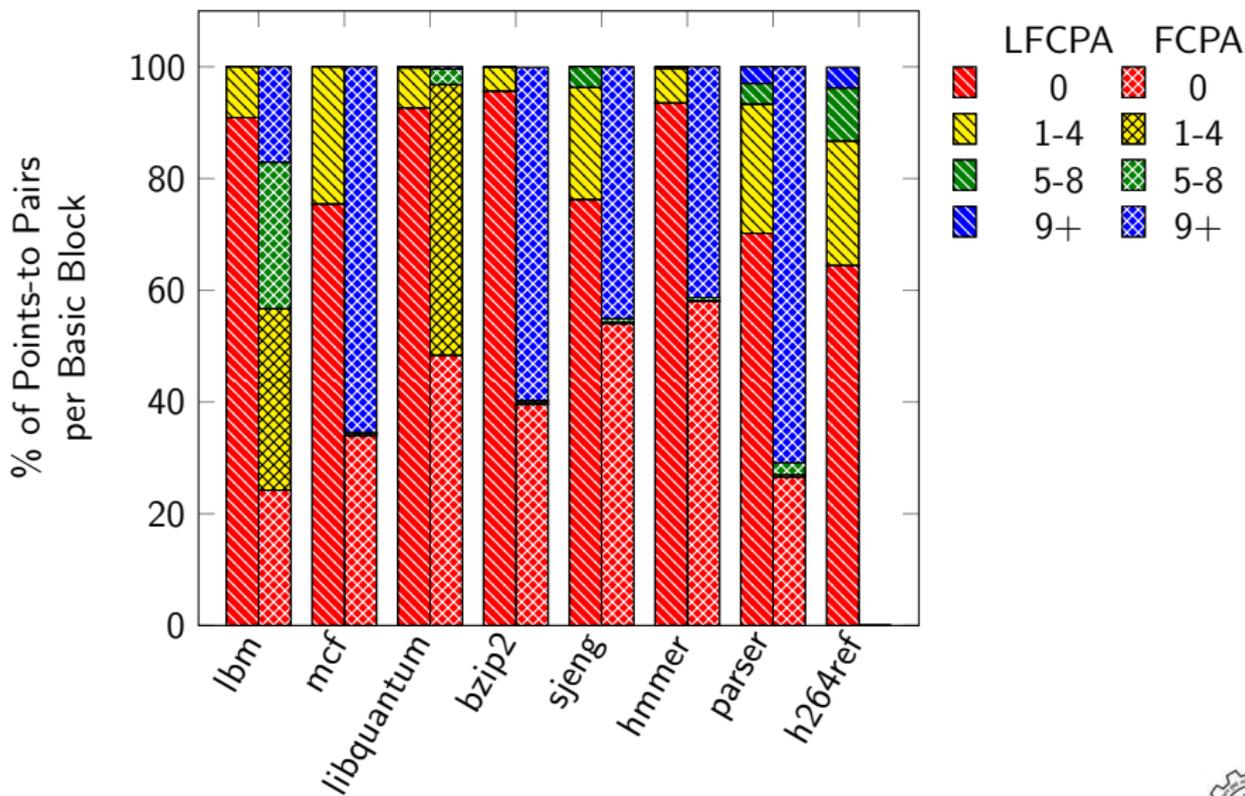


Unique Points-to Pairs

Program	kLoC	Call Sites	Unique points-to pairs		
			L-FCPA	FCPA	GPTA
lbn	0.9	33	12	507	1911
mcf	1.6	29	41	367	2159
libquantum	2.6	258	49	119	2701
bzip2	3.7	233	60	210	8.8×10^4
parser	7.7	1123	531	4196	1.9×10^4
sjeng	10.5	678	267	818	1.1×10^4
hmmer	20.6	1292	232	5805	1.9×10^6
h264ref	36.0	1992	1683	?	1.6×10^7



Points-to Information is Small and Sparse



LFCPA Observations

- Usable pointer information is very small and sparse
- Data flow propagation in real programs seems to involve only a small subset of all possible data flow values
- Earlier approaches reported inefficiency and non-scalability because they computed far more information than the actual usable information



LFCPA Conclusions

- Building quick approximations and compromising on precision may not be necessary for efficiency
- Building clean abstractions to separate the necessary information from redundant information is much more significant



LFCPA Conclusions

- Building quick approximations and compromising on precision may not be necessary for efficiency
- Building clean abstractions to separate the necessary information from redundant information is much more significant

Our experience of points-to analysis shows that

- ▶ Use of liveness reduced the pointer information ...
- ▶ which reduced the number of contexts required ...
- ▶ which reduced the liveness and pointer information ...



LFCPA Conclusions

- Building quick approximations and compromising on precision may not be necessary for efficiency
- Building clean abstractions to separate the necessary information from redundant information is much more significant

Our experience of points-to analysis shows that

- ▶ Use of liveness reduced the pointer information ...
 - ▶ which reduced the number of contexts required ...
 - ▶ which reduced the liveness and pointer information ...
- Approximations should come *after* building abstractions rather than *before*



LFCPA Lessons: The Larger Perspective

exhaustive
computation

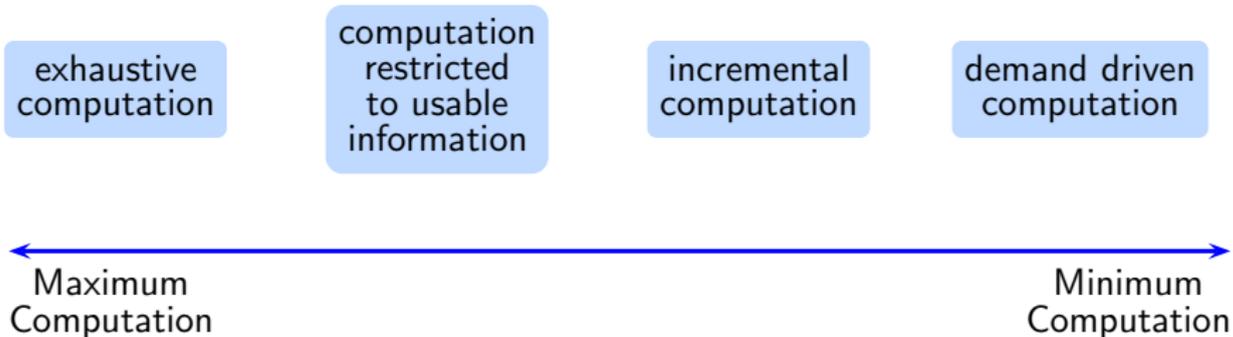
computation
restricted
to usable
information

incremental
computation

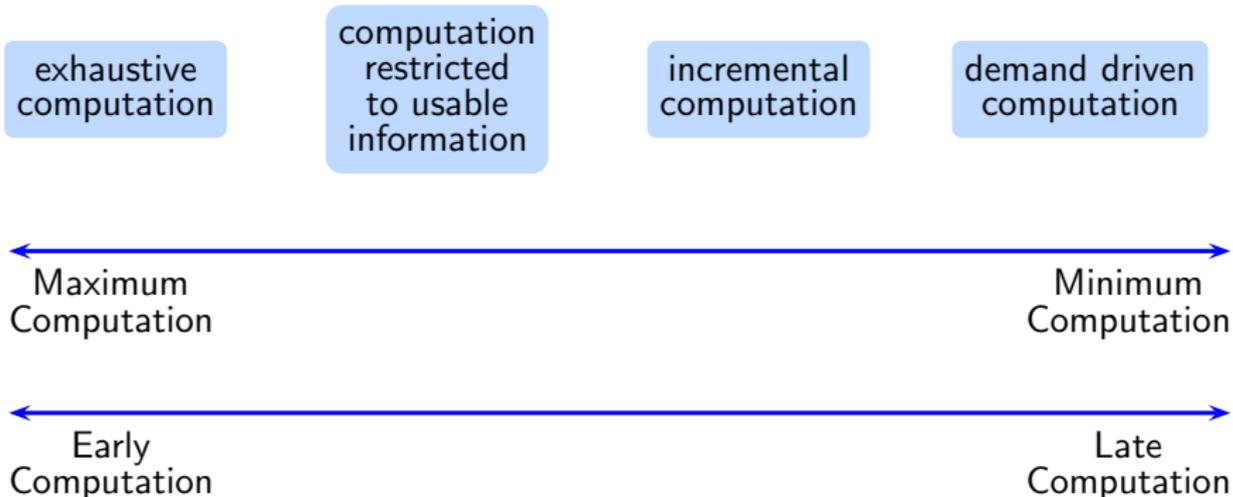
demand driven
computation



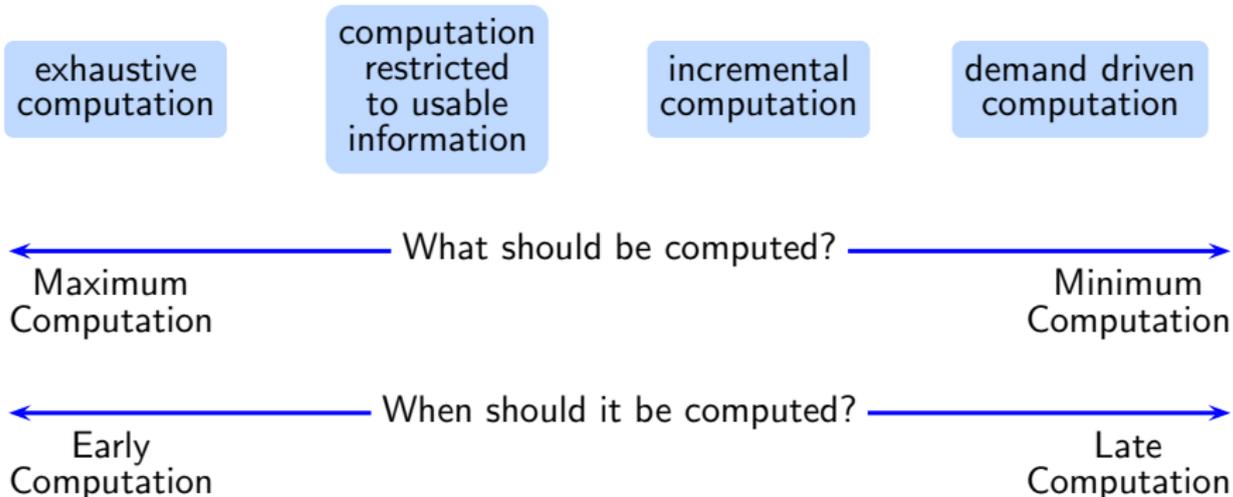
LFCPA Lessons: The Larger Perspective



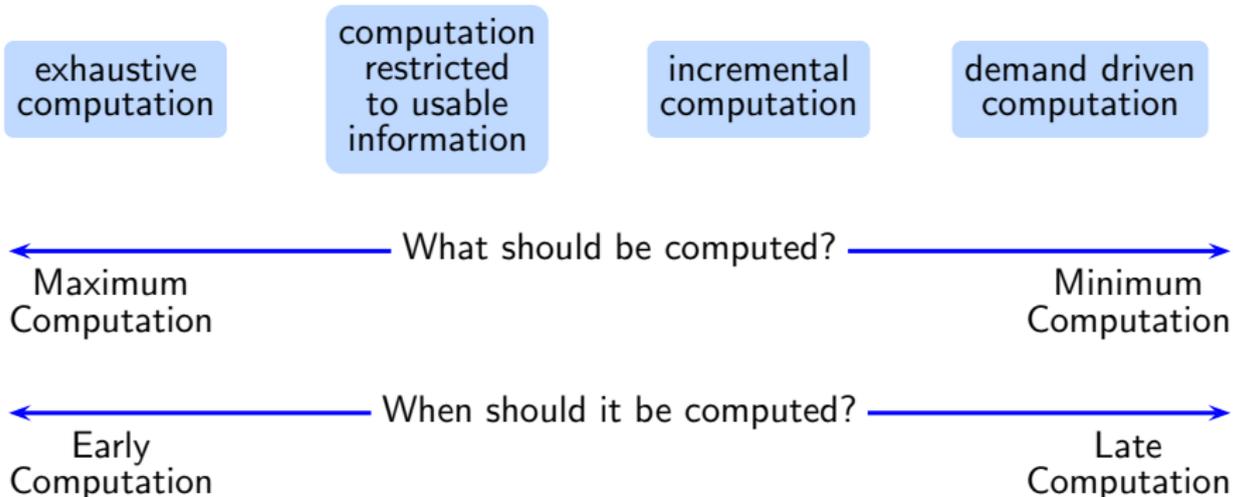
LFCPA Lessons: The Larger Perspective



LFCPA Lessons: The Larger Perspective



LFCPA Lessons: The Larger Perspective

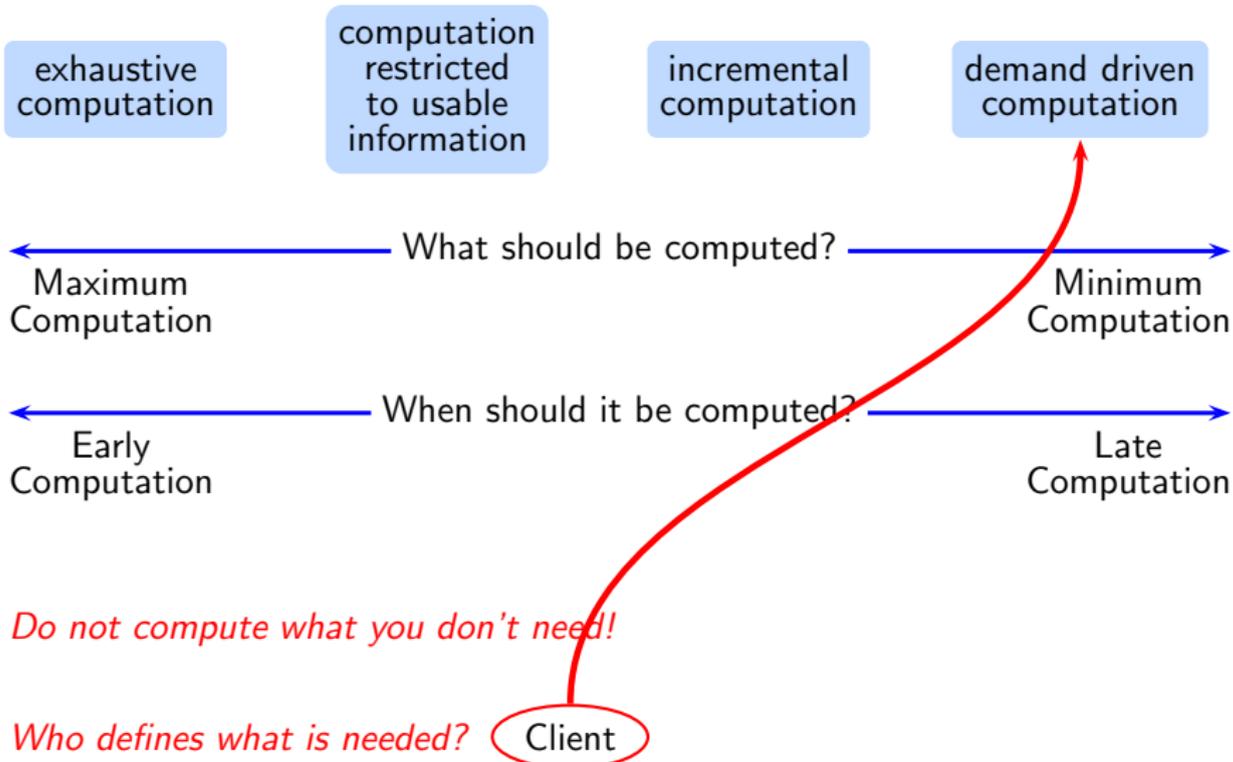


Do not compute what you don't need!

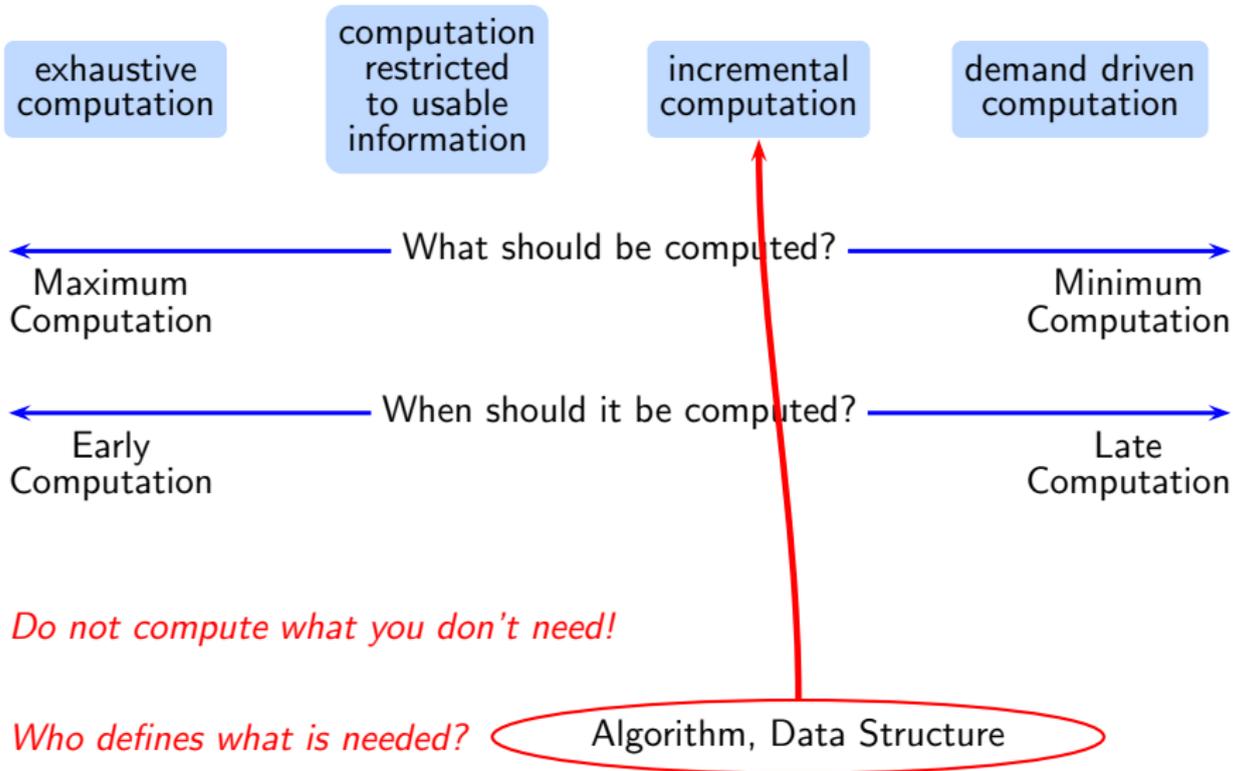
Who defines what is needed?



LFCPA Lessons: The Larger Perspective



LFCPA Lessons: The Larger Perspective



LFCPA Lessons: The Larger Perspective

exhaustive
computation

computation
restricted
to usable
information

incremental
computation

demand driven
computation

← Maximum
Computation

← Early
Computation

Avoid computing some values because

- they have been computed before, or
- they can just be “adjusted”, or
- they are equivalent to some other values

E.g. Value based termination of call strings,
Work list based methods, BDDs

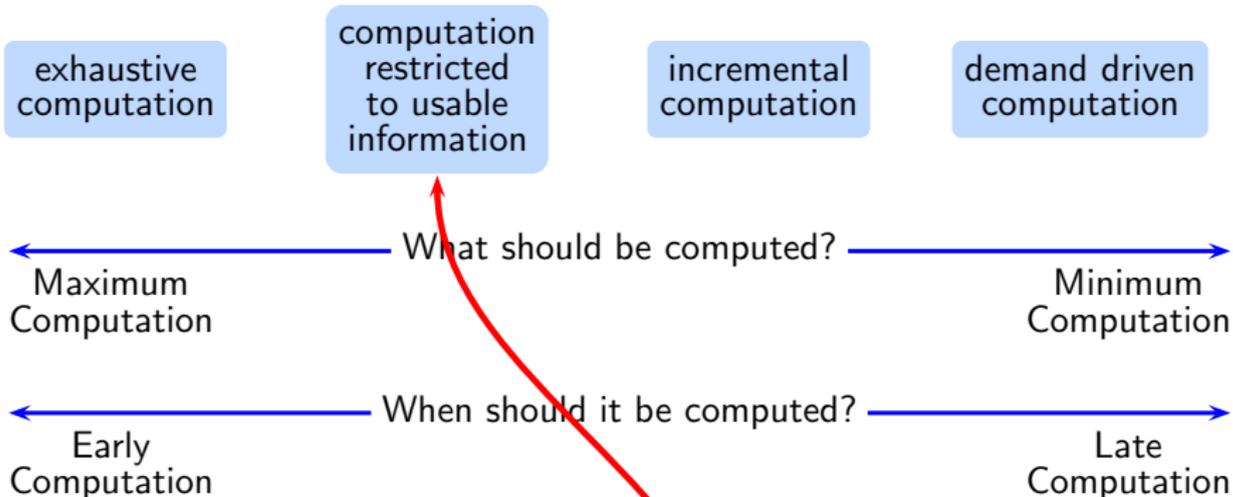
Do not compute what you don't need!

Who defines what is needed?

Algorithm, Data Structure



LFCPA Lessons: The Larger Perspective



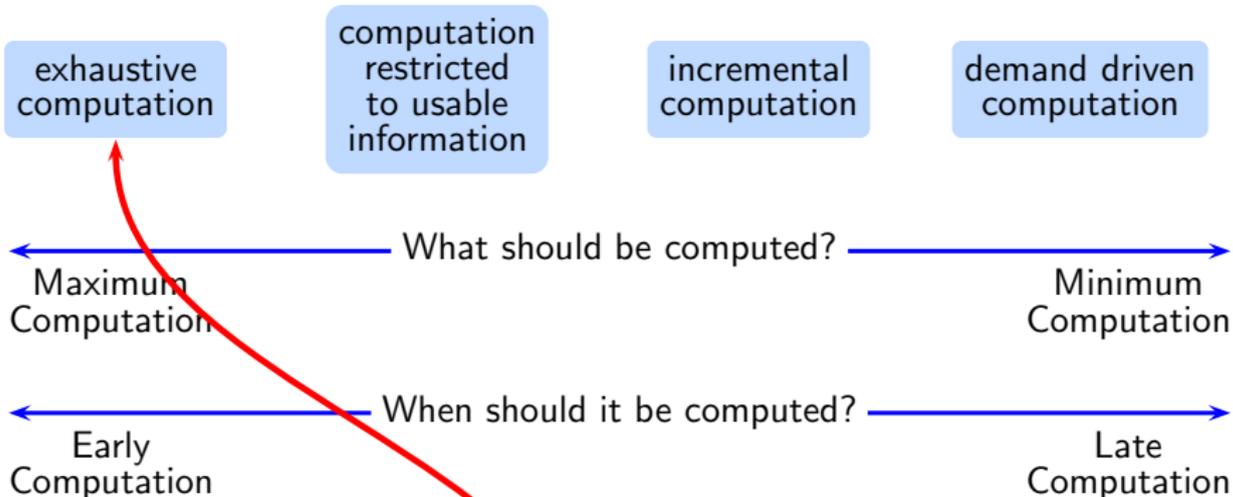
Do not compute what you don't need!

Who defines what is needed?

Definition of Analysis



LFCPA Lessons: The Larger Perspective

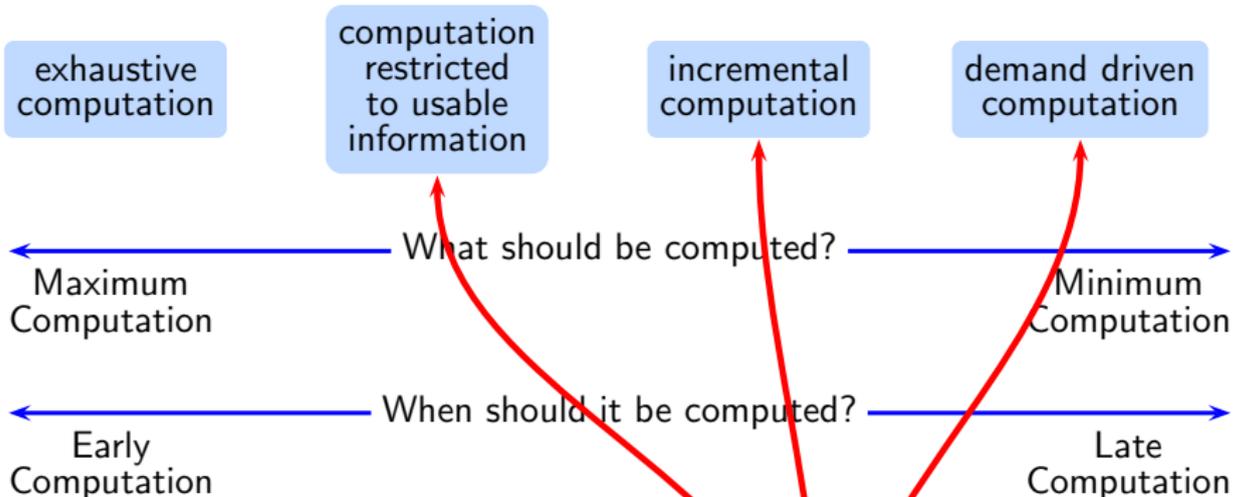


Do not compute what you don't need!

Who defines what is needed? **No One!**



LFCPA Lessons: The Larger Perspective



Do not compute what you don't need!

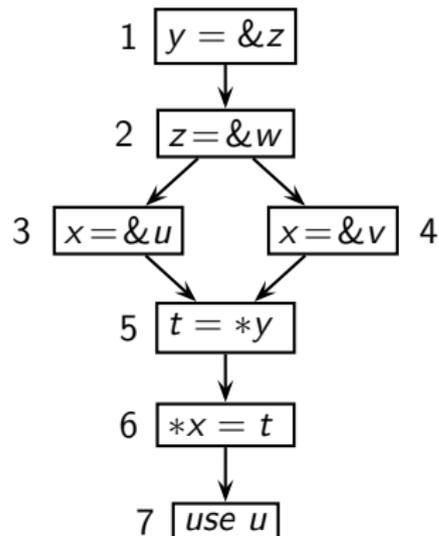
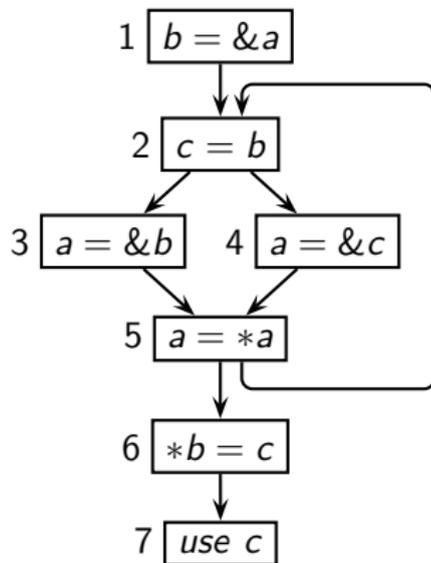
Who defines what is needed?

*These seem orthogonal
and may be used together*



Tutorial Problems for FCPA and LFCPA

- Perform may points-to analysis by deriving must info using “?” in *BI*
- Perform liveness based points-to analysis



An Outline of Pointer Analysis Coverage

- The larger perspective
- Comparing Points-to and Alias information
- Flow Insensitive Points-to Analysis
- Flow Sensitive Points-to Analysis
- Pointer Analyses: An Engineer's Landscape
- Liveness Based Points-to Analysis
- Generalizations to Heap, Arrays, Pointer Arithmetic, and Unions

Next Topic



Original LFCPA Formulation

Data flow equations

Lin/Lout, Ain/Aout

Extractors for
statements

Def, Kill, Ref, Pointee

Lattices

$2^{P \times \text{Var}}, 2^P$

Named locations

Variables Var , Pointers P ,



Formulating Generalizations in LFCPA

Data flow equations

Lin/Lout, Ain/Aout

Extractors for
statements

Def, Kill, Ref, Pointee

Extractors for
pointer expressions
lval, rval, deref, ref

Lattices

$2^{S \times T}, 2^S$

Named locations

Variables $\mathbb{V}ar$, Pointers \mathbf{P} ,
Allocation Sites H ,
Fields F , pF , npF ,
Offsets C



Generalization for Heap and Structures

- Grammar.

$$\begin{array}{l} \alpha := \text{malloc} \mid \&\beta \mid \beta \\ \beta := x \mid \beta.f \mid \beta \rightarrow f \mid *\beta \end{array}$$

where α is a pointer expression, x is a variable, and f is a field

- Memory model: Named memory locations. No numeric addresses

$$\begin{array}{ll} S = \mathbf{P} \cup H \cup S_p & \text{(source locations)} \\ T = \mathbf{Var} \cup H \cup S_m \cup \{?\} & \text{(target locations)} \\ S_p = R \times npF^* \times pF & \text{(pointers in structures)} \\ S_m = R \times npF^* \times (pF \cup npF) & \text{(other locations in structures)} \end{array}$$

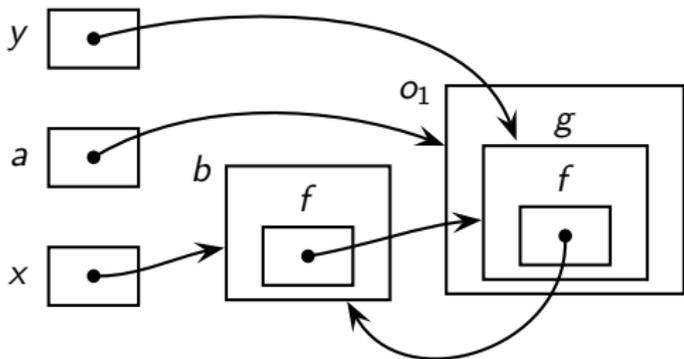


Named Locations for Pointer Expressions

```

typedef struct B
{
  ...
  struct B *f;
} sB;
typedef struct A
{
  ...
  struct B g;
} sA;
  sA *a;
  sB *x, *y, b;
1. a = (sA*) malloc
   (sizeof(sA));
2. y = &a->g;
3. b.f = y;
4. x = &b;
5. y.f = &x;
6. return x->f->f;

```



Pointer Expression	l-value	r-value
x	x	b
$x \rightarrow f$	$b.f$	$o_1.g.f$
$x \rightarrow f \rightarrow f$	$o_1.g.f$	b



L- and R-values of Pointer Expressions

$$lval(\alpha, A) = \begin{cases} \{\sigma\} & (\alpha \equiv \sigma) \wedge (\sigma \in \mathbb{V}\text{ar}) \\ \{\sigma.f \mid \sigma \in lval(\beta, A)\} & \alpha \equiv \beta.f \\ \{\sigma.f \mid \sigma \in rval(\beta, A), \sigma \neq ?\} & \alpha \equiv \beta \rightarrow f \\ \{\sigma \mid \sigma \in rval(\beta, A), \sigma \neq ?\} & \alpha \equiv *\beta \\ \emptyset & \text{otherwise} \end{cases}$$

$$rval(\alpha, A) = \begin{cases} lval(\beta, A) & \alpha \equiv \&\beta \\ \{o_i\} & \alpha \equiv malloc \wedge o_i = get_heap_loc() \\ A(lval(\alpha, A) \cap S) & \text{otherwise} \end{cases}$$



Defining Extractor Functions

- Pointer assignment statement $lhs_n = rhs_n$

$$Def_n = lval(lhs_n, Ain_n)$$

$$Kill_n = lval(lhs_n, Must(Ain_n))$$

$$Ref_n = \begin{cases} deref(lhs_n, Ain_n) \\ deref(lhs_n, Ain_n) \cup ref(rhs_n, Ain_n) \end{cases}$$

$$Def_n \cap Lout_n = \emptyset$$

otherwise

$$Pointee_n = rval(rhs_n, Ain_n)$$

- Use α statement

$$Def_n = Kill_n = Pointee_n = \emptyset$$

$$Ref_n = ref(\alpha, Ain_n)$$

- Any other statement

$$Def_n = Kill_n = Ref_n = Pointee_n = \emptyset$$



Extensions for Handling Arrays and Pointer Arithmetic

- Grammar.

$$\begin{array}{l} \alpha := \text{malloc} \mid \&\beta \mid \beta \mid \&\beta + e \\ \beta := x \mid \beta.f \mid \beta \rightarrow f \mid *\beta \mid \beta[e] \mid \beta + e \end{array}$$

- Memory model: Named memory locations. No numeric addresses
 - ▶ No address calculation
 - ▶ R-values of index expressions retained for each dimension
If $rval(x) = 10$, then $lval(a.f[5][2+x].g) = a.f.5.12.g$
 - ▶ Sizes of the array elements ignored

$$S = \mathbf{P} \cup H \cup G_p \quad (\text{source locations})$$

$$T = \mathbf{Var} \cup H \cup G_m \cup \{?\} \quad (\text{target locations})$$

$$G_p = R \times (C \cup npF)^* \times (C \cup pF) \quad (\text{pointers in aggregates})$$

$$G_m = R \times (C \cup npF)^* \times (C \cup pF \cup npF) \quad (\text{locations in aggregates})$$



Extending L-Value Computation to Arrays and Pointer Arithmetic

- Pointer arithmetic does not have an l-value
- For handling arrays
 - ▶ evaluate index expressions using *eval_e* and accumulate offsets
 - ▶ if *e* cannot be evaluated at compile time, $eval\ e = \perp_{eval}$
(i.e. array accesses in that dimension are treated as index-insensitive)

$$lval(\alpha, A) = \begin{cases} \{\sigma\} & (\alpha \equiv \sigma) \wedge (\sigma \in \mathbb{V}\text{ar}) \\ \{\sigma.f \mid \sigma \in lval(\beta, A)\} & \alpha \equiv \beta.f \\ \{\sigma.f \mid \sigma \in rval(\beta, A), \sigma \neq ?\} & \alpha \equiv \beta \rightarrow f \\ \{\sigma \mid \sigma \in rval(\beta, A), \sigma \neq ?\} & \alpha \equiv * \beta \\ \{\sigma.eval\ e \mid \sigma \in lval(\beta, A)\} & \alpha \equiv \beta[e] \\ \emptyset & \text{otherwise} \end{cases}$$



Extending R-Value Computation to Arrays and Pointer Arithmetic

For handling pointer arithmetic

- If the r-value of the pointer is an array location, add $eval e$ to the offset
- Otherwise, over-approximate the pointees to all possible locations

$$rval(\alpha, A) = \begin{cases} lval(\beta, A) & \alpha \equiv \&\beta \\ \{o_i\} & \alpha \equiv malloc \wedge o_i = get_heap_loc() \\ T & (\alpha \equiv \beta + e) \wedge \\ & (\exists \sigma \in rval(\beta, A), \sigma \neq \sigma'.c, \sigma' \in T, c \in C) \\ \bigcup \{\sigma.(c + eval e)\} & (\alpha \equiv \beta + e) \wedge \\ & (\sigma.c \in rval(\beta, A)) \wedge (c \in C) \\ A(lval(\alpha, A) \cap S) & \text{otherwise} \end{cases}$$



Part 6

Heap Reference Analysis

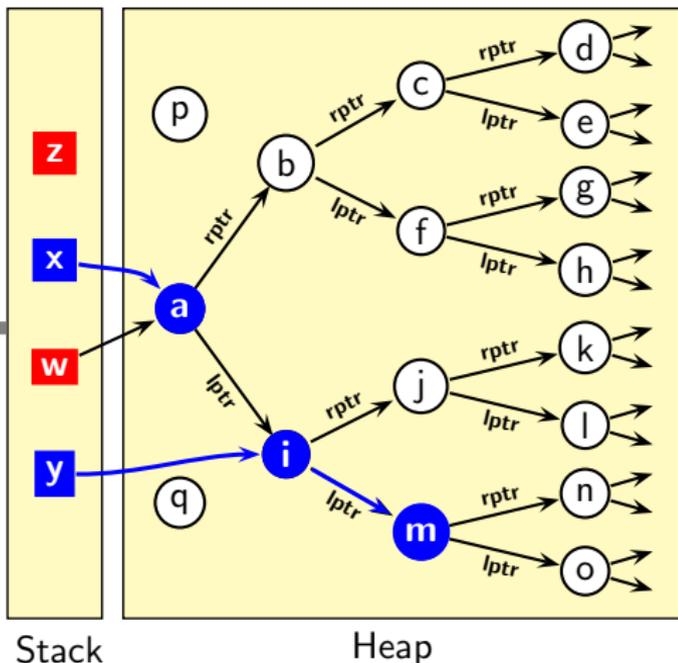
Motivating Example for Heap Liveness Analysis

If the **while** loop is not executed even once.

```

1  w = x      // x points to ma
2  while (x.data < max)
3      x = x.rptr
4  y = x.lptr
5  z = New class_of_z
6  y = y.lptr
7  z.sum = x.data + y.data

```



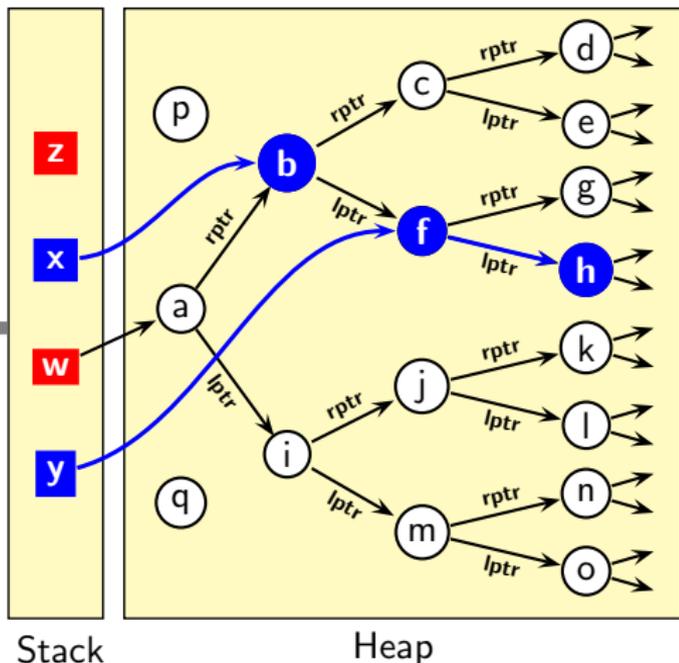
Motivating Example for Heap Liveness Analysis

If the **while** loop is executed once.

```

1  w = x      // x points to ma
2  while (x.data < max)
3      x = x.rptr
4  y = x.lptr
5  z = New class_of_z
6  y = y.lptr
7  z.sum = x.data + y.data

```



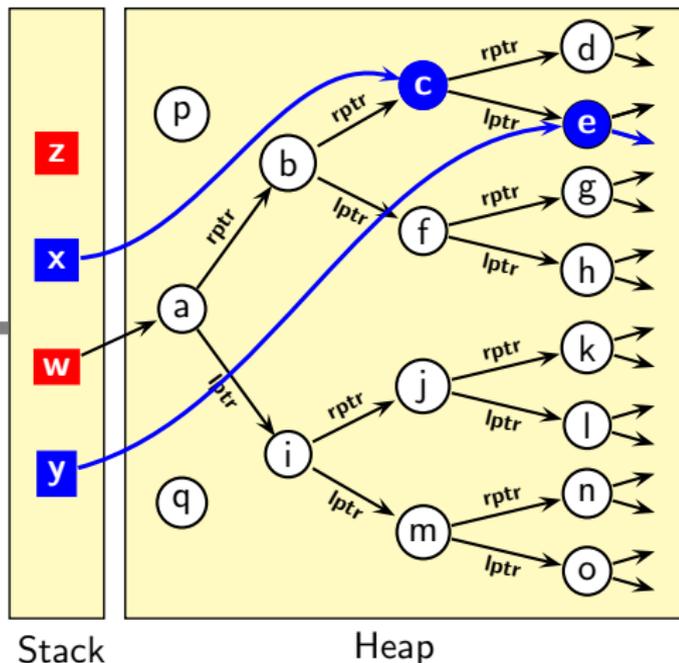
Motivating Example for Heap Liveness Analysis

If the **while** loop is executed twice.

```

1  w = x      // x points to ma
2  while (x.data < max)
3      x = x.rptr
4  y = x.lptr
5  z = New class_of_z
6  y = y.lptr
7  z.sum = x.data + y.data

```



The Moral of the Story

- Mappings between access expressions and l-values keep changing
- This is a *rule* for heap data
For stack and static data, it is an *exception*!
- Static analysis of programs has made significant progress for stack and static data.

What about heap data?

- ▶ Given two access expressions at a program point, do they have the same l-value?
- ▶ Given the same access expression at two program points, does it have the same l-value?



Our Solution

```

1  w = x
   y = z = null
2  while (x.data < max)
   {
3     x = x.rptr    }
   x.rptr = x.lptr.rptr = null
   x.lptr.lptr.lptr = null
   x.lptr.lptr.rptr = null
4  y = x.lptr
   x.lptr = y.rptr = null
   y.lptr.lptr = y.lptr.rptr = null
5  z = New class_of_z
   z.lptr = z.rptr = null
6  y = y.lptr
   y.lptr = y.rptr = null
7  z.sum = x.data + y.data
   x = y = z = null
```



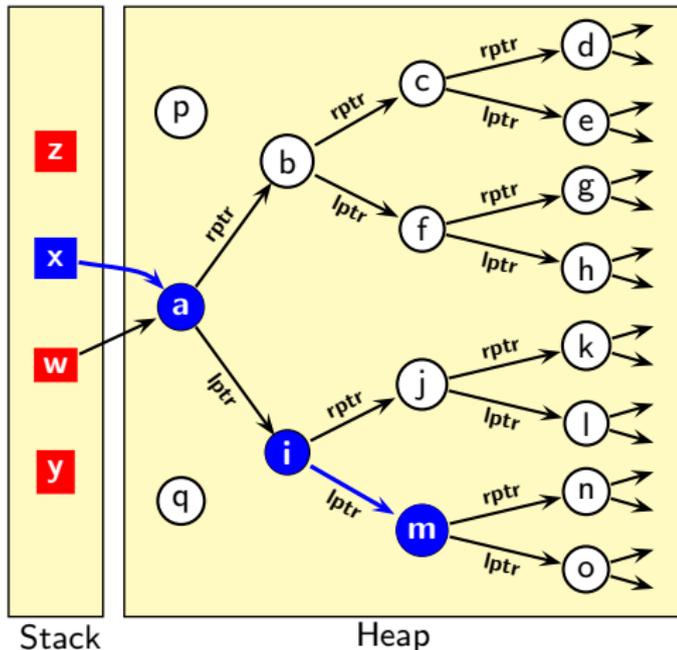
Our Solution

```

1  y = z = null
   w = x
   w = null
2  while (x.data < max)
   {
3     x.lptr = null
     x = x.rptr
   }
   x.rptr = x.lptr.rptr = null
   x.lptr.lptr.lptr = null
   x.lptr.lptr.rptr = null
4  y = x.lptr
   x.lptr = y.rptr = null
   y.lptr.lptr = y.lptr.rptr = null
5  z = New class_of_z
   z.lptr = z.rptr = null
6  y = y.lptr
   y.lptr = y.rptr = null
7  z.sum = x.data + y.data
   x = y = z = null

```

While loop is not executed even once



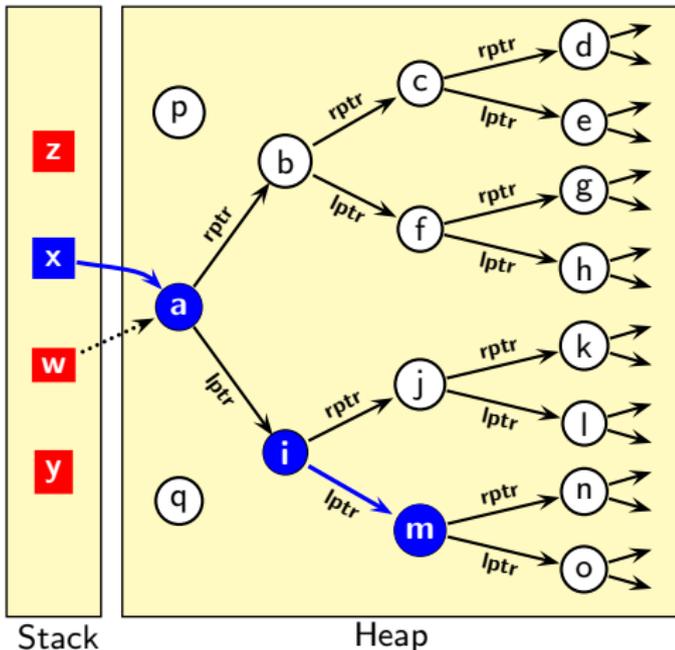
Our Solution

```

1  y = z = null
2  w = x
   w = null
3  while (x.data < max)
   {
4     x.lptr = null
     x = x.rptr
   }
   x.rptr = x.lptr.rptr = null
   x.lptr.lptr.lptr = null
   x.lptr.lptr.rptr = null
5  y = x.lptr
   x.lptr = y.rptr = null
   y.lptr.lptr = y.lptr.rptr = null
6  z = New class_of_z
   z.lptr = z.rptr = null
7  y = y.lptr
   y.lptr = y.rptr = null
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```

While loop is not executed even once



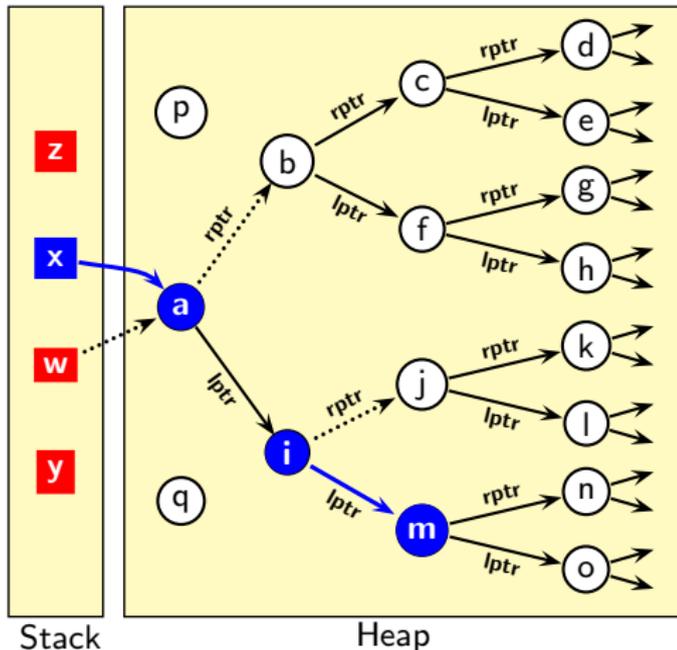
Our Solution

```

1  y = z = null
   w = x
   w = null
2  while (x.data < max)
   {
3     x.lptr = null
     x = x.rptr
   }
   x.rptr = x.lptr.rptr = null
   x.lptr.lptr.lptr = null
   x.lptr.lptr.rptr = null
4  y = x.lptr
   x.lptr = y.rptr = null
   y.lptr.lptr = y.lptr.rptr = null
5  z = New class_of_z
   z.lptr = z.rptr = null
6  y = y.lptr
   y.lptr = y.rptr = null
7  z.sum = x.data + y.data
   x = y = z = null

```

While loop is not executed even once



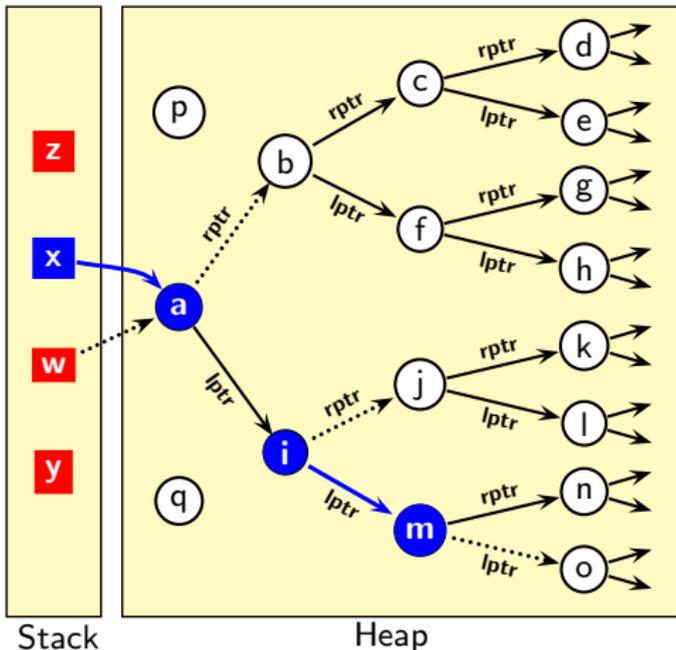
Our Solution

```

1  y = z = null
   w = x
   w = null
2  while (x.data < max)
   {
3     x.lptr = null
     x = x.rptr
   }
   x.rptr = x.lptr.rptr = null
   x.lptr.lptr.lptr = null
   x.lptr.lptr.rptr = null
4  y = x.lptr
   x.lptr = y.rptr = null
   y.lptr.lptr = y.lptr.rptr = null
5  z = New class_of_z
   z.lptr = z.rptr = null
6  y = y.lptr
   y.lptr = y.rptr = null
7  z.sum = x.data + y.data
   x = y = z = null

```

While loop is not executed even once



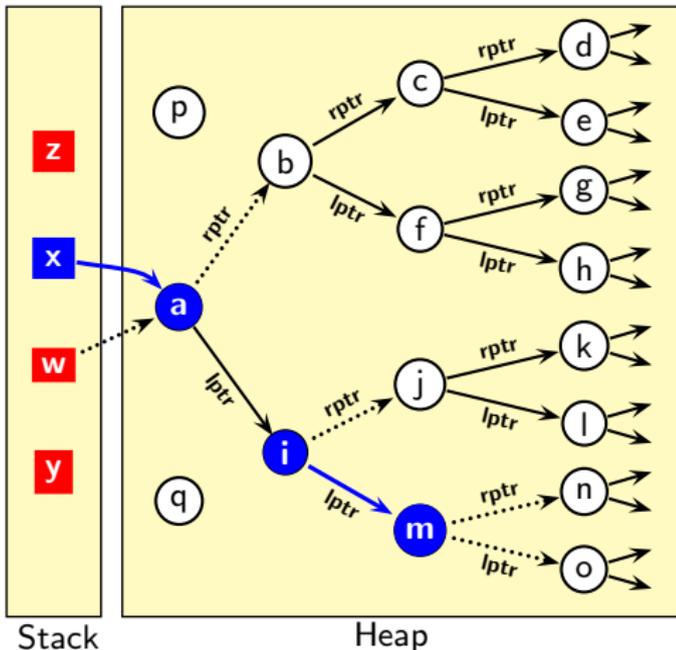
Our Solution

```

1  y = z = null
   w = x
   w = null
2  while (x.data < max)
   {
3     x.lptr = null
     x = x.rptr
   }
   x.rptr = x.lptr.rptr = null
   x.lptr.lptr.lptr = null
   x.lptr.lptr.rptr = null
4  y = x.lptr
   x.lptr = y.rptr = null
   y.lptr.lptr = y.lptr.rptr = null
5  z = New class_of_z
   z.lptr = z.rptr = null
6  y = y.lptr
   y.lptr = y.rptr = null
7  z.sum = x.data + y.data
   x = y = z = null

```

While loop is not executed even once



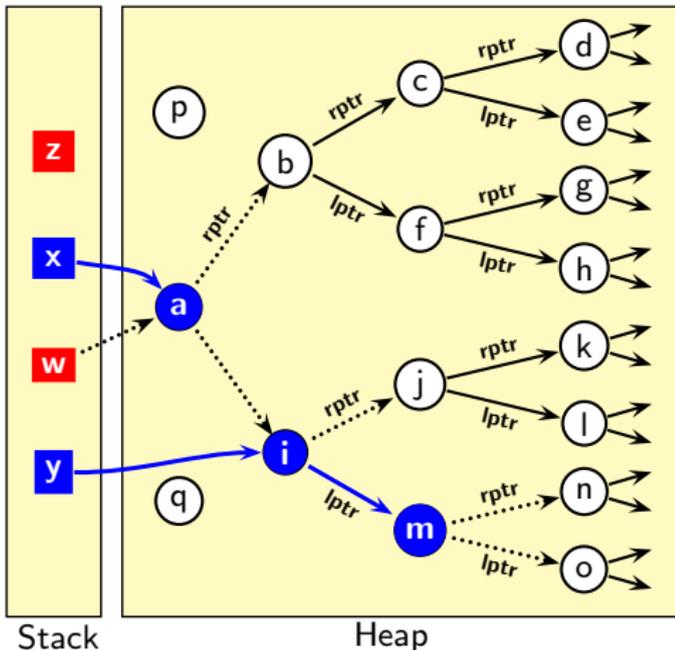
Our Solution

```

1  y = z = null
   w = x
   w = null
2  while (x.data < max)
   {
3     x.lptr = null
       x = x.rptr
   }
   x.rptr = x.lptr.rptr = null
   x.lptr.lptr.lptr = null
   x.lptr.lptr.rptr = null
4  y = x.lptr
   x.lptr = y.rptr = null
   y.lptr.lptr = y.lptr.rptr = null
5  z = New class_of_z
   z.lptr = z.rptr = null
6  y = y.lptr
   y.lptr = y.rptr = null
7  z.sum = x.data + y.data
   x = y = z = null

```

While loop is not executed even once



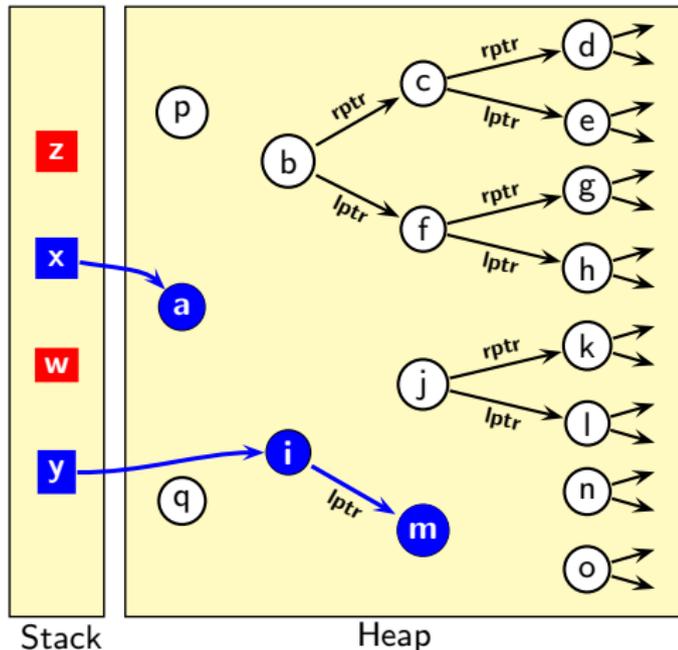
Our Solution

```

1  y = z = null
   w = x
   w = null
2  while (x.data < max)
   {
3     x.lptr = null
     x = x.rptr
   }
   x.rptr = x.lptr.rptr = null
   x.lptr.lptr.lptr = null
   x.lptr.lptr.rptr = null
4  y = x.lptr
   x.lptr = y.rptr = null
   y.lptr.lptr = y.lptr.rptr = null
5  z = New class_of_z
   z.lptr = z.rptr = null
6  y = y.lptr
   y.lptr = y.rptr = null
7  z.sum = x.data + y.data
   x = y = z = null

```

While loop is not executed even once



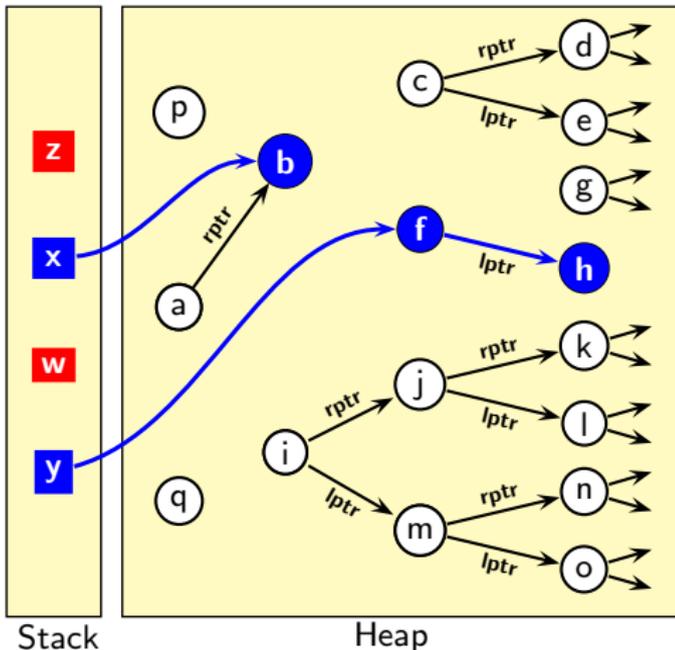
Our Solution

```

y = z = null
1 w = x
  w = null
2 while (x.data < max)
  {   x.lptr = null
3     x = x.rptr   }
  x.rptr = x.lptr.rptr = null
  x.lptr.lptr.lptr = null
  x.lptr.lptr.rptr = null
4 y = x.lptr
  x.lptr = y.rptr = null
  y.lptr.lptr = y.lptr.rptr = null
5 z = New class_of_z
  z.lptr = z.rptr = null
6 y = y.lptr
  y.lptr = y.rptr = null
7 z.sum = x.data + y.data
  x = y = z = null

```

While loop is executed once



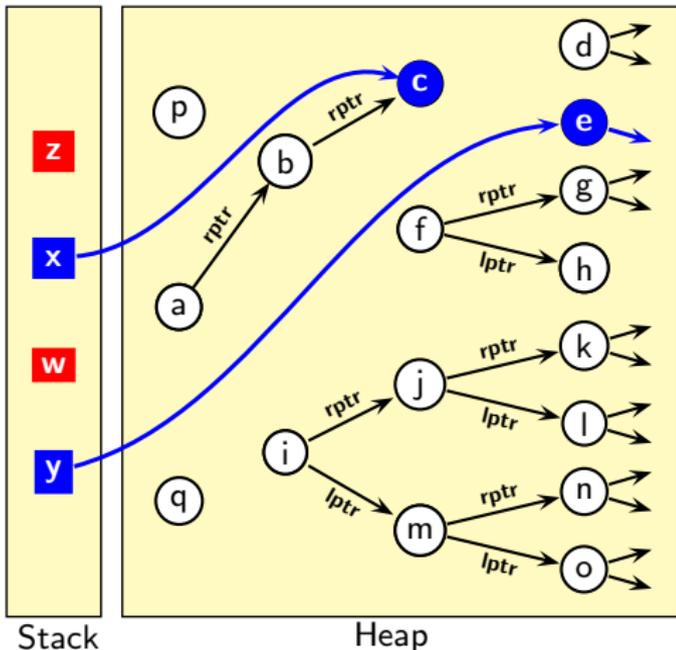
Our Solution

```

y = z = null
1 w = x
  w = null
2 while (x.data < max)
  {   x.lptr = null
3     x = x.rptr   }
  x.rptr = x.lptr.rptr = null
  x.lptr.lptr.lptr = null
  x.lptr.lptr.rptr = null
4 y = x.lptr
  x.lptr = y.rptr = null
  y.lptr.lptr = y.lptr.rptr = null
5 z = New class_of_z
  z.lptr = z.rptr = null
6 y = y.lptr
  y.lptr = y.rptr = null
7 z.sum = x.data + y.data
  x = y = z = null

```

While loop is executed twice



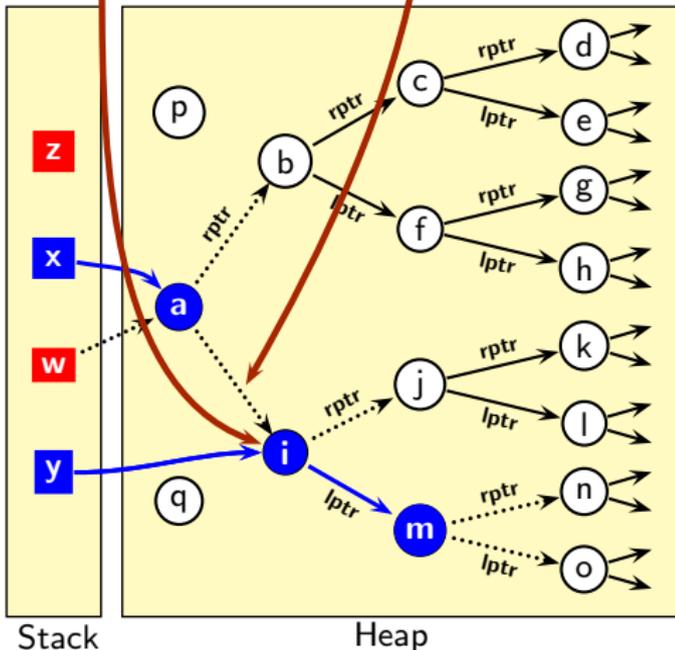
Some Observations

```

1  y = z = null
   w = x
   w = null
2  while (x.data < max)
   {
3      x.lptr = null
      x = x.rptr
   }
   x.rptr = x.lptr.rptr = null
   x.lptr.lptr.lptr = null
   x.lptr.lptr.rptr = null
4  y = x.lptr
   x.lptr = y.rptr = null
   y.lptr.lptr = y.lptr.rptr = null
5  z = New class_of_z
   z.lptr = z.rptr = null
6  y = y.lptr
   y.lptr = y.rptr = null
7  z.sum = x.data + y.data
   x = y = z = null

```

Node i is live but link $a \rightarrow i$ is nullified



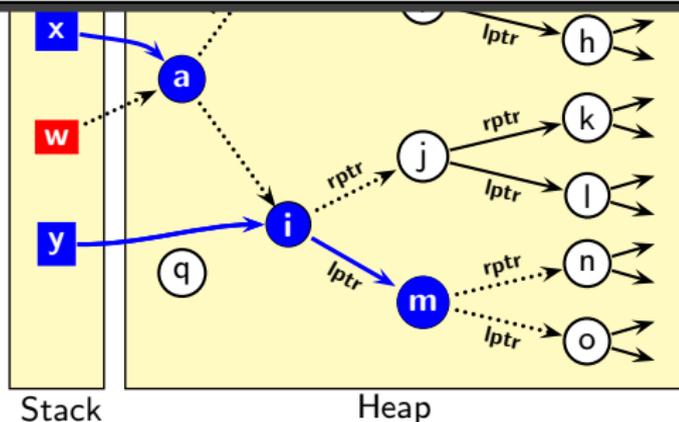
Some Observations

```

y = z = null
1 w = x
  w = null
2 while (x.data < max)
  {   x.lptr = null
3     x = x.rptr   }
  x.rptr = x.lptr.rptr = null
  x.lptr.lptr.lptr = null
  x.lptr.lptr.rptr = null
4 y = x.lptr
  x.lptr = y.rptr = null
  y.lptr.lptr = y.lptr.rptr = null
5 z = New class_of_z
  z.lptr = z.rptr = null
6 y = y.lptr
  y.lptr = y.rptr = null
7 z.sum = x.data + y.data
  x = y = z = null

```

- The memory address that x holds when the execution reaches a given program point is not an invariant of program execution



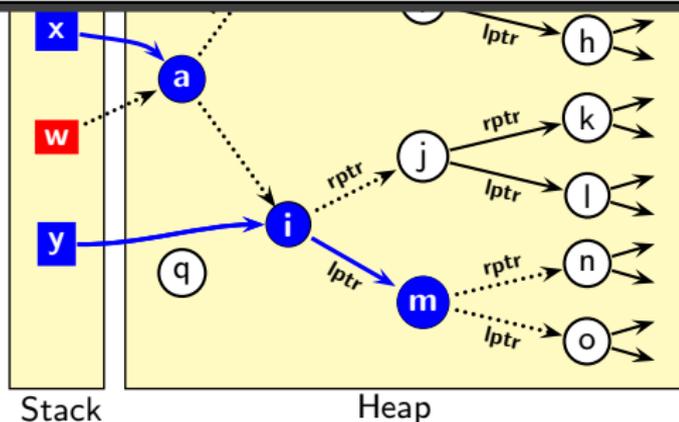
Some Observations

```

y = z = null
1 w = x
  w = null
2 while (x.data < max)
  {   x.lptr = null
3     x = x.rptr   }
  x.rptr = x.lptr.rptr = null
  x.lptr.lptr.lptr = null
  x.lptr.lptr.rptr = null
4 y = x.lptr
  x.lptr = y.rptr = null
  y.lptr.lptr = y.lptr.rptr = null
5 z = New class_of_z
  z.lptr = z.rptr = null
6 y = y.lptr
  y.lptr = y.rptr = null
7 z.sum = x.data + y.data
  x = y = z = null

```

- The memory address that x holds when the execution reaches a given program point is not an invariant of program execution
- Whether we dereference $lptr$ out of x or $rptr$ out of x at a given program point is an invariant of program execution



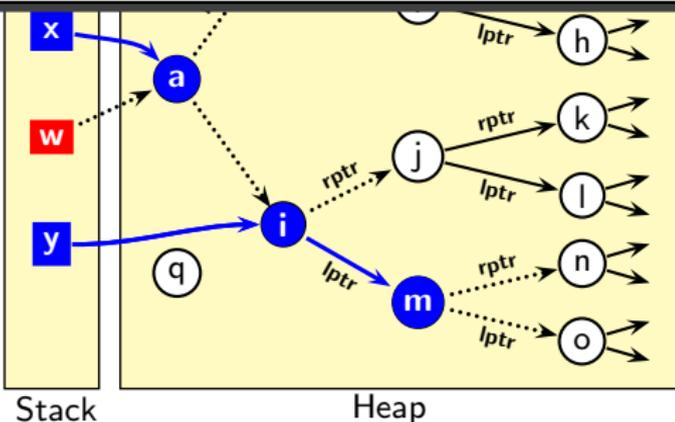
Some Observations

```

y = z = null
1 w = x
  w = null
2 while (x.data < max)
  {   x.lptr = null
3     x = x.rptr   }
  x.rptr = x.lptr.rptr = null
  x.lptr.lptr.lptr = null
  x.lptr.lptr.rptr = null
4 y = x.lptr
  x.lptr = y.rptr = null
  y.lptr.lptr = y.lptr.rptr = null
5 z = New class_of_z
  z.lptr = z.rptr = null
6 y = y.lptr
  y.lptr = y.rptr = null
7 z.sum = x.data + y.data
  x = y = z = null

```

- The memory address that x holds when the execution reaches a given program point is not an invariant of program execution
- Whether we dereference $lptr$ out of x or $rptr$ out of x at a given program point is an invariant of program execution
- *A static analysis can discover only invariants*



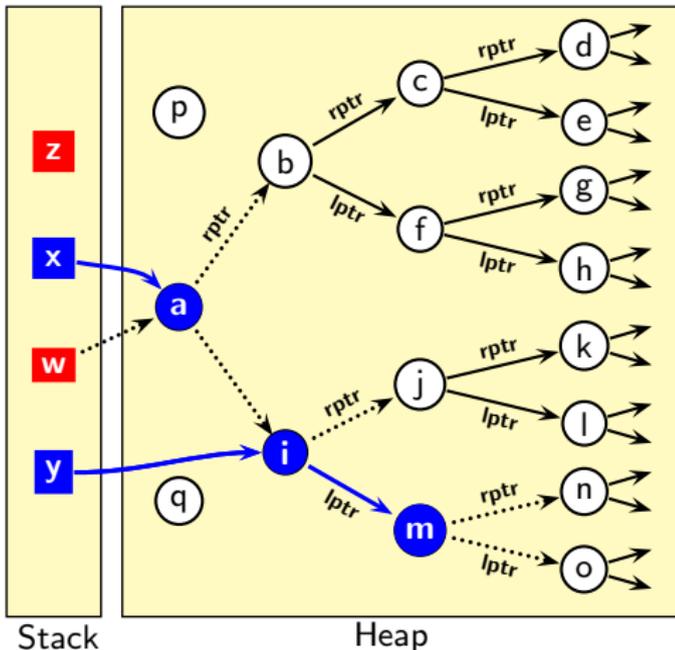
Some Observations

```

1  y = z = null
   w = x
   w = null
2  while (x.data < max)
   {
3     x.lptr = null
     x = x.rptr
   }
   x.rptr = x.lptr.rptr = null
   x.lptr.lptr.lptr = null
   x.lptr.lptr.rptr = null
4  y = x.lptr
   x.lptr = y.rptr = null
   y.lptr.lptr = y.lptr.rptr = null
5  z = New class_of_z
   z.lptr = z.rptr = null
6  y = y.lptr
   y.lptr = y.rptr = null
7  z.sum = x.data + y.data
   x = y = z = null

```

New access expressions are created.
Can they cause exceptions?



An Overview of Heap Reference Analysis

- A reference (called a *link*) can be represented by an *access path*.
Eg. “ $x \rightarrow \text{lptr} \rightarrow \text{rptr}$ ”
- A link may be accessed in multiple ways
- Setting links to null
 - ▶ *Alias Analysis*. Identify all possible ways of accessing a link
 - ▶ *Liveness Analysis*. For each program point, identify “dead” links (i.e. links which are not accessed after that program point)
 - ▶ *Availability and Anticipability Analyses*. Dead links should be reachable for making null assignment.
 - ▶ *Code Transformation*. Set “dead” links to null



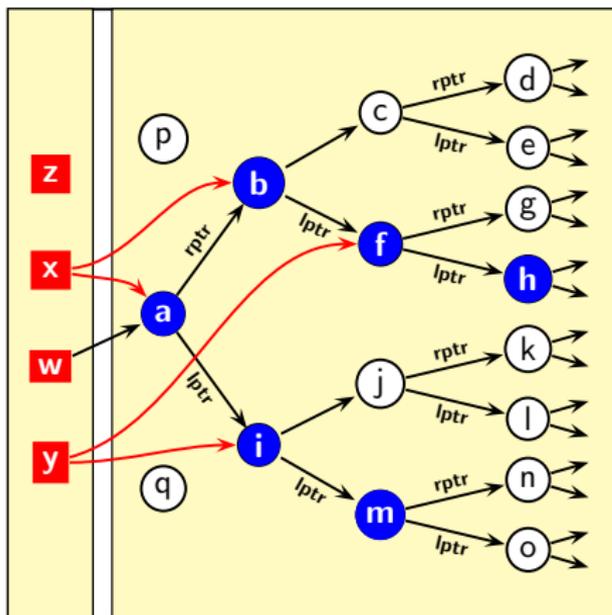
Assumptions

For simplicity of exposition

- Java model of heap access
 - ▶ Root variables are on stack and represent references to memory in heap.
 - ▶ Root variables cannot be pointed to by any reference.
- Simple extensions for C++
 - ▶ Root variables can be pointed to by other pointers.
 - ▶ Pointer arithmetic is not handled.



Key Idea #1 : Access Paths Denote Links



- Root variables : x, y, z
- Field names : $rptr, lptr$
- Access path : $x \rightarrow rptr \rightarrow lptr$
Semantically, sequence of “links”
- Frontier : name of the last link
- Live access path : If the link corresponding to its frontier is used in future

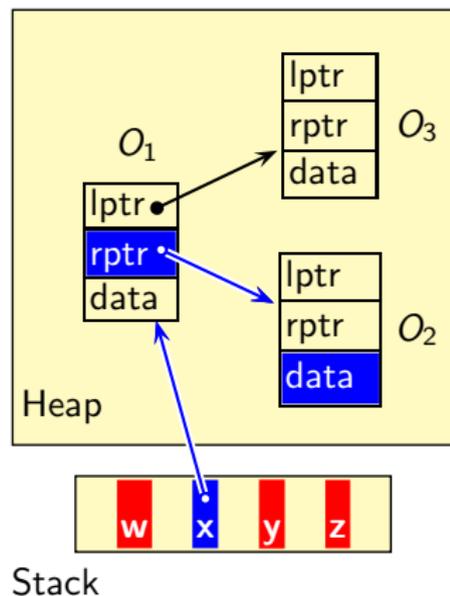


What Makes a Link Live?

Assuming that a statement must be executed, if nullifying a link **read** in the statement can change the semantics of the program, then the link is live.

Reading a link for *accessing the contents* of the corresponding target object:

Example	Objects read	Live access paths
<code>sum = x.rptr.data</code>	x, O_1, O_2	$x, x \rightarrow \text{rptr}$
<code>if (x.rptr.data < sum)</code>	x, O_1, O_2	$x, x \rightarrow \text{rptr}$

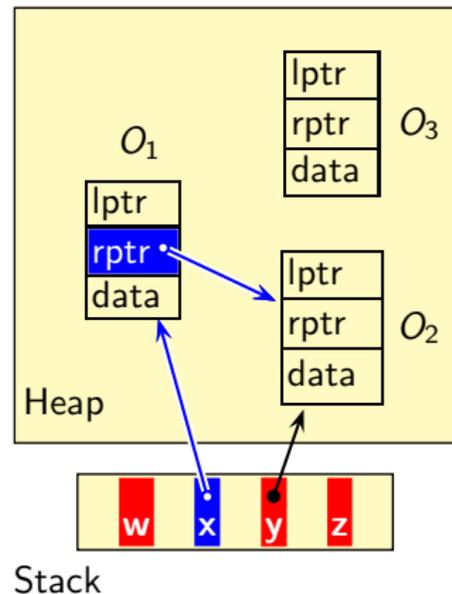


What Makes a Link Live?

Assuming that a statement must be executed, if nullifying a link **read** in the statement can change the semantics of the program, then the link is live.

Reading a link for *copying the contents* of the corresponding target object:

Example	Objects read	Live access paths
<code>y = x.rptr</code>	x, O_1	$x, x.rptr$

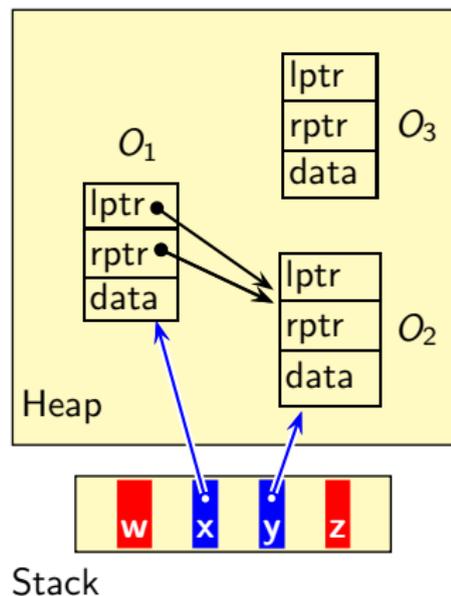


What Makes a Link Live?

Assuming that a statement must be executed, if nullifying a link **read** in the statement can change the semantics of the program, then the link is live.

Reading a link for *copying the contents* of the corresponding target object:

Example	Objects read	Live access paths
<code>y = x.rptr</code>	x, O_1	$x, x.rptr$
<code>x.lptr = y</code>	x, O_1, y	x, y

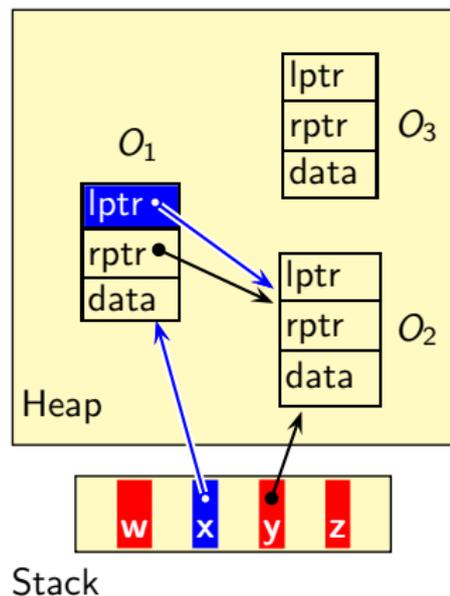


What Makes a Link Live?

Assuming that a statement must be executed, if nullifying a link **read** in the statement can change the semantics of the program, then the link is live.

Reading a link for *comparing the address* of the corresponding target object:

Example	Objects read	Live access paths
<code>if (x.lptr == null)</code>	x, O_1	$x, x \rightarrow \text{lptr}$

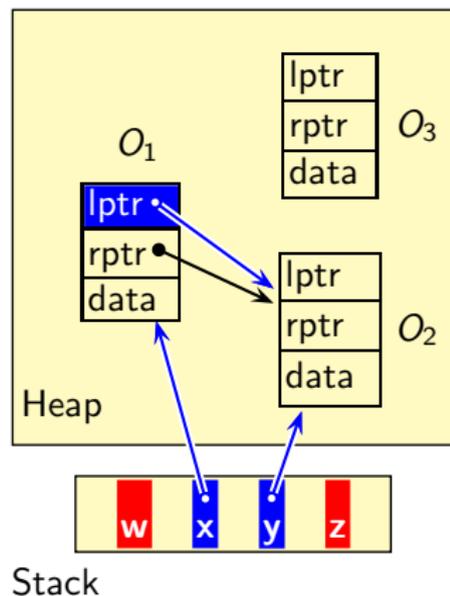


What Makes a Link Live?

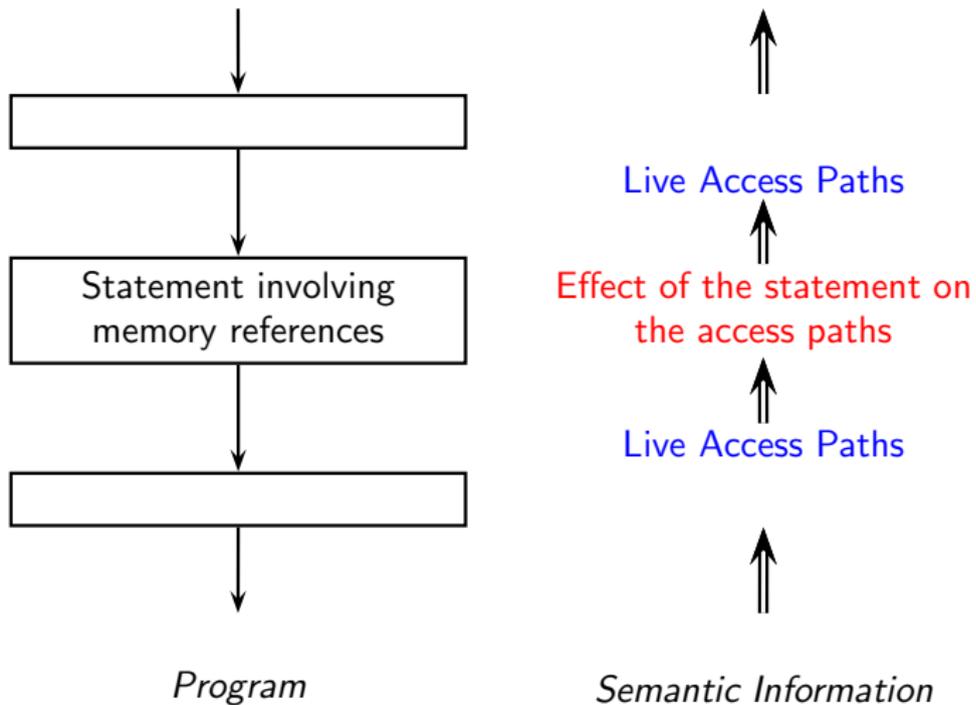
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Reading a link for *comparing the address* of the corresponding target object:

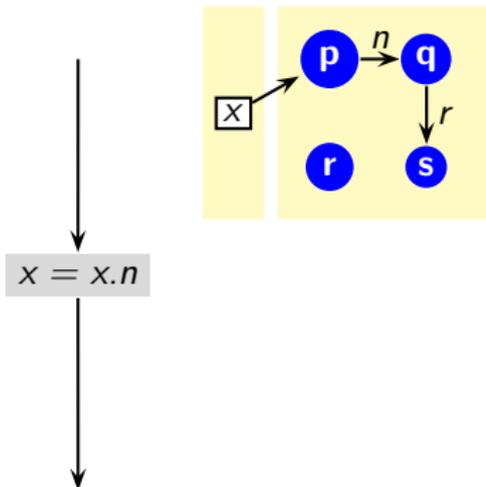
Example	Objects read	Live access paths
<code>if (x.lptr == null)</code>	x, O_1	$x, x \rightarrow \text{lptr}$
<code>if (y == x.lptr)</code>	x, O_1, y	$x, x \rightarrow \text{lptr}, y$



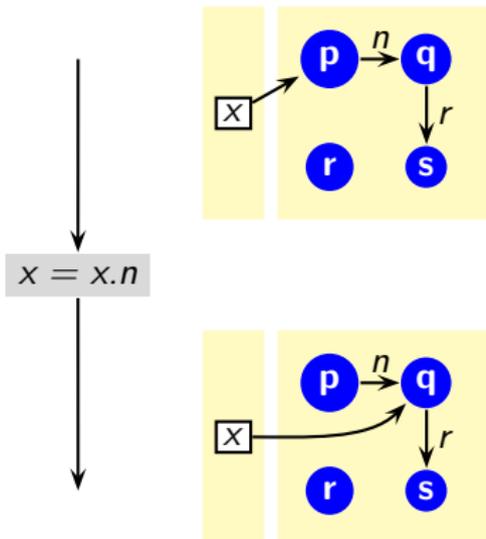
Liveness Analysis



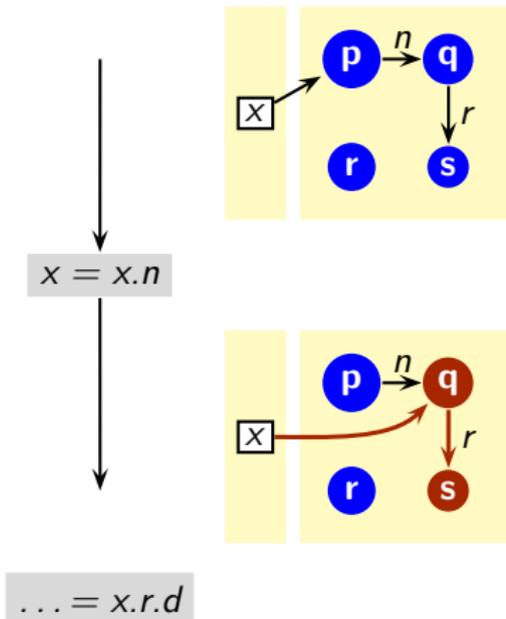
Key Idea #2 : Transfer of Access Paths



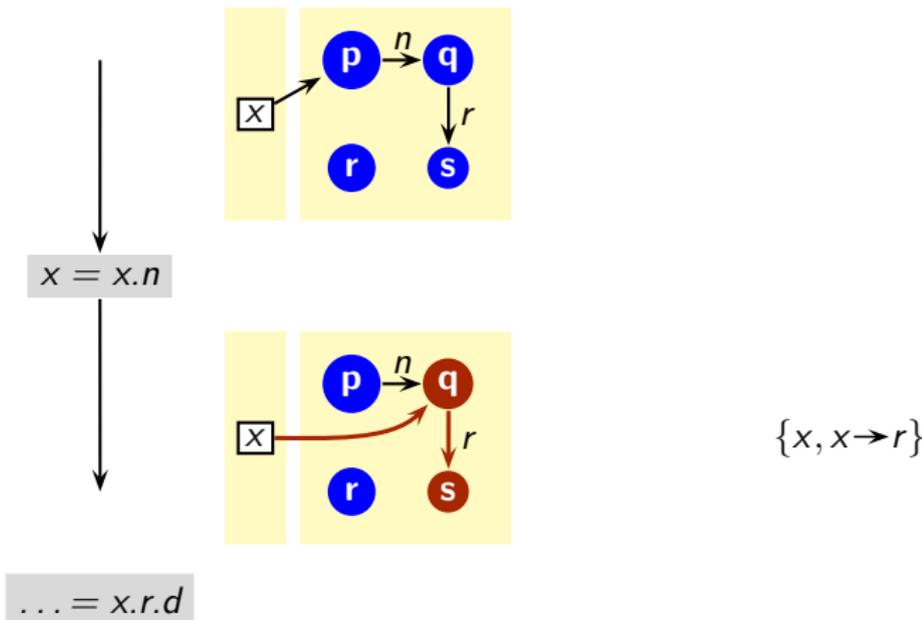
Key Idea #2 : Transfer of Access Paths



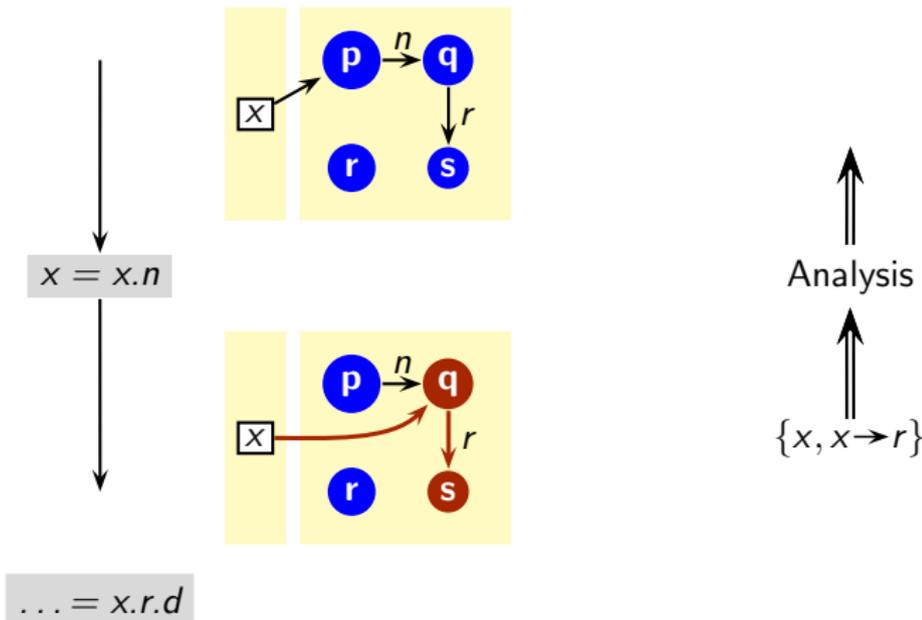
Key Idea #2 : Transfer of Access Paths



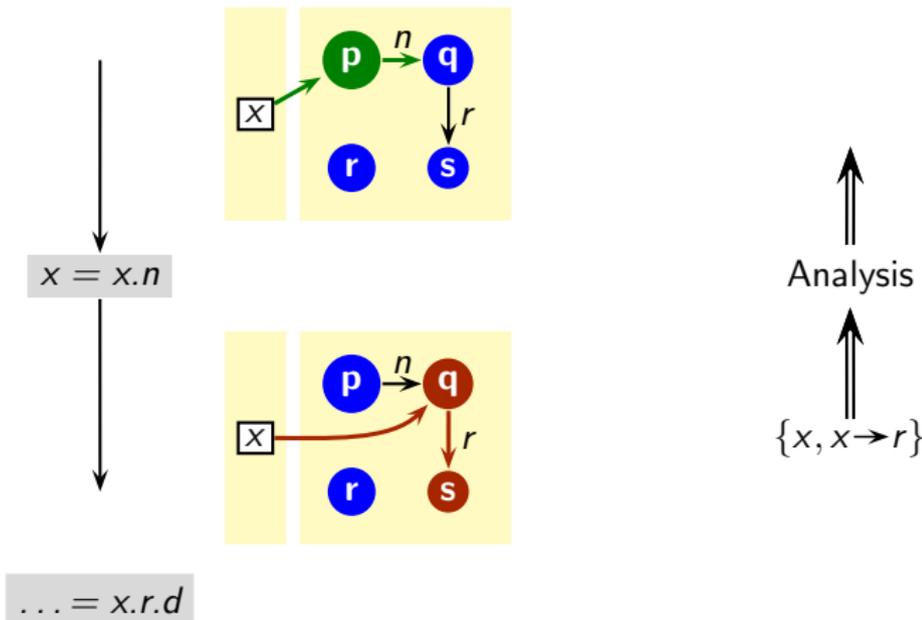
Key Idea #2 : Transfer of Access Paths



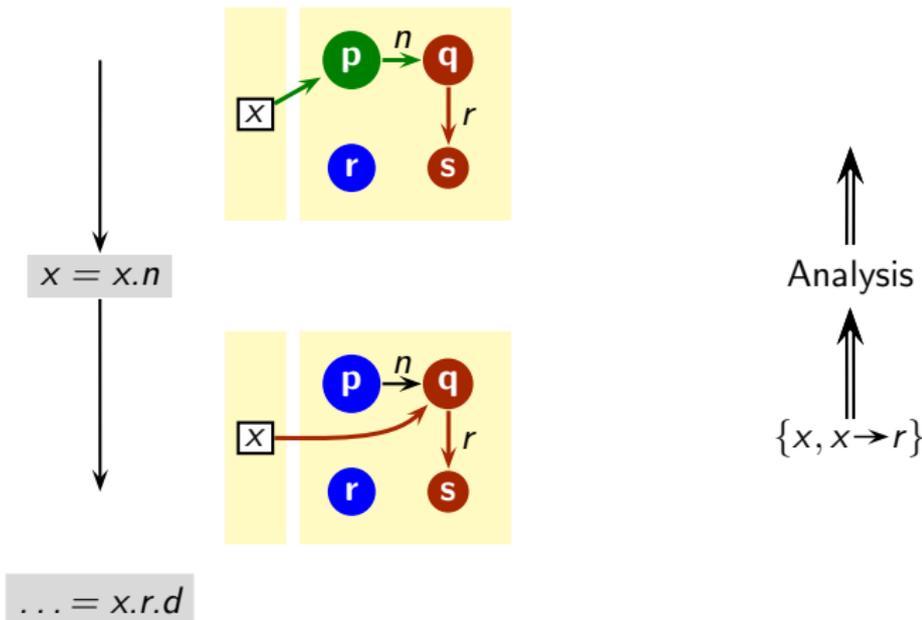
Key Idea #2 : Transfer of Access Paths



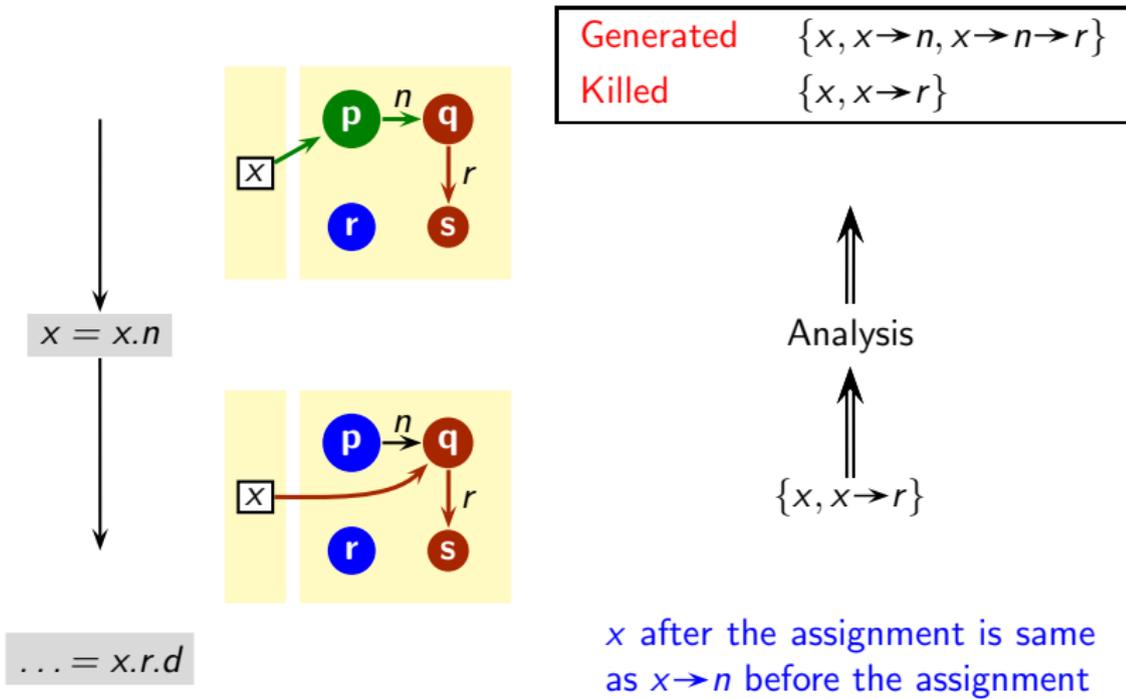
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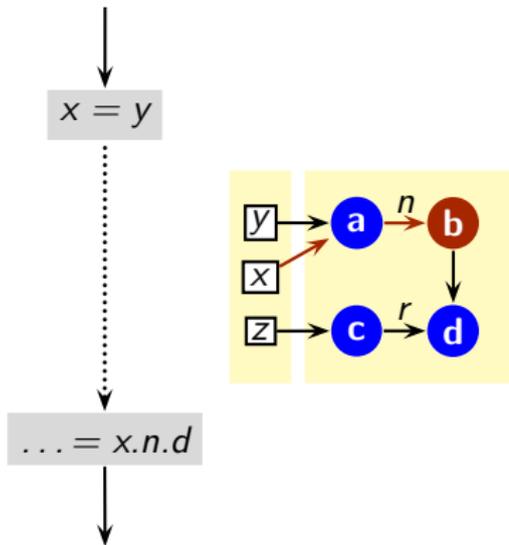
Key Idea #2 : Transfer of Access Paths



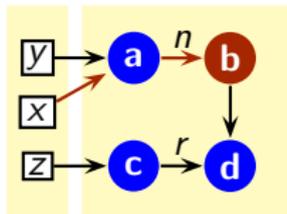
Key Idea #2 : Transfer of Access Paths



Key Idea #3 : Liveness Closure Under Link Aliasing



Key Idea #3 : Liveness Closure Under Link Aliasing



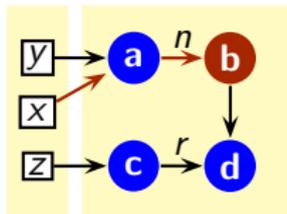
x and y are **node aliases**

$x.n$ and $y.n$ are **link aliases**

$x \rightarrow n$ is live $\Rightarrow y \rightarrow n$ is live



Key Idea #3 : Liveness Closure Under Link Aliasing



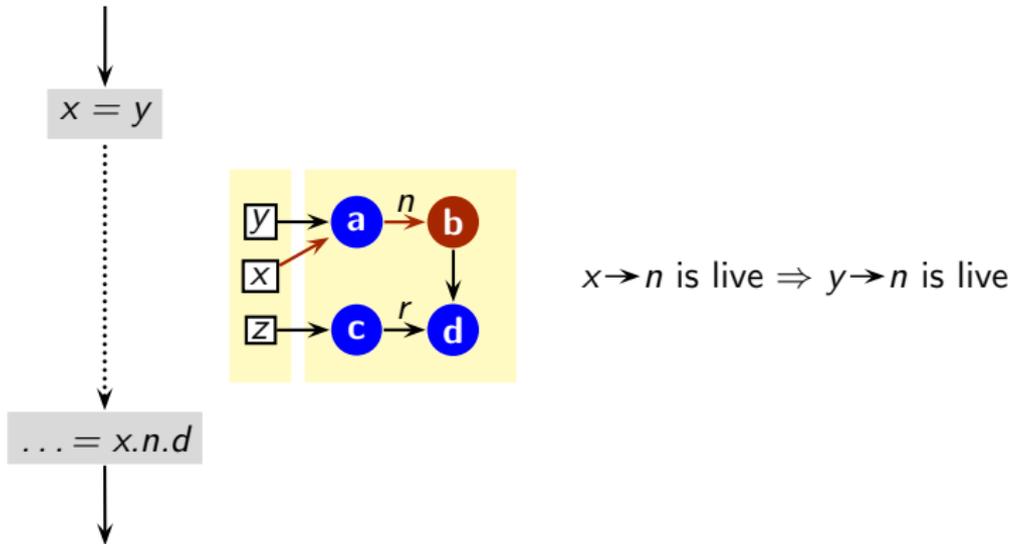
`x` and `y` are **node aliases**

`x.n` and `y.n` are **link aliases**

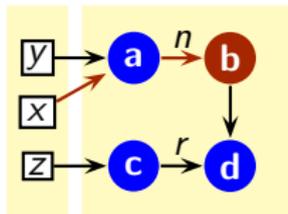
`x → n` is live \Rightarrow `y → n` is live

Nullifying `y → n` will have the side effect of nullifying `x → n`

Explicit and Implicit Liveness



Explicit and Implicit Liveness

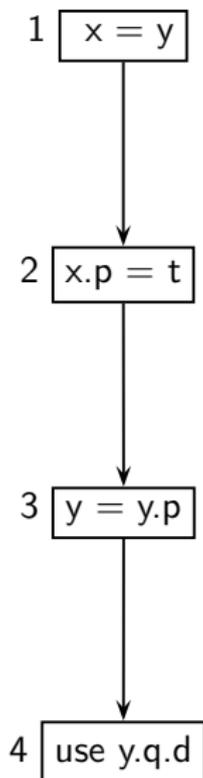


$x \rightarrow n$ is live $\Rightarrow y \rightarrow n$ is live

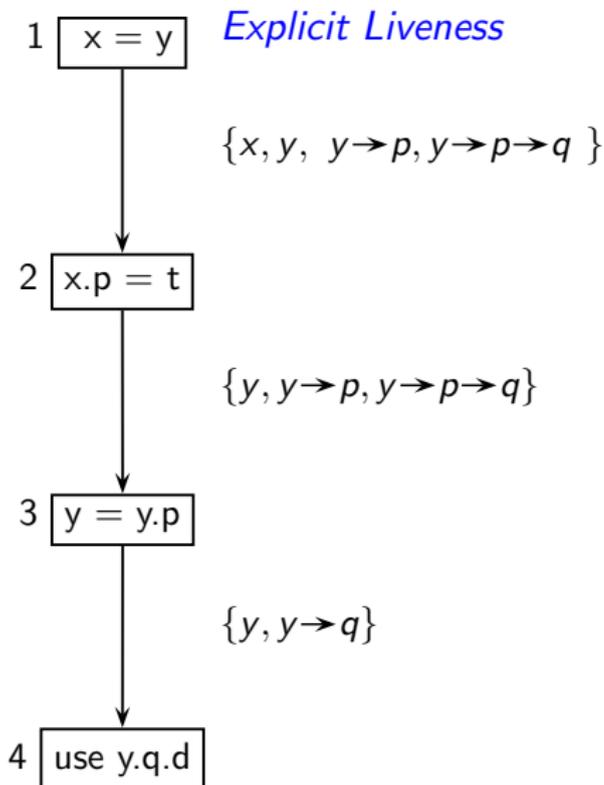
$y \rightarrow n$ is implicitly live
 $x \rightarrow n$ is explicitly live



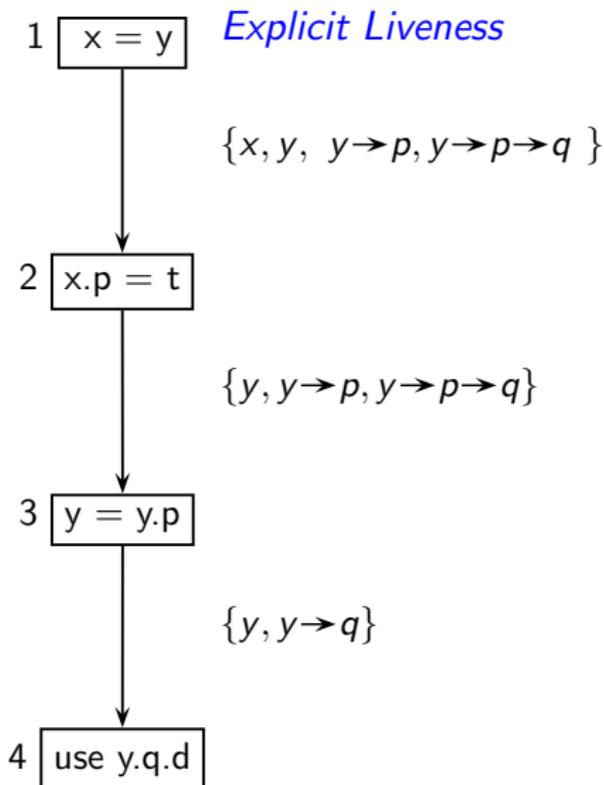
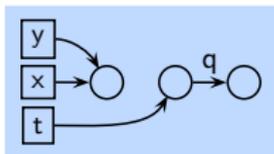
Key Idea #4: Aliasing is Required with Explicit Liveness



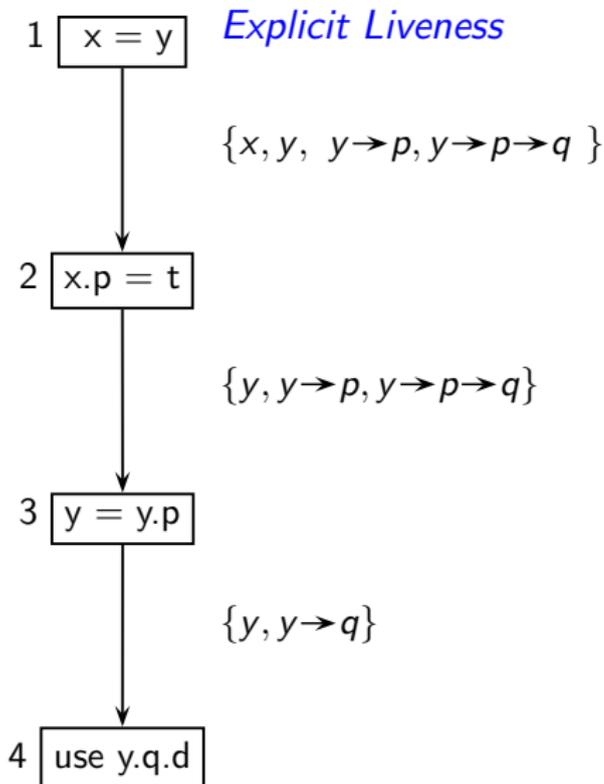
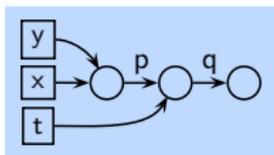
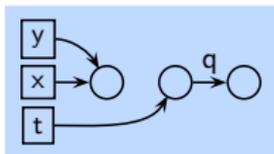
Key Idea #4: Aliasing is Required with Explicit Liveness



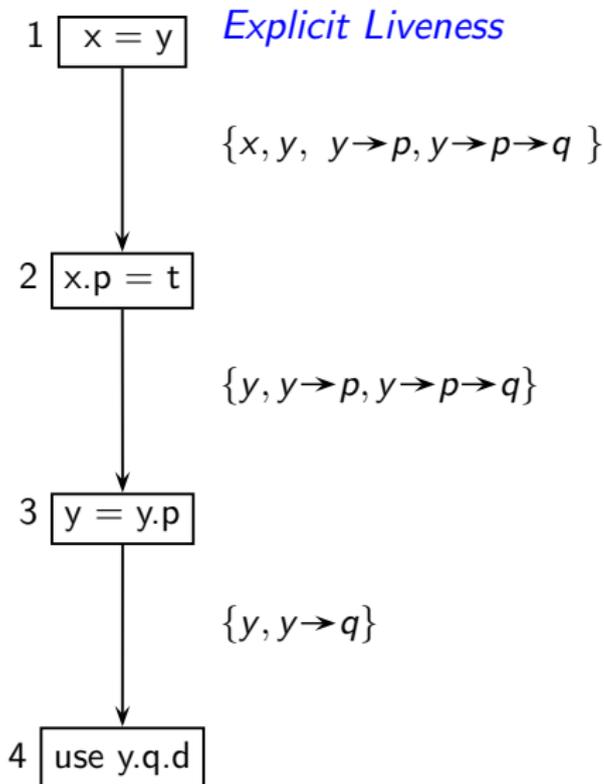
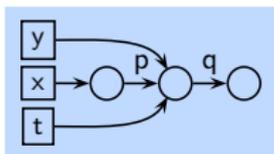
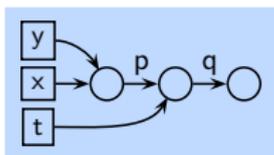
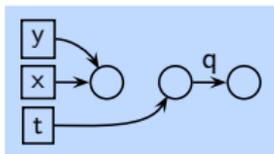
Key Idea #4: Aliasing is Required with Explicit Liveness



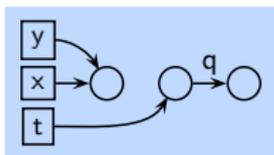
Key Idea #4: Aliasing is Required with Explicit Liveness



Key Idea #4: Aliasing is Required with Explicit Liveness



Key Idea #4: Aliasing is Required with Explicit Liveness

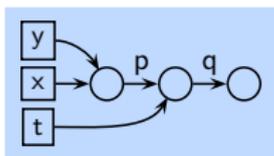


1 $x = y$ *Explicit Liveness*

$\{x, y, y \rightarrow p, y \rightarrow p \rightarrow q\}$

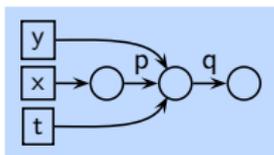
2 $x.p = t$

$\{y, y \rightarrow p, y \rightarrow p \rightarrow q\}$



3 $y = y.p$

$\{y, y \rightarrow q\}$

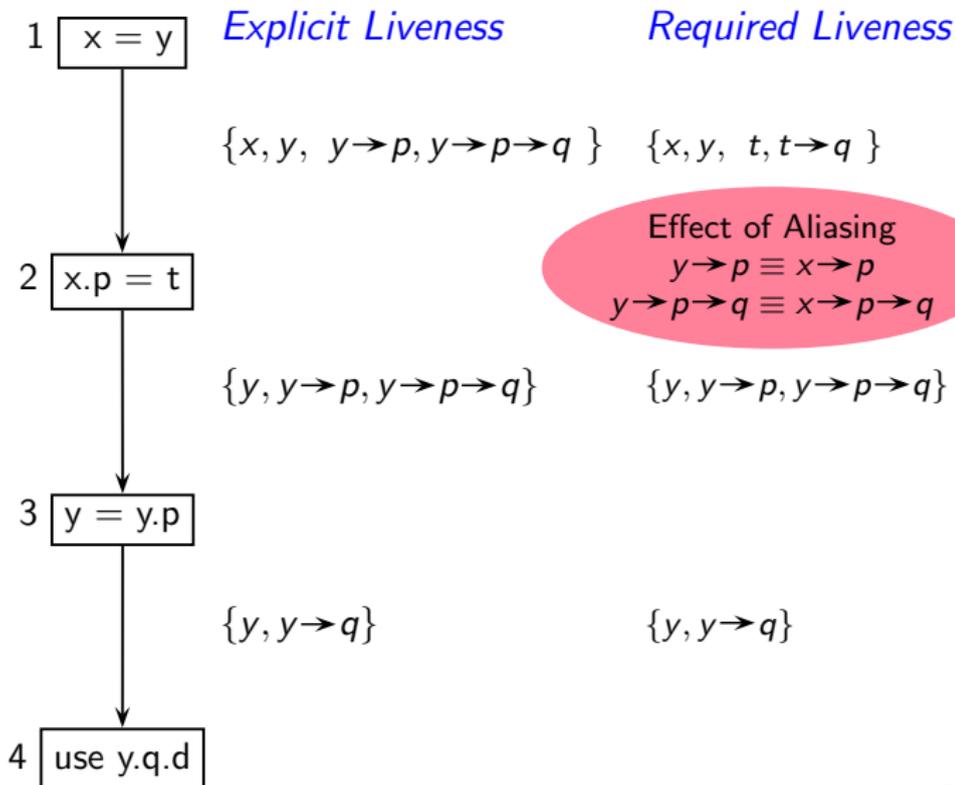
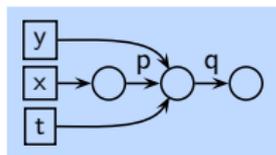
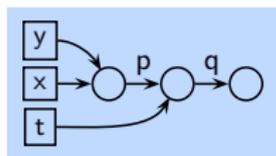
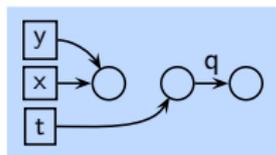


4 use $y.q.d$

Effect of Aliasing
 $y \rightarrow p \equiv x \rightarrow p$
 $y \rightarrow p \rightarrow q \equiv x \rightarrow p \rightarrow q$



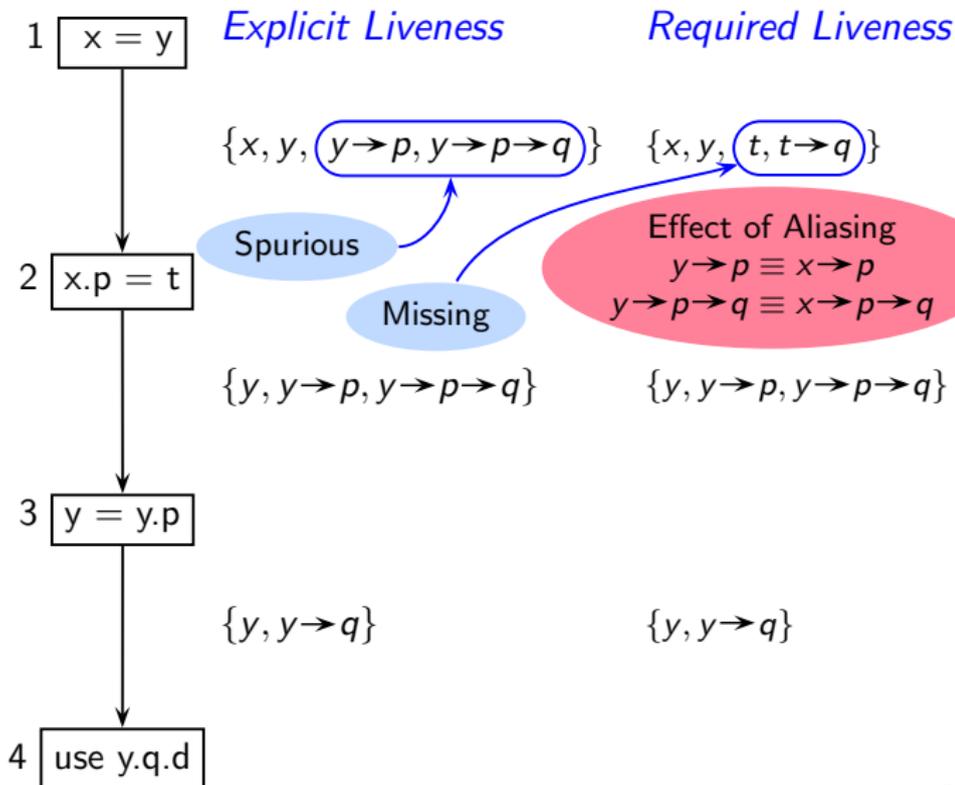
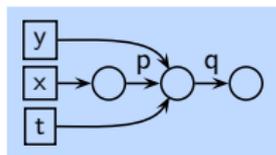
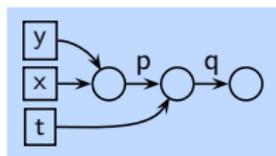
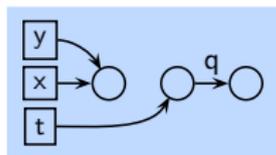
Key Idea #4: Aliasing is Required with Explicit Liveness



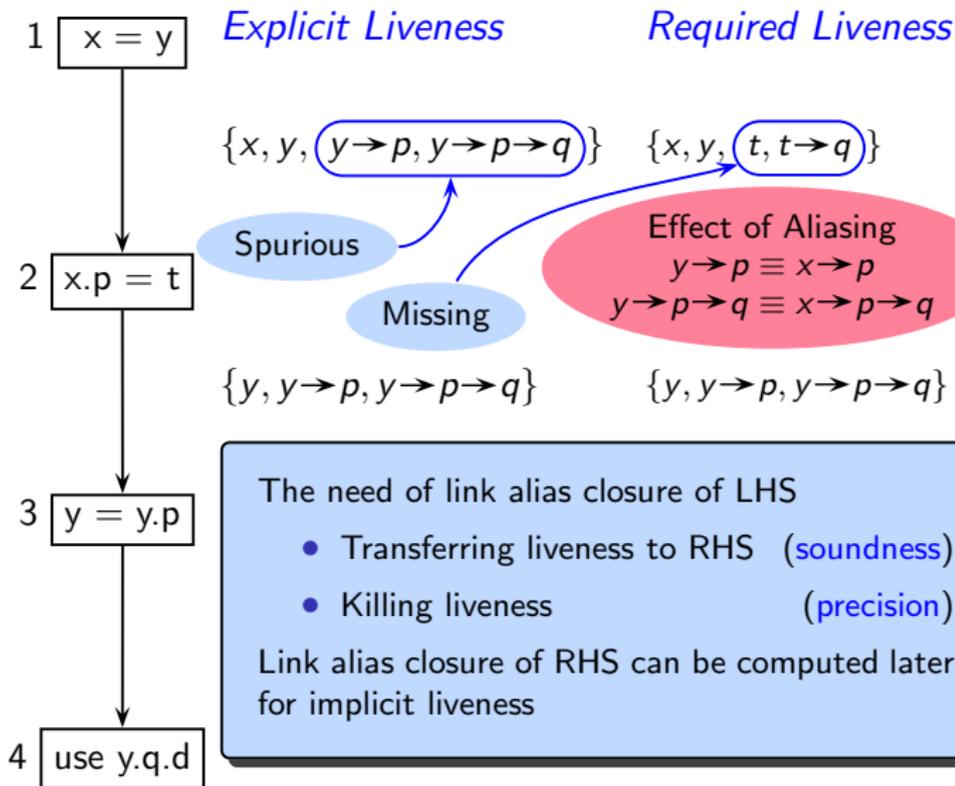
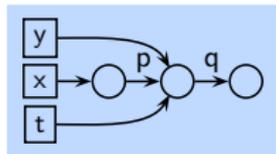
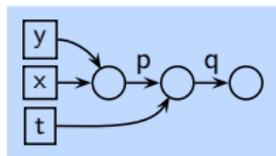
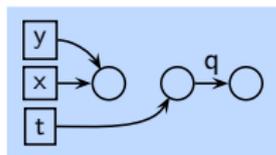
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Key Idea #4: Aliasing is Required with Explicit Liveness



Key Idea #4: Aliasing is Required with Explicit Liveness



Notation for Defining Flow Functions for Explicit Liveness

- Basic entities
 - ▶ Variables $u, v \in \mathbb{V}\text{ar}$
 - ▶ Pointer variables $w, x, y, z \in \mathbf{P} \subseteq \mathbb{V}\text{ar}$
 - ▶ Pointer fields $f, g, h \in pF$
 - ▶ Non-pointer fields $a, b, c, d \in npF$
- Additional notation
 - ▶ Sequence of pointer fields $\sigma \in pF^*$ (could be ϵ)
 - ▶ Access paths $\rho \in \mathbf{P} \times pF^*$
Example: $\{x, x \rightarrow f, x \rightarrow f \rightarrow g\}$
 - ▶ Summarized access paths rooted at x or $x \rightarrow \sigma$ for a given x and σ
 - ▶ $x \rightarrow * = \{x \rightarrow \sigma \mid \sigma \in pF^*\}$
 - ▶ $x \rightarrow \sigma \rightarrow * = \{x \rightarrow \sigma \rightarrow \sigma' \mid \sigma' \in pF^*\}$



Data Flow Equations for Explicit Liveness Analysis

$$In_n = (Out_n - Kill_n(Out_n)) \cup Gen_n(Out_n)$$

$$Out_n = \begin{cases} BI & n \text{ is } End \\ \bigcup_{s \in succ(n)} In_s & \text{otherwise} \end{cases}$$



Flow Functions for Explicit Liveness Analysis

Let A denote May Aliases at the exit of node n

Statement n	$\text{Gen}_n(X)$	$\text{Kill}_n(X)$
$x = y$	$\{y \rightarrow \sigma \mid x \rightarrow \sigma \in X\}$	$x \rightarrow *$
$x = y.f$	$\{y \rightarrow f \rightarrow \sigma \mid x \rightarrow \sigma \in X\}$	$x \rightarrow *$
$x.f = y$	$\{y \rightarrow \sigma \mid z \rightarrow f \rightarrow \sigma \in X, z \in A(x)\}$	$\bigcup_{z \in \text{Must}(A)(x)} z \rightarrow f \rightarrow *$
$x = \text{new}$	\emptyset	$x \rightarrow *$
$x = \text{null}$	\emptyset	$x \rightarrow *$
other	\emptyset	\emptyset



Flow Functions for Explicit Liveness Analysis

Let A denote May Aliases at the exit of node n

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May link aliasing for soundness



Flow Functions for Explicit Liveness Analysis

Let A denote May Aliases at the exit of node n

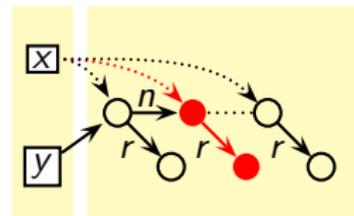
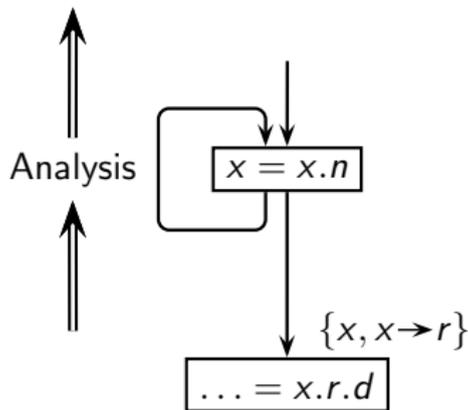
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$x.f = y$	$\{y \rightarrow \sigma \mid \boxed{z \rightarrow f \rightarrow \sigma \in X, z \in A(x)}\}$	$\boxed{\bigcup_{z \in Must(A)(x)} z \rightarrow f \rightarrow *}$
$x = new$	\emptyset	$x \rightarrow *$
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May link aliasing for soundness

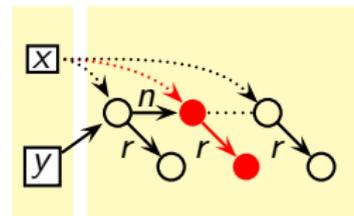
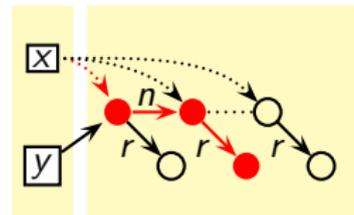
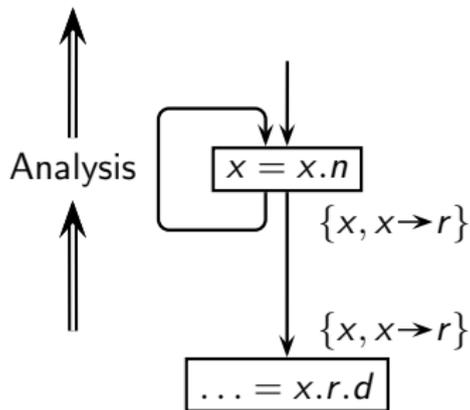
Must link aliasing for precision



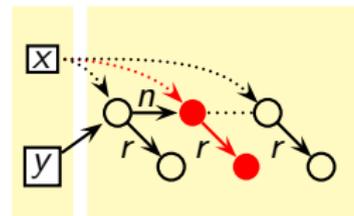
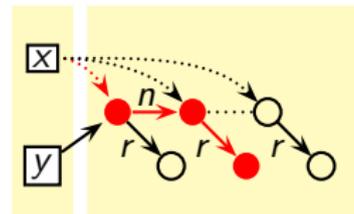
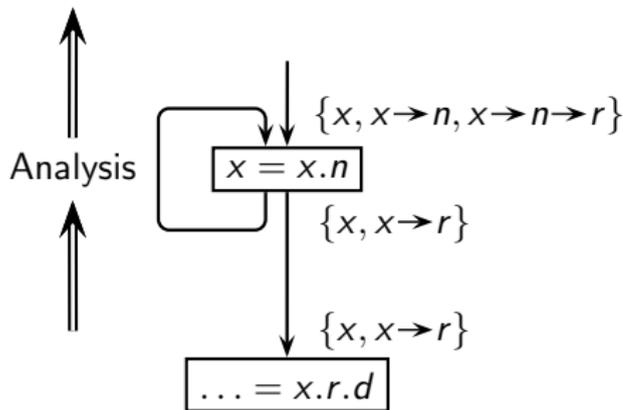
Computing Explicit Liveness Using Sets of Access Paths



Computing Explicit Liveness Using Sets of Access Paths

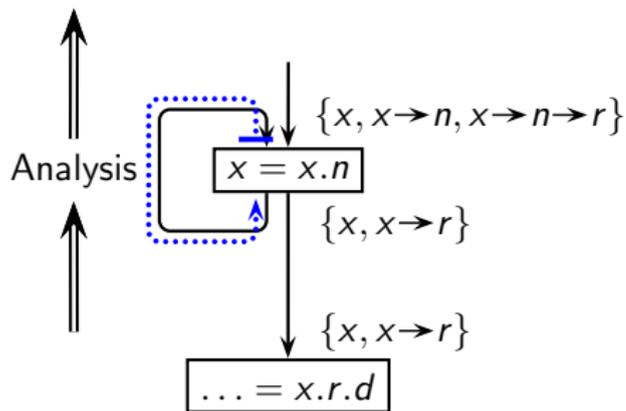


Computing Explicit Liveness Using Sets of Access Paths



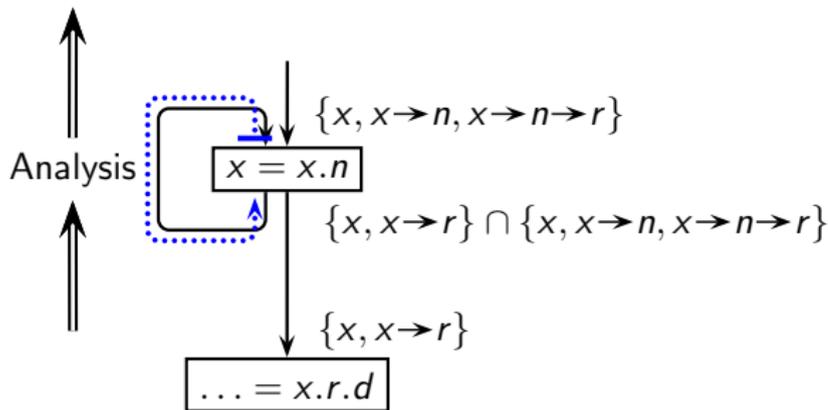
Computing Explicit Liveness Using Sets of Access Paths

Anticipability of Heap References: An *All Paths* problem



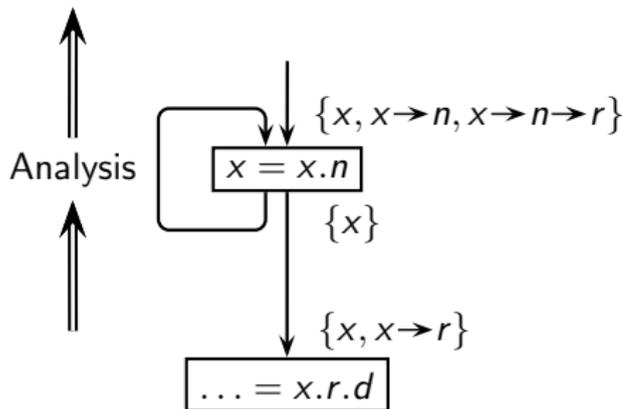
Computing Explicit Liveness Using Sets of Access Paths

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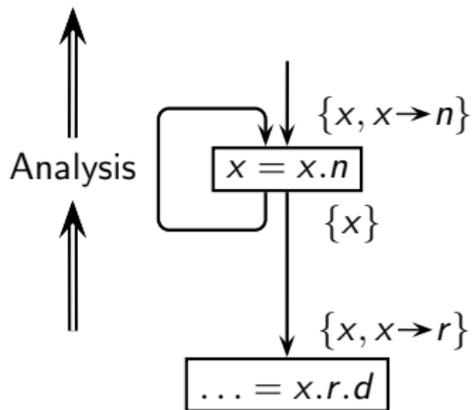
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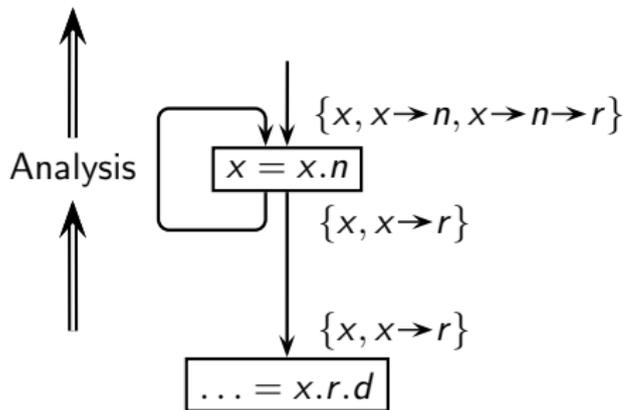
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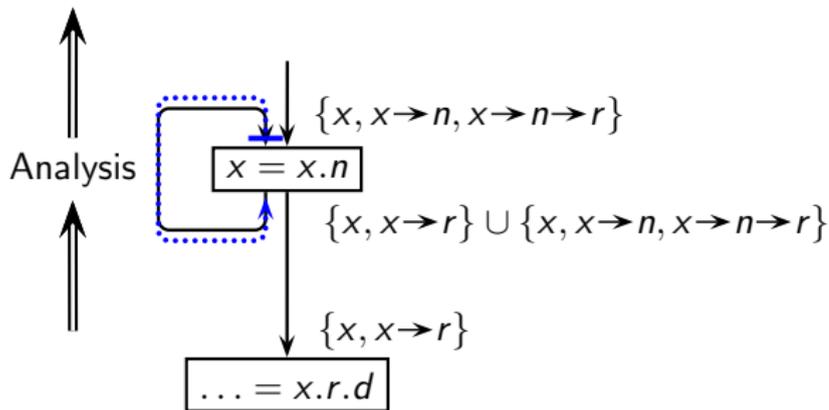
Computing Explicit Liveness Using Sets of Access Paths

Liveness of Heap References: An *Any Path* problem



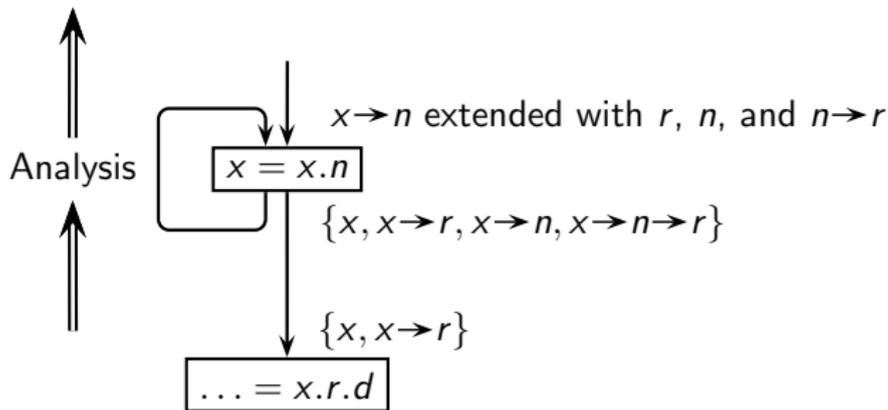
Computing Explicit Liveness Using Sets of Access Paths

Liveness of Heap References: An *Any Path* problem



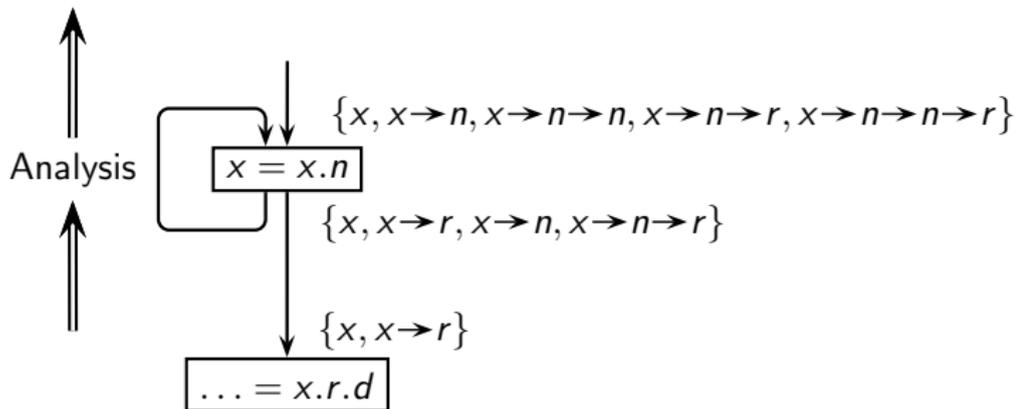
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Liveness of Heap References: An *Any Path* problem



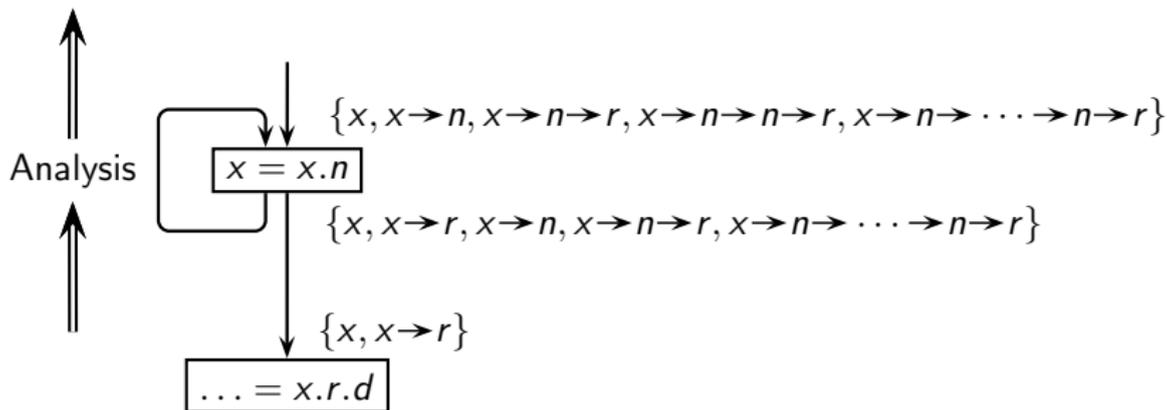
Computing Explicit Liveness Using Sets of Access Paths

Liveness of Heap References: An *Any Path* problem



Computing Explicit Liveness Using Sets of Access Paths

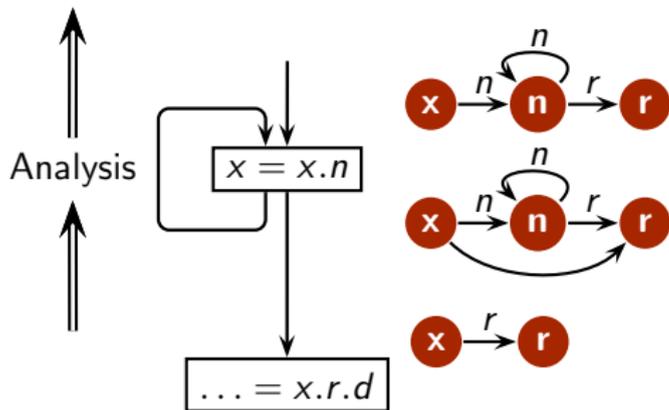
Liveness of Heap References: An *Any Path* problem



Infinite Number of Unbounded Access Paths



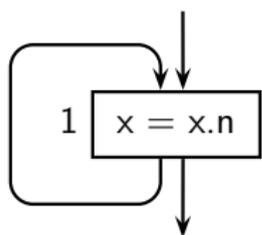
Key Idea #5: Using Graphs as Data Flow Values



Finite Number of Bounded Structures

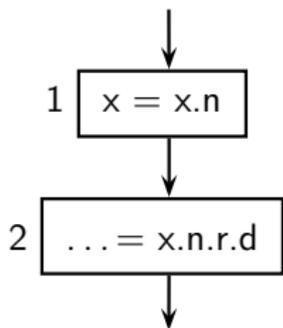


Key Idea #6 : Include Program Point in Graphs



$$\{x, x \rightarrow n, x \rightarrow n \rightarrow n, x \rightarrow n \rightarrow n \rightarrow n, \dots\}$$

Different occurrences of n 's in an access path are
Indistinguishable

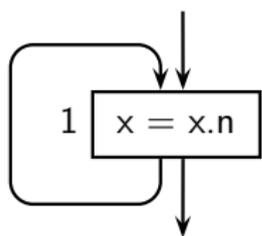


$$\{x, x \rightarrow n, x \rightarrow n \rightarrow n, x \rightarrow n \rightarrow n \rightarrow r\}$$

Different occurrences of n 's in an access path are
Distinct

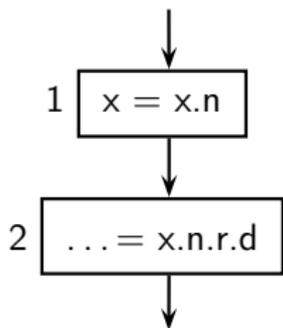


Key Idea #6 : Include Program Point in Graphs



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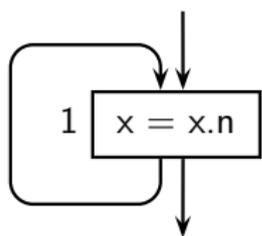


$$\{x, x \rightarrow n, x \rightarrow n \rightarrow n, x \rightarrow n \rightarrow n \rightarrow r\}$$

Different occurrences of n 's in an access path are
Distinct
(pattern of subsequent dereferences could be distinct)

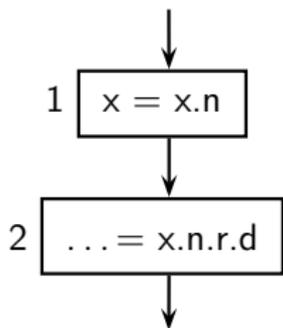


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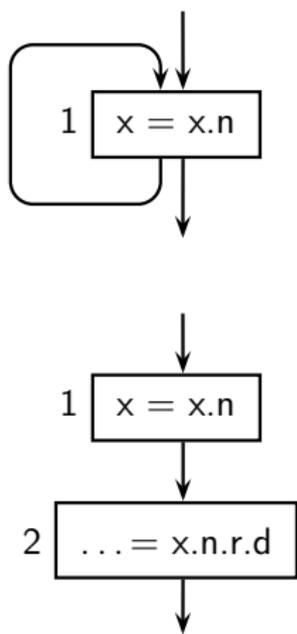


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Key Idea #6 : Include Program Point in Graphs



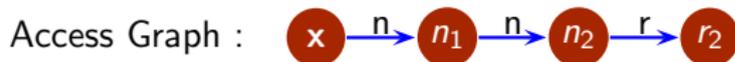
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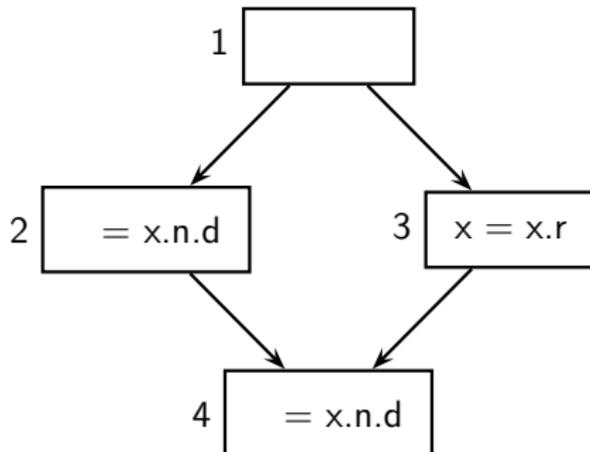


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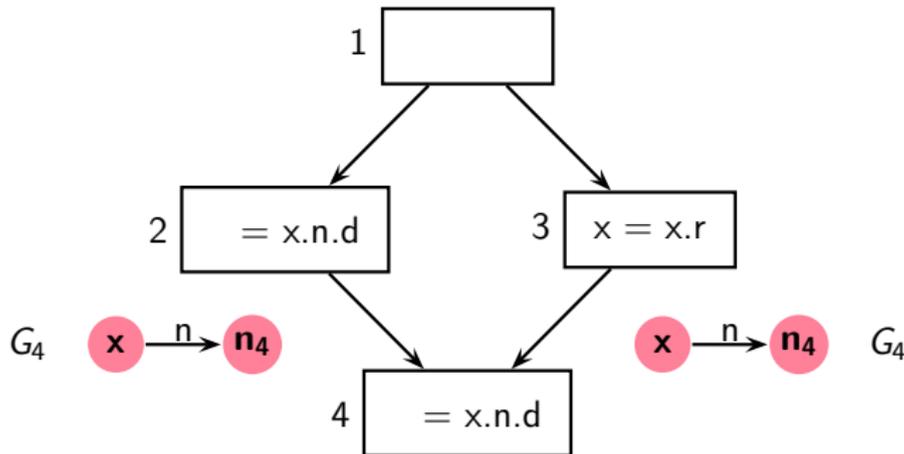
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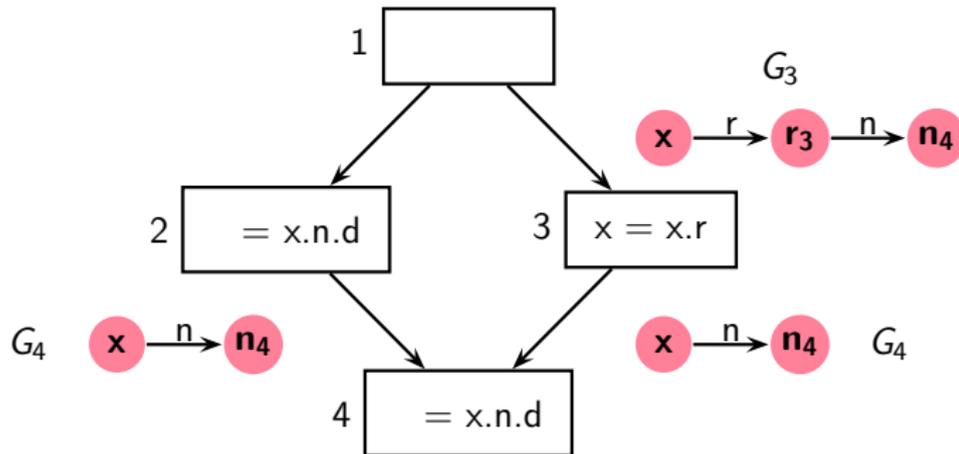
Inclusion of Program Point Facilitates Summarization



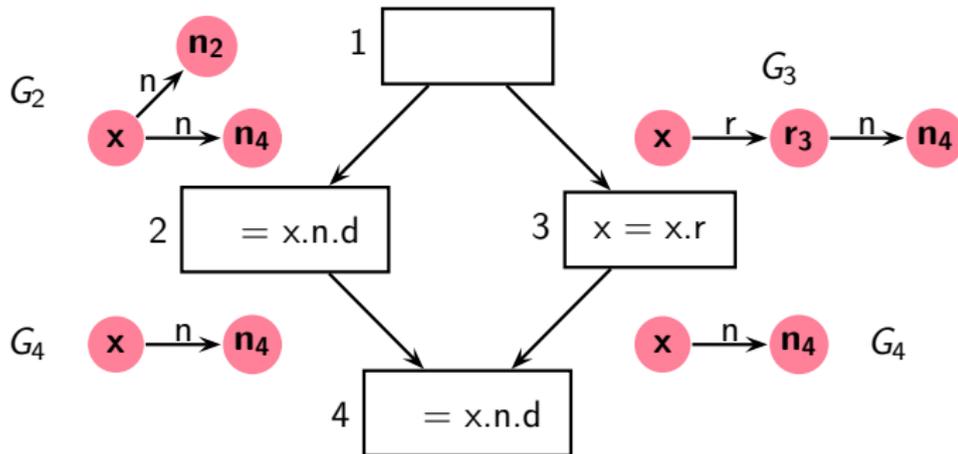
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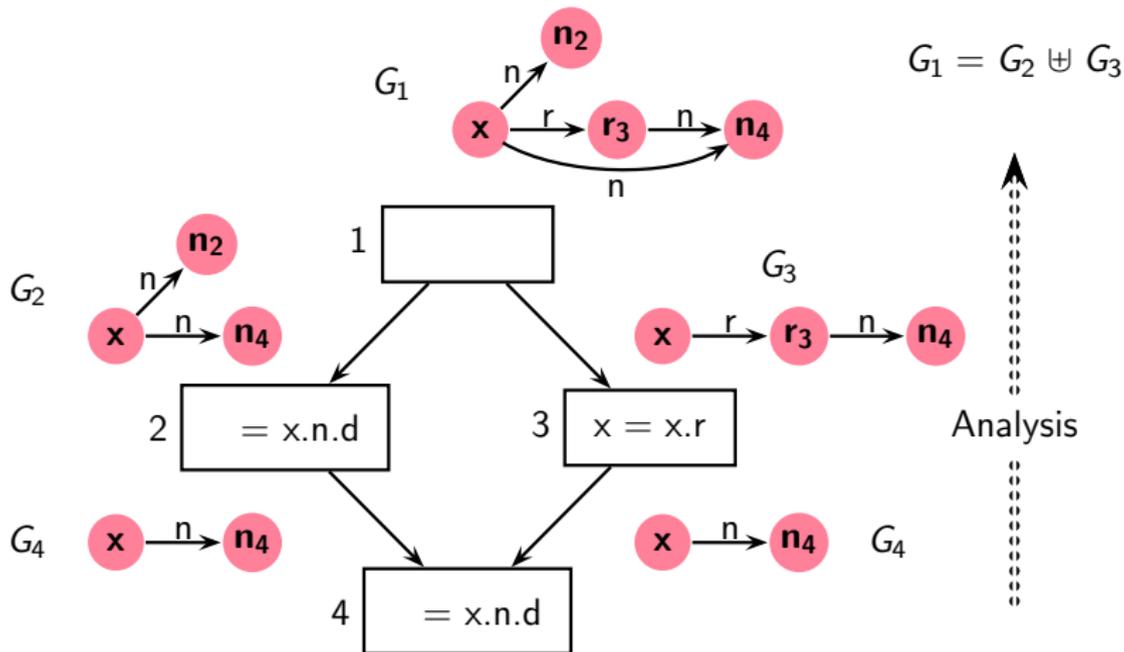
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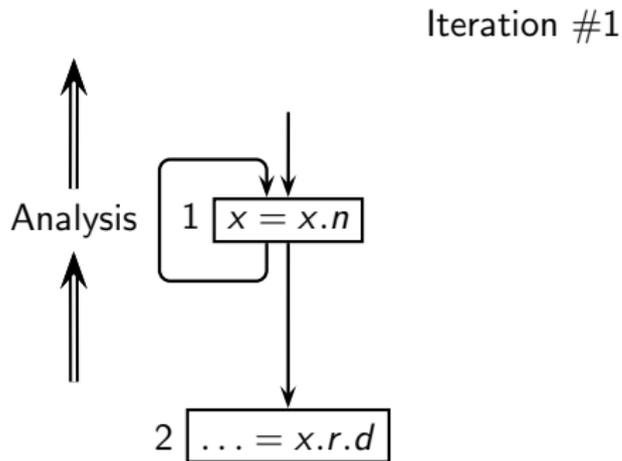
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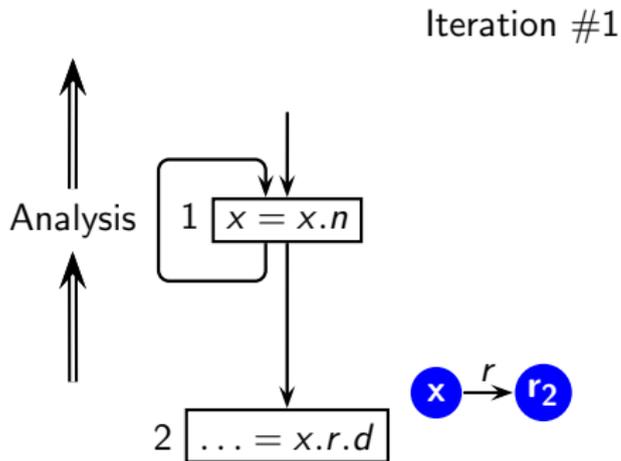
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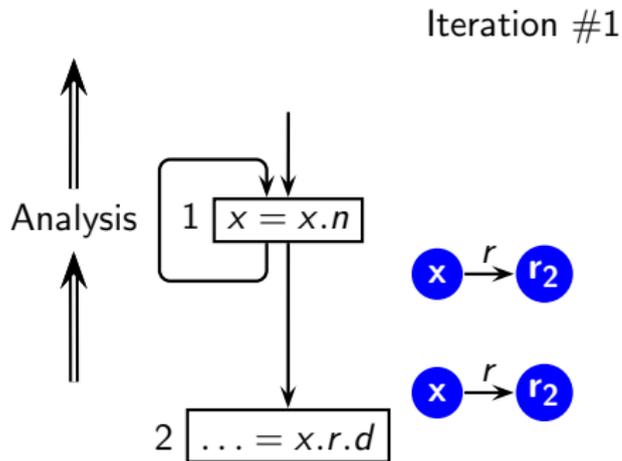
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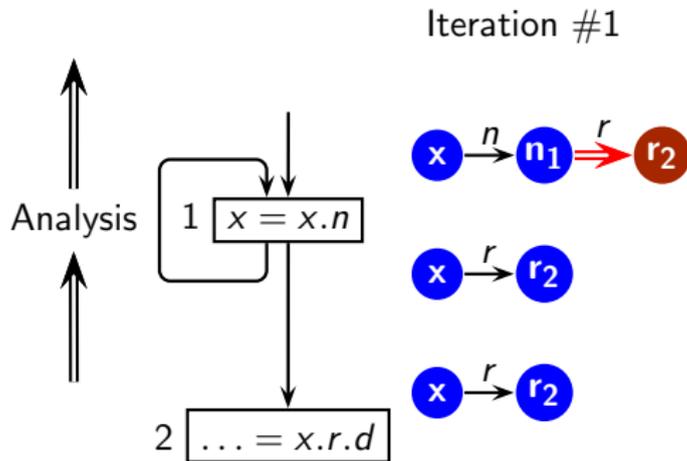
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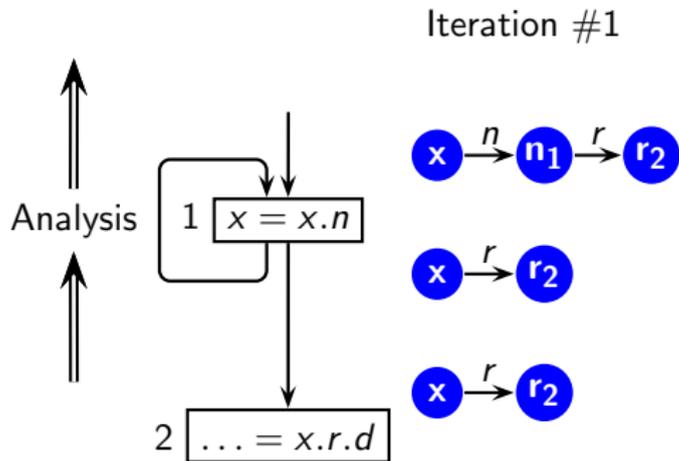
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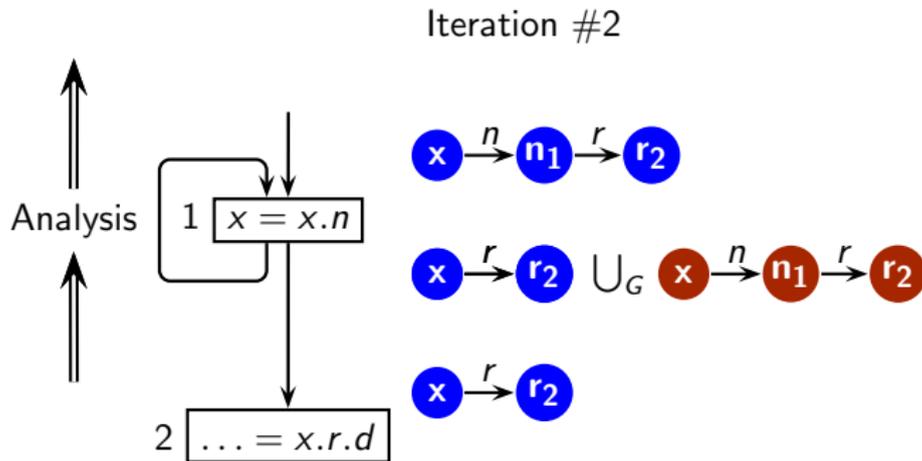
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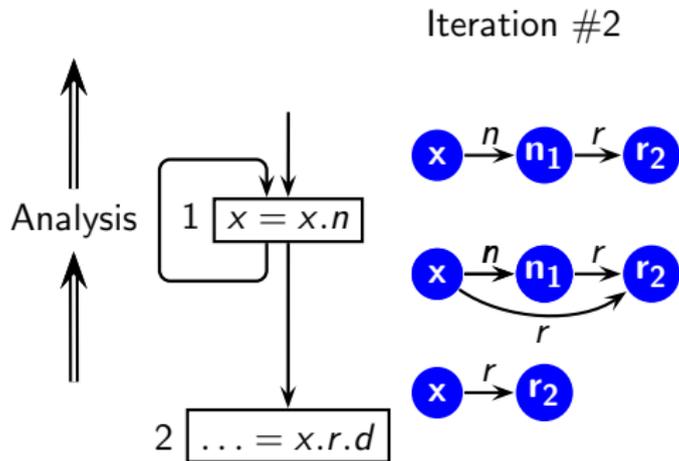
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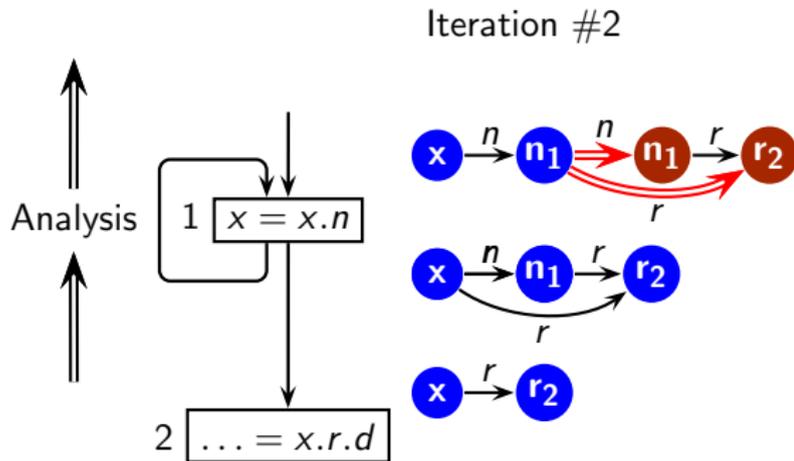
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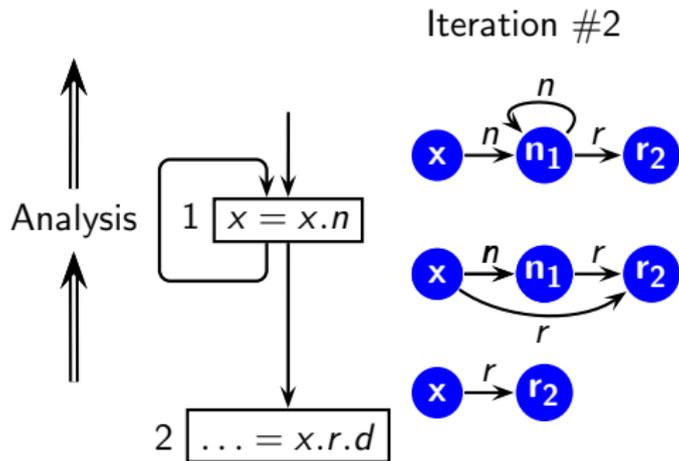
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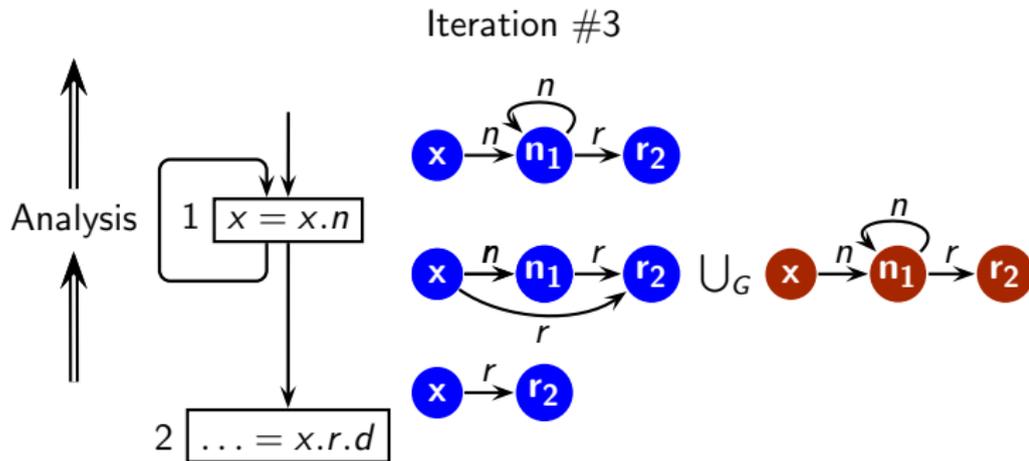
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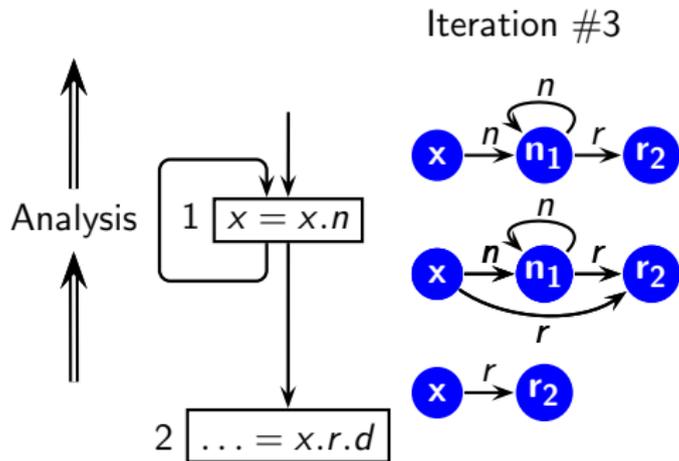
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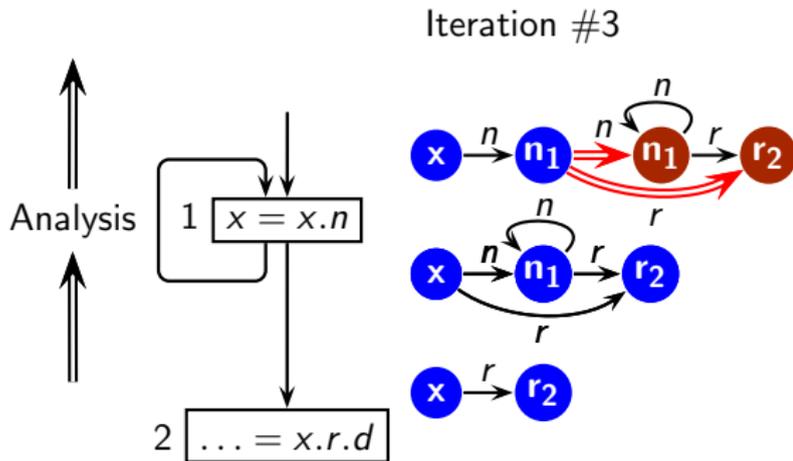
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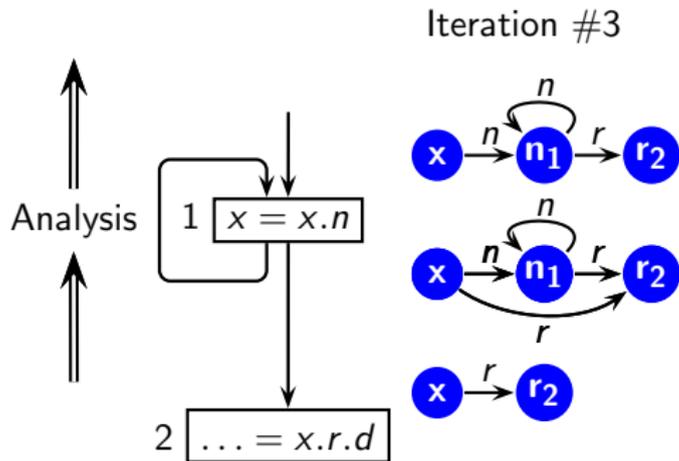
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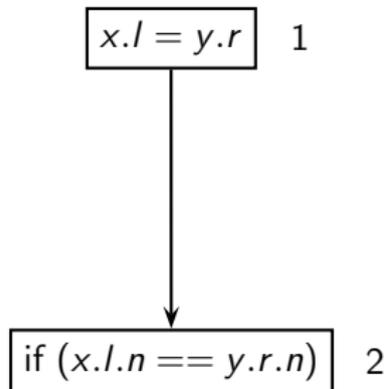


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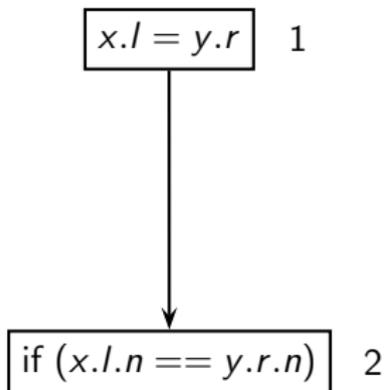
Access Graph and Memory Graph

Program Fragment

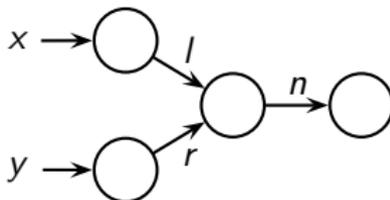


Access Graph and Memory Graph

Program Fragment

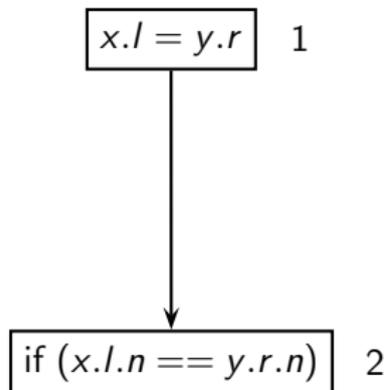


Memory Graph

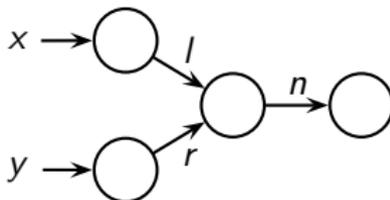


Access Graph and Memory Graph

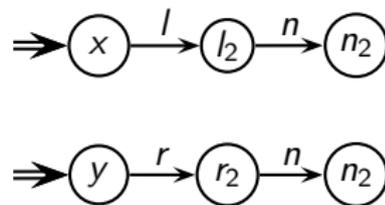
Program Fragment



Memory Graph

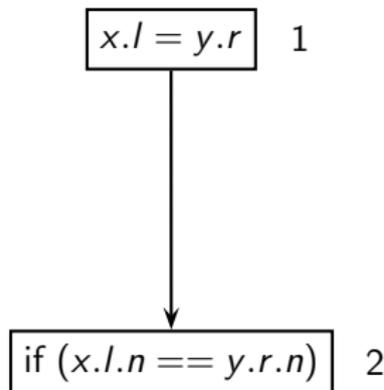


Access Graphs

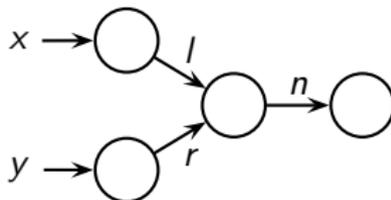


Access Graph and Memory Graph

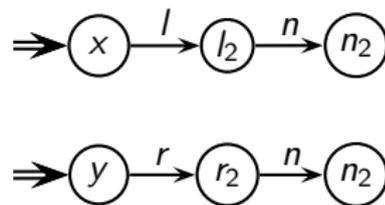
Program Fragment



Memory Graph



Access Graphs

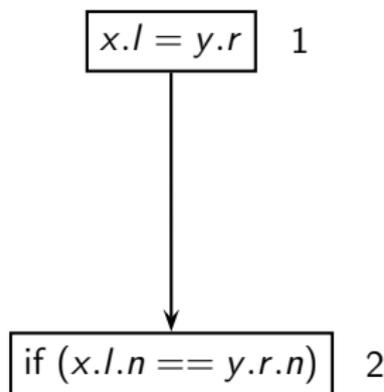


- Memory Graph: Nodes represent locations and edges represent links (i.e. pointers).

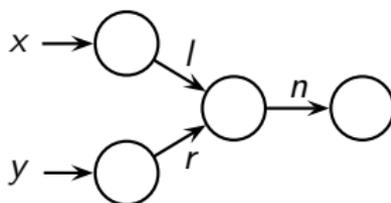


Access Graph and Memory Graph

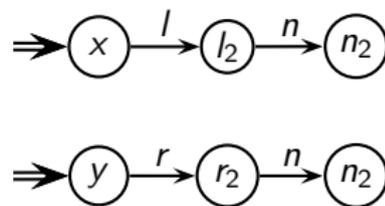
Program Fragment



Memory Graph



Access Graphs



- Memory Graph: Nodes represent locations and edges represent links (i.e. pointers).
- Access Graphs: Nodes represent dereference of links at particular statements. Memory locations are implicit.



Lattice of Access Graphs

- Finite number of nodes in an access graph for a variable
- \sqsubseteq induces a partial order on access graphs
 - \Rightarrow a finite (and hence complete) lattice
 - \Rightarrow All standard results of classical data flow analysis can be extended to this analysis.

Termination and boundedness, convergence on MFP, complexity etc.



Access Graph Operations

- *Union.* $G \uplus G'$
- *Path Removal*
 $G \ominus R$ removes those access paths in G which have $\rho \in R$ as a prefix
- *Factorization* ($/$)
- *Extension*



Defining Factorization

Given statement $x.n = y$, what should be the result of transfer?

Live AP	Memory Graph	Transfer	Remainder
$x \rightarrow n \rightarrow r$		$y \rightarrow r$	r (LHS is contained in the live access path)
$x \rightarrow n$		y	ϵ (LHS is contained in the live access path)
x		no transfer	?? (LHS is not contained in the live access path)



Defining Factorization

Given statement $x.n = y$, what should be the result of transfer?

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$x \rightarrow n \rightarrow r$		$y \rightarrow r$	r (LHS is contained in the live access path)
$x \rightarrow n$		y	ϵ (LHS is contained in the live access path)
x		no transfer	?? (LHS is not contained in the live access path) Quotient is empty So no remainder



Semantics of Access Graph Operations

- $P(G)$ is the set of all paths in graph G
- $P(G, M)$ is the set of paths in G terminating on nodes in M
- S is the set of remainder graphs
- $P(S)$ is the set of all paths in all remainder graphs in S

Operation	Access Paths	
Union $G_3 = G_1 \uplus G_2$	$P(G_3) \supseteq P(G_1) \cup P(G_2)$	
Path Removal $G_2 = G_1 \ominus X$	$P(G_2) \supseteq P(G_1) - \{\rho \rightarrow \sigma \mid \rho \in X, \rho \rightarrow \sigma \in P(G_1)\}$	
Factorization $S = G_1 / \rho$	$P(S) = \{\sigma \mid \rho \rightarrow \sigma \in P(G_1)\}$	
Extension	$G_2 = (G_1, M) \# \emptyset$	$P(G_2) = \emptyset$
	$G_2 = (G_1, M) \# S$	$P(G_2) \supseteq P(G_1) \cup \{\rho \rightarrow \sigma \mid \rho \in P(G_1, M), \sigma \in P(S)\}$



Semantics of Access Graph Operations

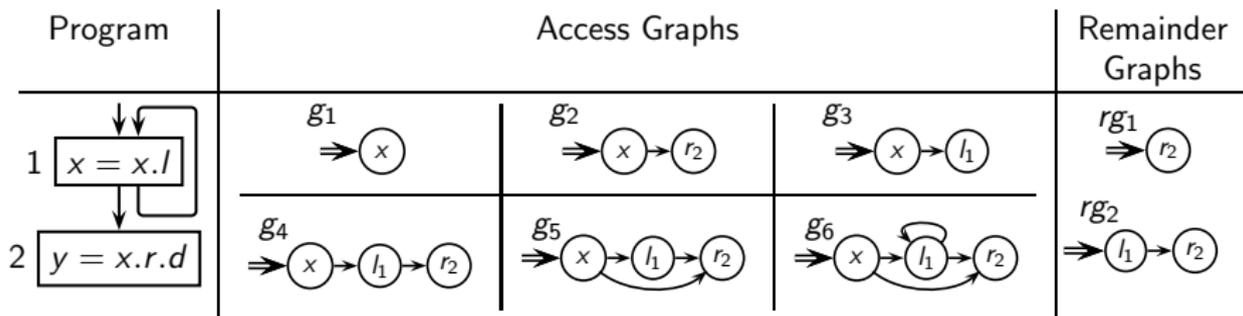
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	$G_2 = (G_1, M) \# S$	$P(G_2) \supseteq P(G_1) \cup \{\rho \rightarrow \sigma \mid \rho \in P(G_1, M), \sigma \in P(S)\}$

σ represents remainder



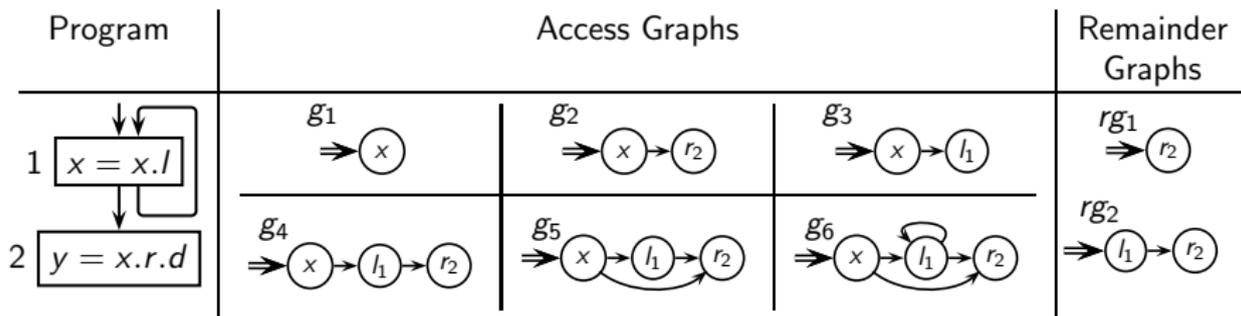
Access Graph Operations: Examples



Union	Path Removal	Factorisation	Extension



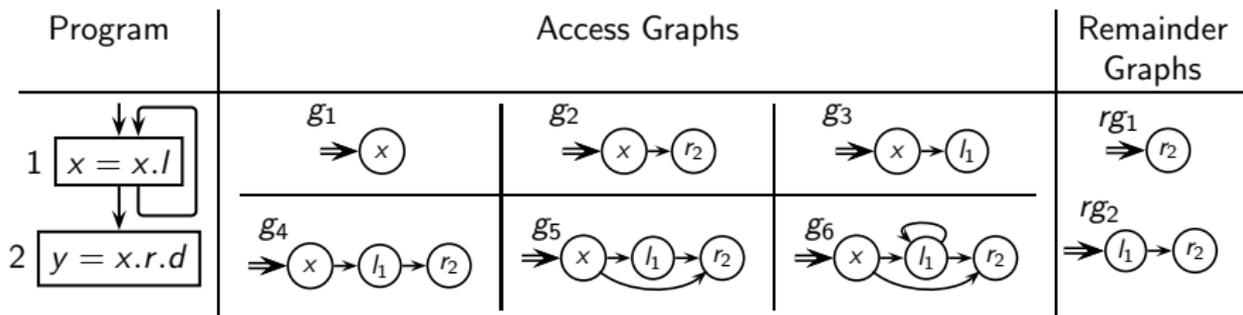
Access Graph Operations: Examples



Union	Path Removal	Factorisation	Extension
$g_3 \uplus g_4 = g_4$			
$g_2 \uplus g_4 = g_5$			
$g_5 \uplus g_4 = g_5$			
$g_5 \uplus g_6 = g_6$			



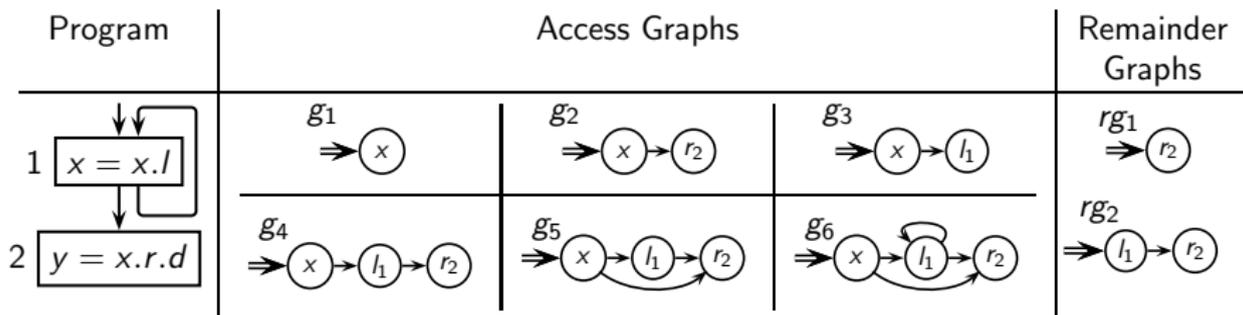
Access Graph Operations: Examples



Union	Path Removal	Factorisation	Extension
$g_3 \uplus g_4 = g_4$	$g_6 \ominus \{x \rightarrow l\} = g_2$		
$g_2 \uplus g_4 = g_5$	$g_5 \ominus \{x\} = \mathcal{E}_G$		
$g_5 \uplus g_4 = g_5$	$g_4 \ominus \{x \rightarrow r\} = g_4$		
$g_5 \uplus g_6 = g_6$	$g_4 \ominus \{x \rightarrow l\} = g_1$		



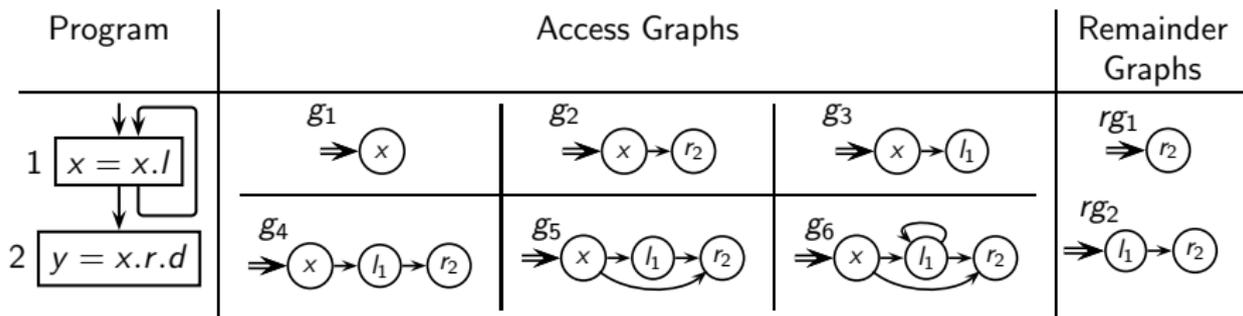
Access Graph Operations: Examples



Union	Path Removal	Factorisation	Extension
$g_3 \uplus g_4 = g_4$	$g_6 \ominus \{x \rightarrow l\} = g_2$	$g_2 / x = \{rg_1\}$	
$g_2 \uplus g_4 = g_5$	$g_5 \ominus \{x\} = \mathcal{E}_G$	$g_5 / x = \{rg_1, rg_2\}$	
$g_5 \uplus g_4 = g_5$	$g_4 \ominus \{x \rightarrow r\} = g_4$	$g_5 / x \rightarrow r = \{\epsilon_{RG}\}$	
$g_5 \uplus g_6 = g_6$	$g_4 \ominus \{x \rightarrow l\} = g_1$	$g_4 / x \rightarrow r = \emptyset$	



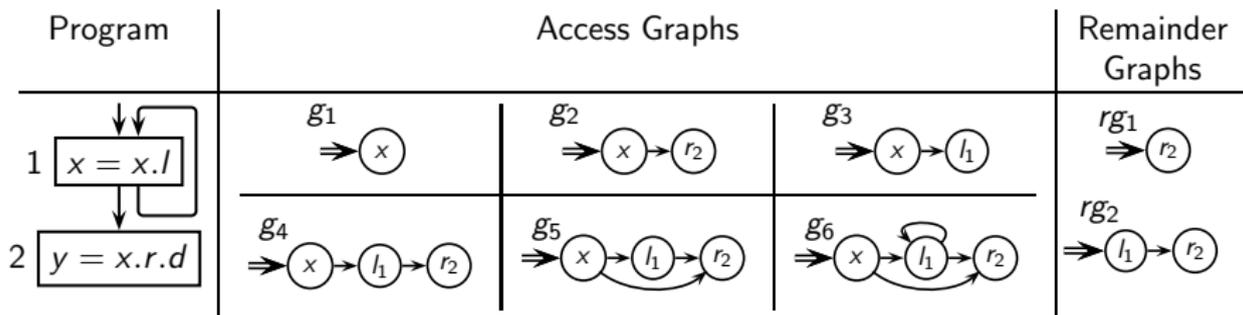
Access Graph Operations: Examples



Union	Path Removal	Factorisation	Extension
$g_3 \uplus g_4 = g_4$	$g_6 \ominus \{x \rightarrow l\} = g_2$	$g_2 / x = \{rg_1\}$	$(g_3, \{l_1\}) \# \{rg_1\} = g_4$
$g_2 \uplus g_4 = g_5$	$g_5 \ominus \{x\} = \mathcal{E}_G$	$g_5 / x = \{rg_1, rg_2\}$	$(g_3, \{x, l_1\}) \# \{rg_1, rg_2\} = g_6$
$g_5 \uplus g_4 = g_5$	$g_4 \ominus \{x \rightarrow r\} = g_4$	$g_5 / x \rightarrow r = \{\epsilon_{RG}\}$	$(g_2, \{r_2\}) \# \{\epsilon_{RG}\} = g_2$
$g_5 \uplus g_6 = g_6$	$g_4 \ominus \{x \rightarrow l\} = g_1$	$g_4 / x \rightarrow r = \emptyset$	$(g_2, \{r_2\}) \# \emptyset = \mathcal{E}_G$



Access Graph Operations: Examples



Union	Path Removal	Factorisation	Extension
$g_3 \uplus g_4 = g_4$	$g_6 \ominus \{x \rightarrow l\} = g_2$	$g_2 / x = \{rg_1\}$	$(g_3, \{l_1\}) \# \{rg_1\} = g_4$
$g_2 \uplus g_4 = g_5$	$g_5 \ominus \{x\} = \mathcal{E}_G$	$g_5 / x = \{rg_1, rg_2\}$	$(g_3, \{x, l_1\}) \# \{rg_1, rg_2\} = g_6$
$g_5 \uplus g_4 = g_5$	$g_4 \ominus \{x \rightarrow r\} = g_4$	$g_5 / x \rightarrow r = \{\epsilon_{RG}\}$	$(g_2, \{r_2\}) \# \{\epsilon_{RG}\} = g_2$
$g_5 \uplus g_6 = g_6$	$g_4 \ominus \{x \rightarrow l\} = g_1$	$g_4 / x \rightarrow r = \emptyset$	$(g_2, \{r_2\}) \# \emptyset = \mathcal{E}_G$

Remainder is empty

Quotient is empty



Data Flow Equations for Explicit Liveness Analysis: Access Graphs Version

$$In_n = (Out_n \ominus Kill_n(Out_n)) \uplus Gen_n(Out_n)$$

$$Out_n = \begin{cases} BI & n \text{ is End} \\ \uplus_{s \in succ(n)} In_s & \text{otherwise} \end{cases}$$

- In_n , Out_n , and Gen_n are access graphs
- $Kill_n$ is a set of access paths



Flow Functions for Explicit Liveness Analysis: Access Paths Version

Let A denote May Aliases at the exit of node n

Statement n	$\text{Gen}_n(X)$	$\text{Kill}_n(X)$
$x = y$	$\{y \rightarrow \sigma \mid x \rightarrow \sigma \in X\}$	$x \rightarrow *$
$x = y.f$	$\{y \rightarrow f \rightarrow \sigma \mid x \rightarrow \sigma \in X\}$	$x \rightarrow *$
$x.f = y$	$\{y \rightarrow \sigma \mid z \rightarrow f \rightarrow \sigma \in X, z \in A(x)\}$	$\bigcup_{z \in \text{Must}(A)(x)} z \rightarrow f \rightarrow *$
$x = \text{new}$	\emptyset	$x \rightarrow *$
$x = \text{null}$	\emptyset	$x \rightarrow *$
other	\emptyset	\emptyset



Flow Functions for Explicit Liveness Analysis: Access Paths Version

Let A denote May Aliases at the exit of node n

Statement n	$\text{Gen}_n(X)$	$\text{Kill}_n(X)$
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$x = y.f$	$\{y \rightarrow f \rightarrow \sigma \mid x \rightarrow \sigma \in X\}$	$x \rightarrow *$
$x.f = y$	$\{y \rightarrow \sigma \mid \boxed{z \rightarrow f \rightarrow \sigma \in X, z \in A(x)}\}$	$\bigcup_{z \in \text{Must}(A)(x)} z \rightarrow f \rightarrow *$
$x = \text{new}$	\emptyset	$x \rightarrow *$
$x = \text{null}$	\emptyset	$x \rightarrow *$
other	\emptyset	\emptyset

May link aliasing for soundness



Flow Functions for Explicit Liveness Analysis: Access Paths Version

Let A denote May Aliases at the exit of node n

Statement n	$\text{Gen}_n(X)$	$\text{Kill}_n(X)$
$x = y$	$\{y \rightarrow \sigma \mid x \rightarrow \sigma \in X\}$	$x \rightarrow *$
$x = y.f$	$\{y \rightarrow f \rightarrow \sigma \mid x \rightarrow \sigma \in X\}$	$x \rightarrow *$
$x.f = y$	$\{y \rightarrow \sigma \mid \boxed{z \rightarrow f \rightarrow \sigma \in X, z \in A(x)}\}$	$\boxed{\bigcup_{z \in \text{Must}(A)(x)} z \rightarrow f \rightarrow *}$
$x = \text{new}$	\emptyset	$x \rightarrow *$
$x = \text{null}$	\emptyset	$x \rightarrow *$
other	\emptyset	\emptyset

May link aliasing for soundness

Must link aliasing for precision



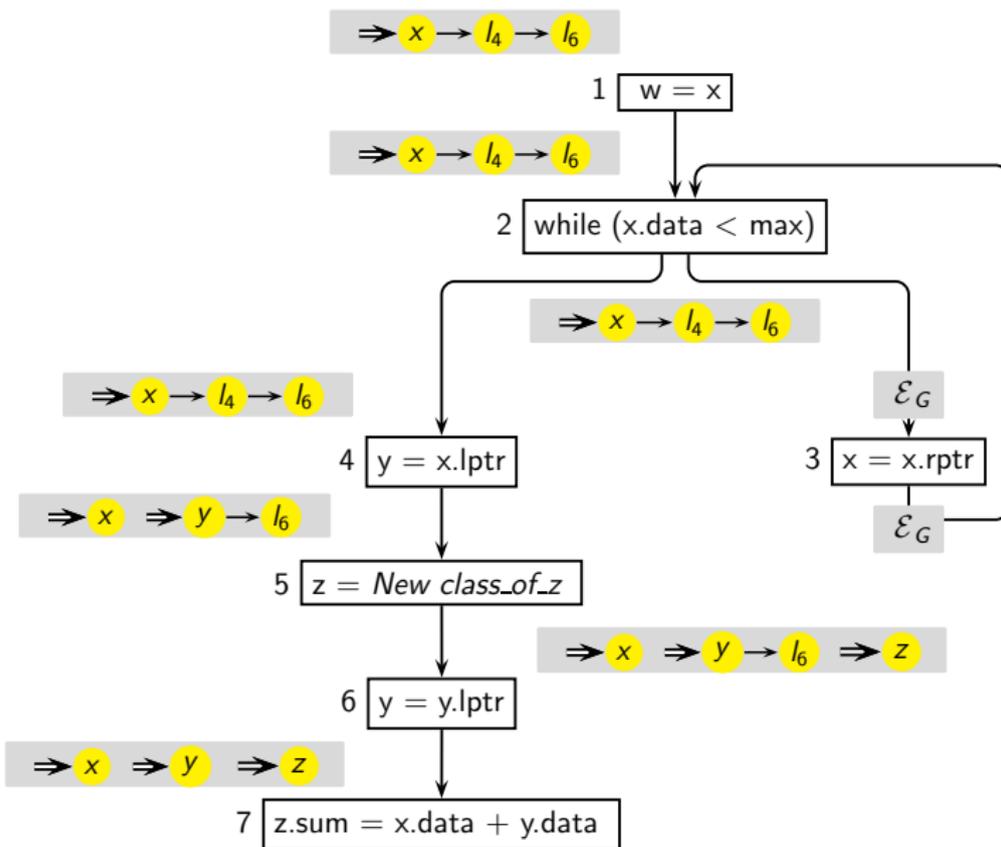
Flow Functions for Explicit Liveness Analysis: Access Graphs Version

- A denotes May Aliases at the exit of node n
- $mkGraph(\rho)$ creates an access graph for access path ρ

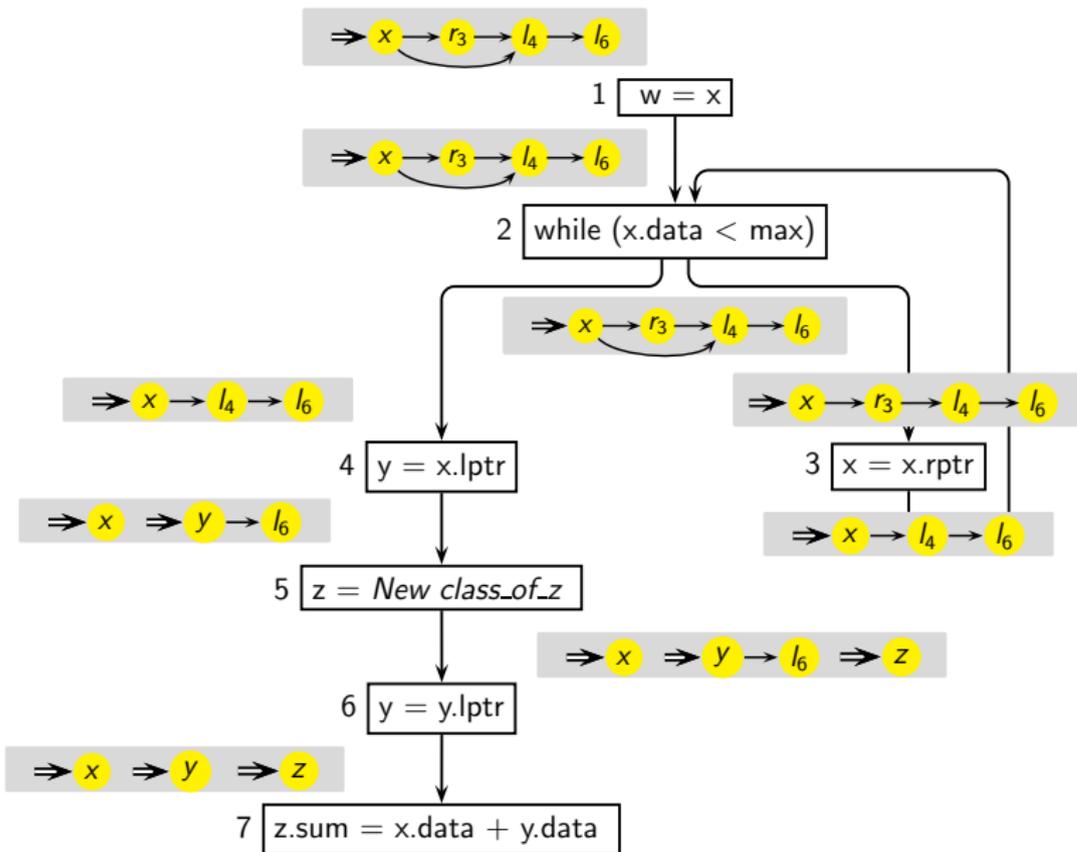
Statement n	$Gen_n(X)$	$Kill_n(X)$
$x = y$	$mkGraph(y)\#(X/x)$	$\{x\}$
$x = y.f$	$mkGraph(y \rightarrow f)\#(X/x)$	$\{x\}$
$x.f = y$	$mkGraph(y)\# \left(\bigcup_{z \in A(x)} (X/(z \rightarrow f)) \right)$	$\{z \rightarrow f \mid z \in Must(A)(x)\}$
$x = new$	\emptyset	$\{x\}$
$x = null$	\emptyset	$\{x\}$
other	\emptyset	\emptyset



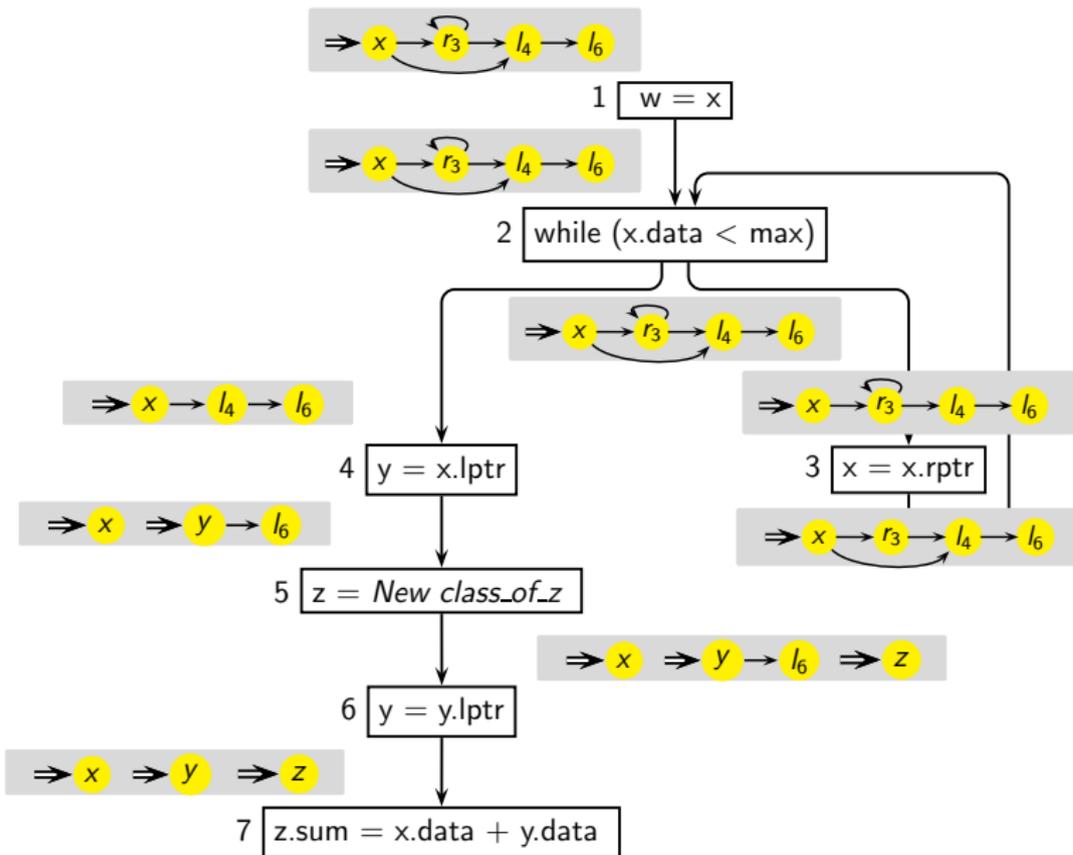
Liveness Analysis of Example Program: 1st Iteration



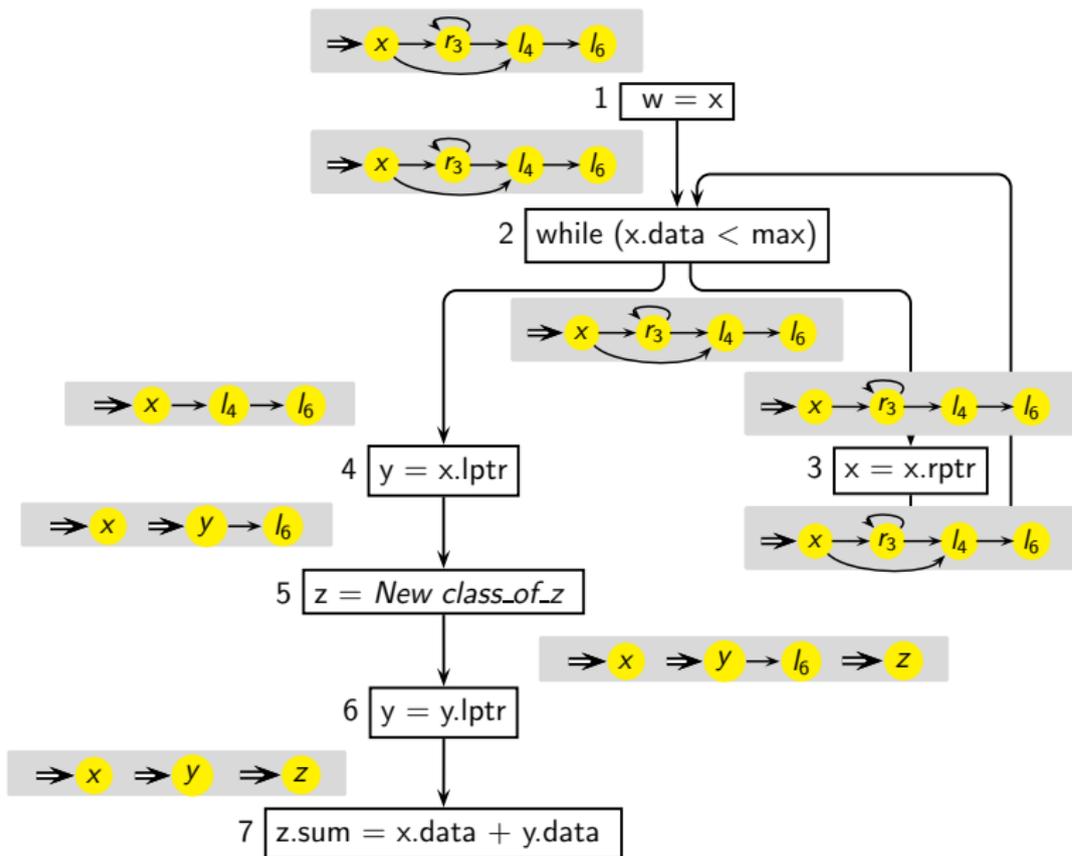
Liveness Analysis of Example Program: 2nd Iteration



Liveness Analysis of Example Program: 3rd Iteration



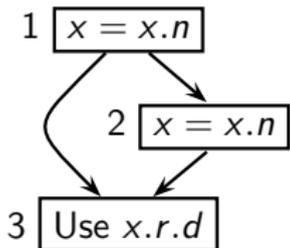
Liveness Analysis of Example Program: 4th Iteration



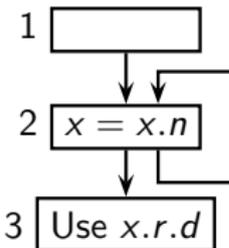
Tutorial Problem for Explicit Liveness (1)

Construct access graphs at the entry of block 1 for the following programs

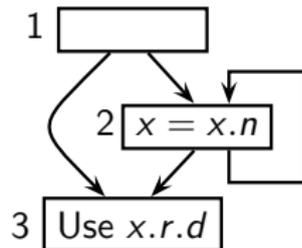
A



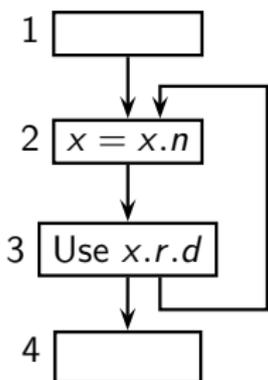
B



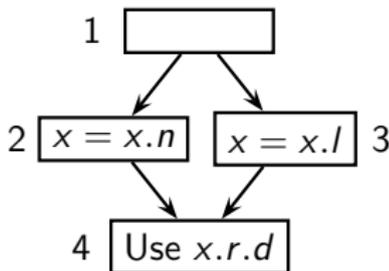
C



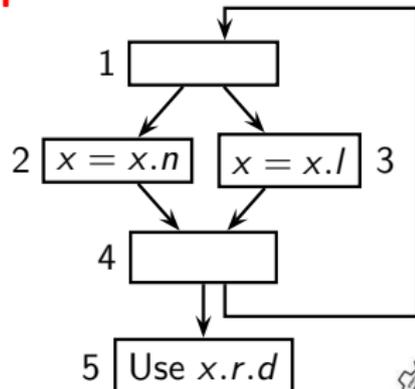
D



E



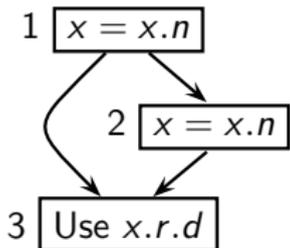
F



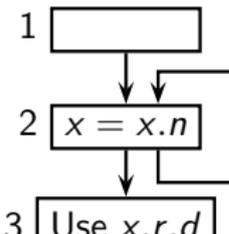
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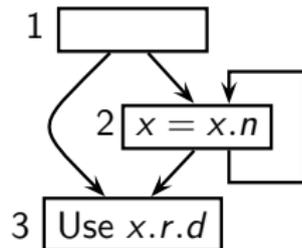
A



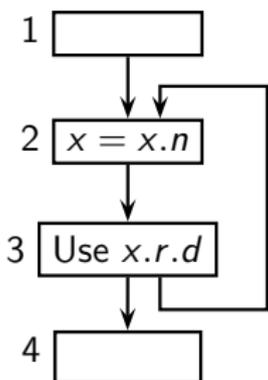
B



C

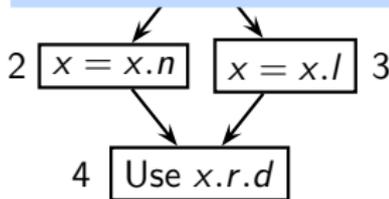


D

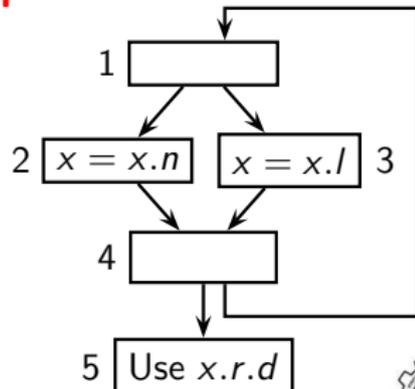


E

Why are the access graphs for programs B and D identical?



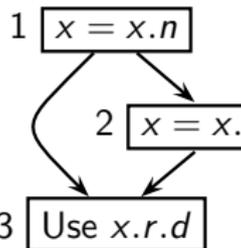
F



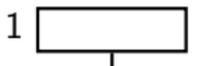
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Construct access graphs at the entry of block 1 for the following programs

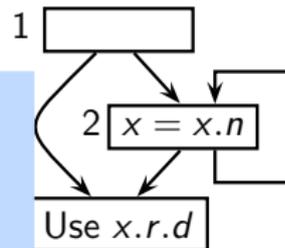
A



B



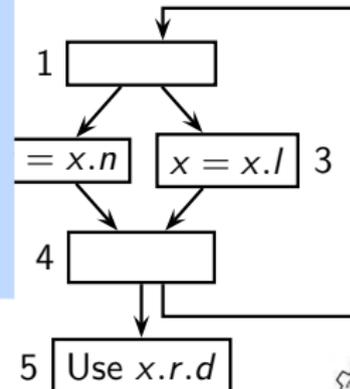
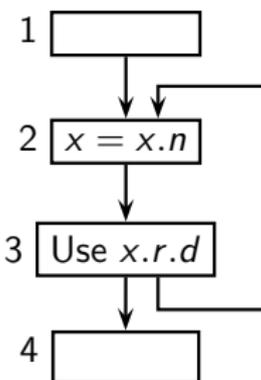
C



The final magic!!

Rotate each picture
anti-clockwise by 90° and
compare it with its access graph

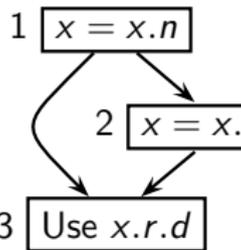
D



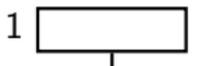
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Construct access graphs at the entry of block 1 for the following programs

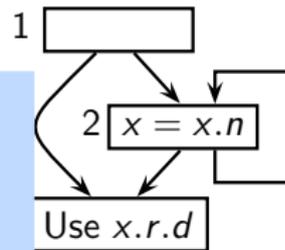
A



B



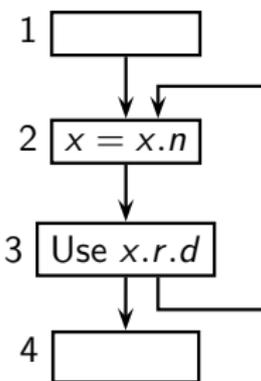
C



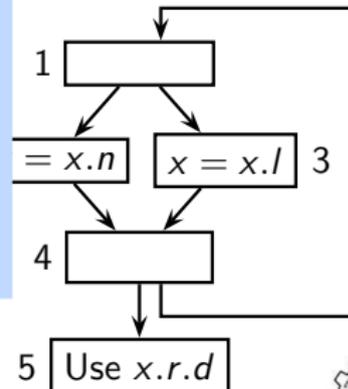
The final magic!!

Rotate each picture
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compare it with its access graph

D

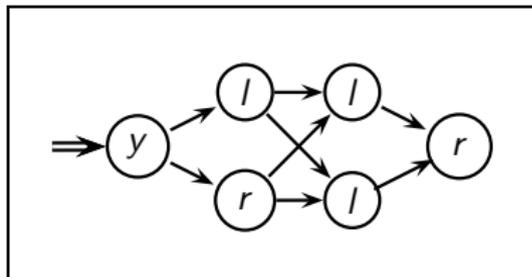
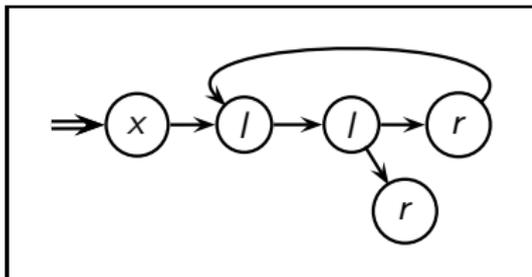


*The structure of access graph of
variable x is identical to the
control flow structure between
pointer assignments of x*



Tutorial Problem for Explicit Liveness (2)

- Unfortunately the student who constructed these access graphs forgot to attach statement numbers as subscripts to node labels and has misplaced the programs which gave rise to these graphs
- Please help her by constructing CFGs for which these access graphs represent explicit liveness at some program point in the CFGs



Tutorial Problem for Explicit Liveness (3)

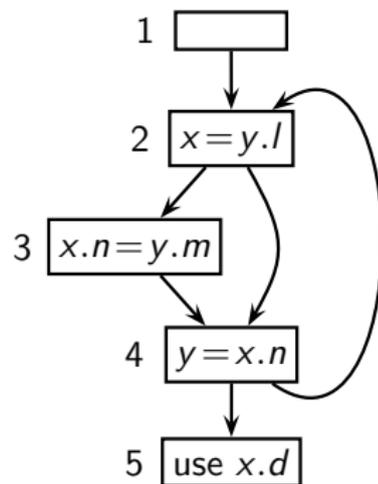
- Compute explicit liveness for the program.
- Are the following access paths live at node 1? Show the corresponding execution sequence of statements

P1 : $y \rightarrow m \rightarrow l$

P2 : $y \rightarrow l \rightarrow n \rightarrow m$

P3 : $y \rightarrow l \rightarrow n \rightarrow l$

P4 : $y \rightarrow n \rightarrow l \rightarrow n$



Which Access Paths Can be Nullified?

- Consider extensions of accessible paths for nullification.

Let ρ be accessible at p (i.e. available or anticipable)
for each reference field f of the object pointed to by ρ
if $\rho \rightarrow f$ is not live at p **then**
 Insert $\rho \rightarrow f = \text{null}$ at p subject to profitability

- For simple access paths, ρ is empty and f is the root variable name.



Which Access Paths Can be Nullified?

Can be safely
dereferenced

- Consider extensions of accessible paths for nullification.

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Which Access Paths Can be Nullified?

Can be safely
dereferenced

Consider link
aliases at p

- Consider extensions of accessible paths for nullification.

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for each reference field f of the object pointed to by ρ
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Which Access Paths Can be Nullified?

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if $\rho \rightarrow f$ is not live at p **then**
 Insert $\rho \rightarrow f = \text{null}$ at p subject to profitability

- For simple access paths, ρ is empty and f is the root variable name.

Cannot be hoisted and is
not redefined at p



Availability and Anticipability Analyses

- ρ is **available** at program point p if the target of each prefix of ρ is guaranteed to be created along every control flow path reaching p .
- ρ is **anticipable** at program point p if the target of each prefix of ρ is guaranteed to be dereferenced along every control flow path starting at p .



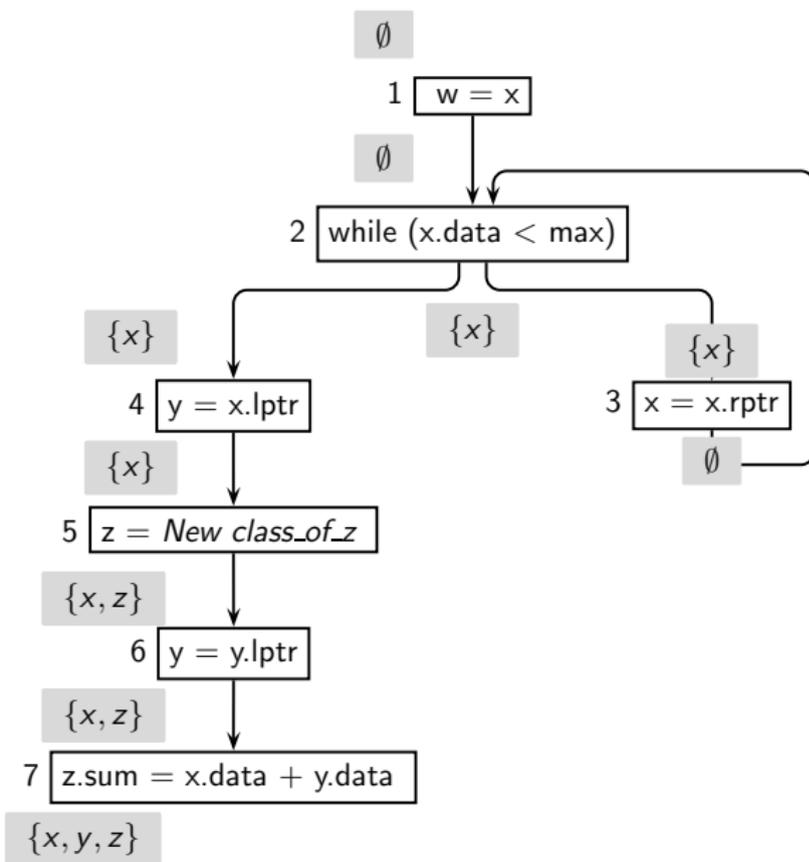
Availability and Anticipability Analyses

- ρ is **available** at program point p if the target of each prefix of ρ is guaranteed to be created along every control flow path reaching p .
- ρ is **anticipable** at program point p if the target of each prefix of ρ is guaranteed to be dereferenced along every control flow path starting at p .
- Finiteness.
 - ▶ An anticipable (available) access path must be anticipable (available) along every paths. Thus unbounded paths arising out of loops cannot be anticipable (available).
 - ▶ Due to “every control flow path nature”, computation of anticipable and available access paths uses \cap as the confluence. Thus the sets are bounded.

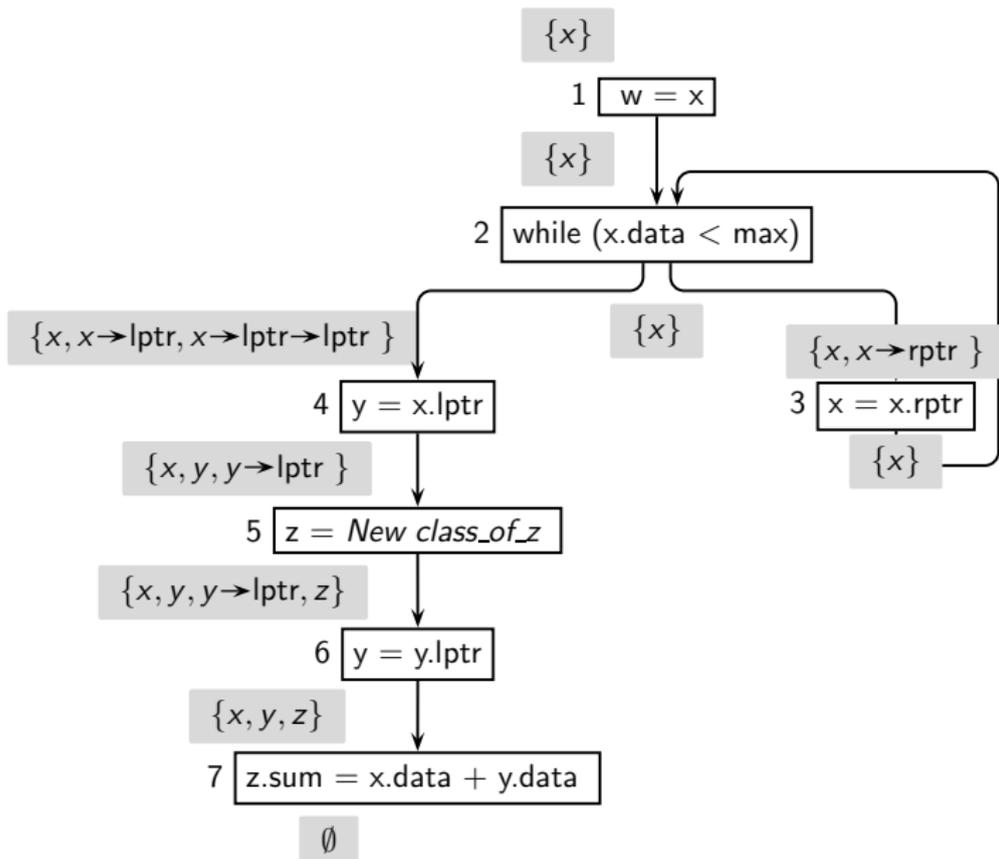
⇒ No need of access graphs.



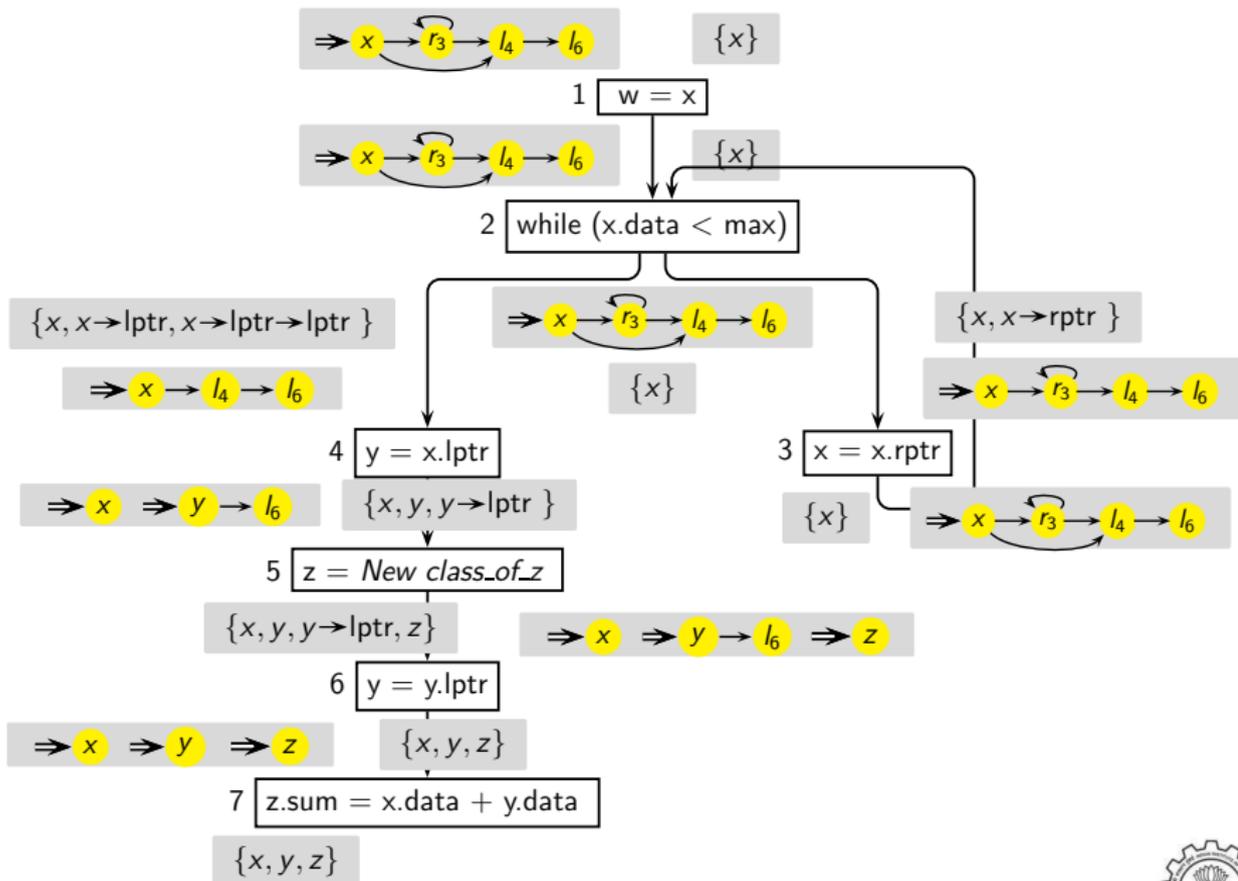
Availability Analysis of Example Program



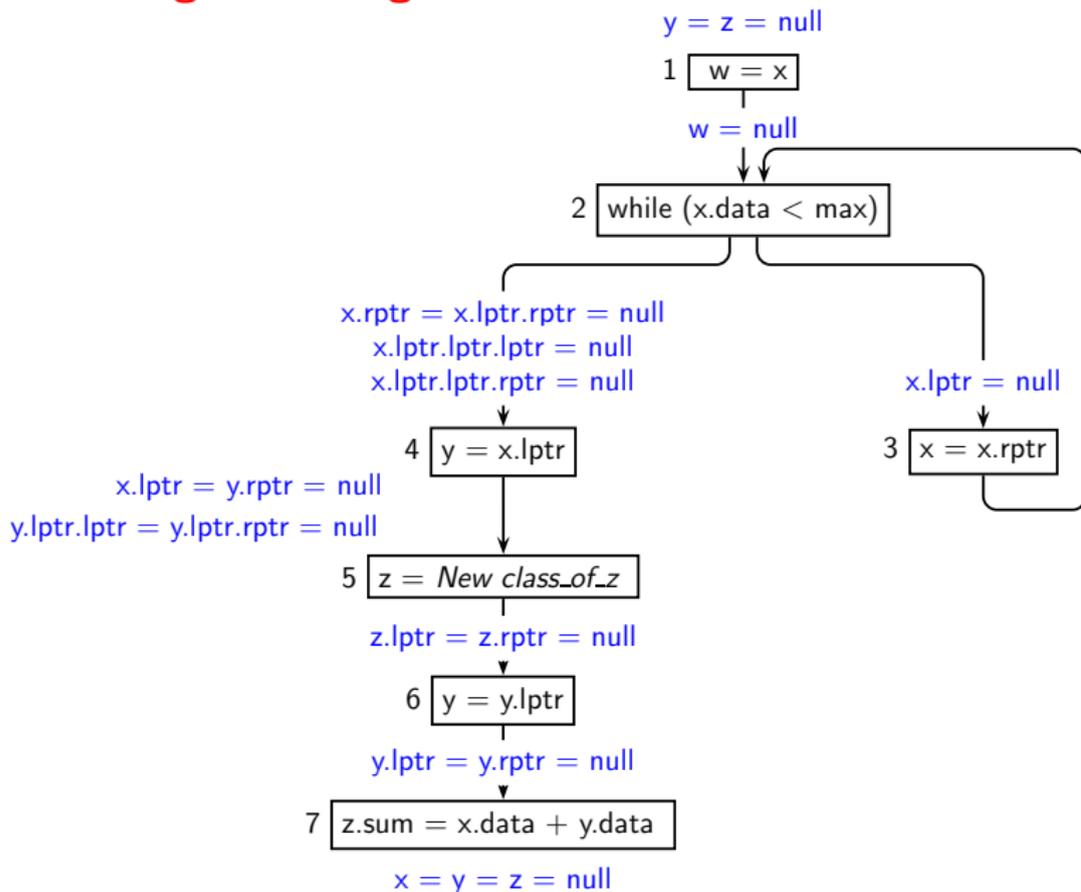
Anticipability Analysis of Example Program



Live and Accessible Paths



Creating null Assignments from Live and Accessible Paths



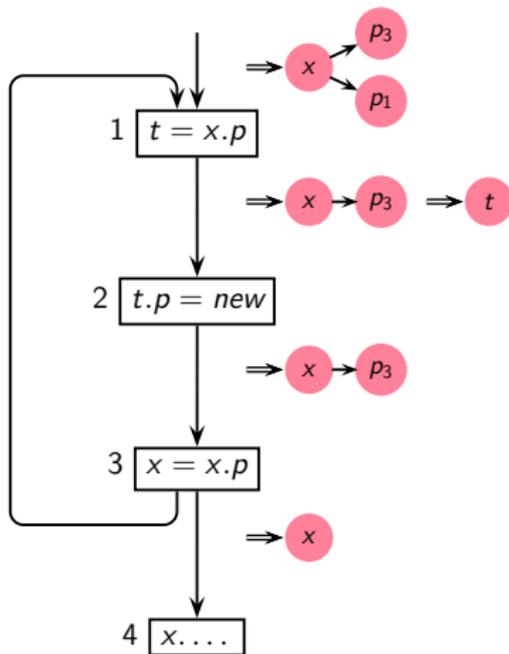
The Resulting Program

```

1
    y = z = null
w = x
    w = null
2 while (x.data < max)
    {
3     x = x.rptr    }
    x.rptr = x.lptr.rptr = null
    x.lptr.lptr.lptr = null
    x.lptr.lptr.rptr = null
4 y = x.lptr
    x.lptr = y.rptr = null
    y.lptr.lptr = y.lptr.rptr = null
5 z = New class_of_z
    z.lptr = z.rptr = null
6 y = y.lptr
    y.lptr = y.rptr = null
7 z.sum = x.data + y.data
    x = y = z = null
```



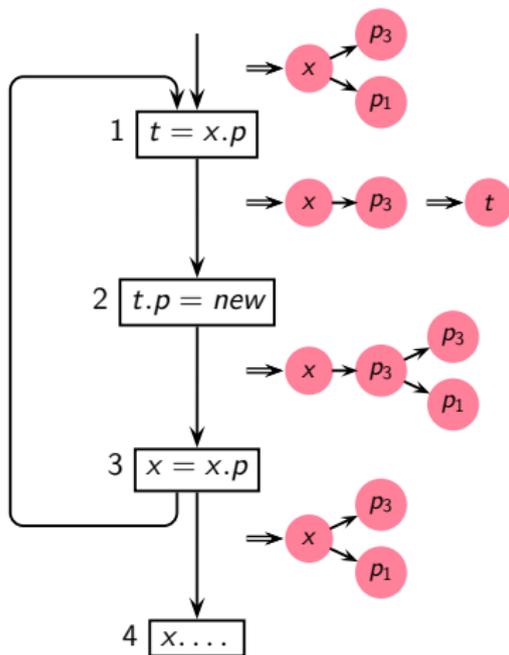
Overapproximation Caused by Our Summarization



- The program allocates $x \rightarrow p$ in one iteration and uses it in the next



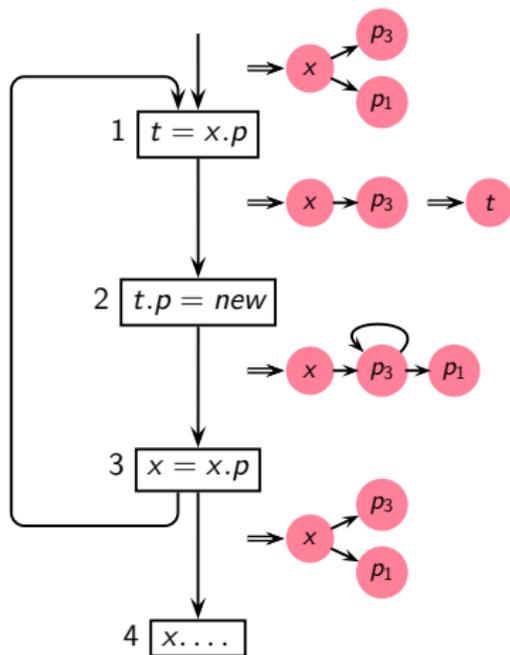
Overapproximation Caused by Our Summarization



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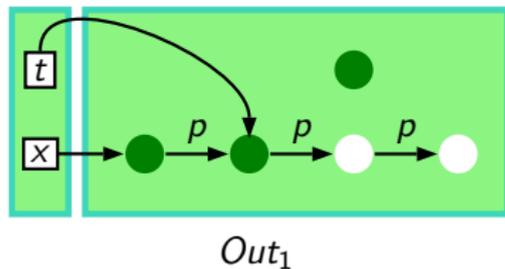
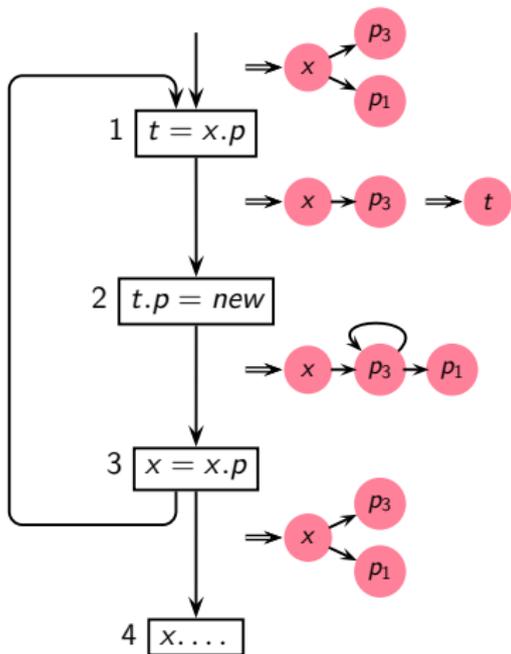
Overapproximation Caused by Our Summarization



- The program allocates $x \rightarrow p$ in one iteration and uses it in the next
- *Only $x \rightarrow p \rightarrow p$ is live at Out_2*

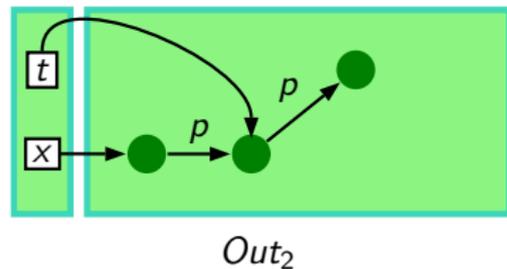
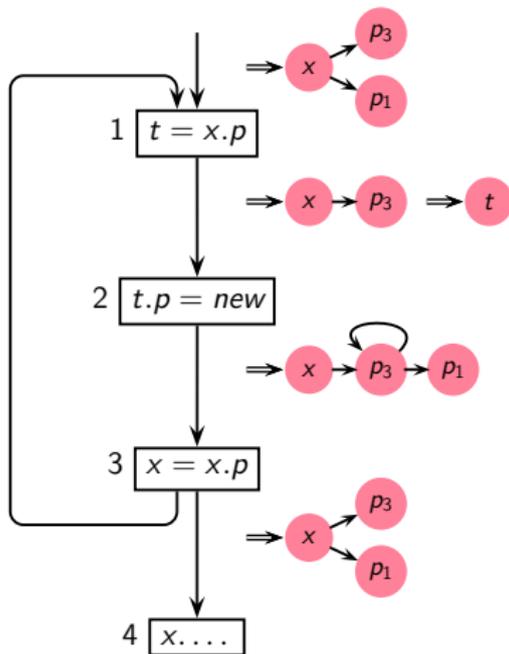


Overapproximation Caused by Our Summarization



- The program allocates $x \rightarrow p$ in one iteration and uses it in the next
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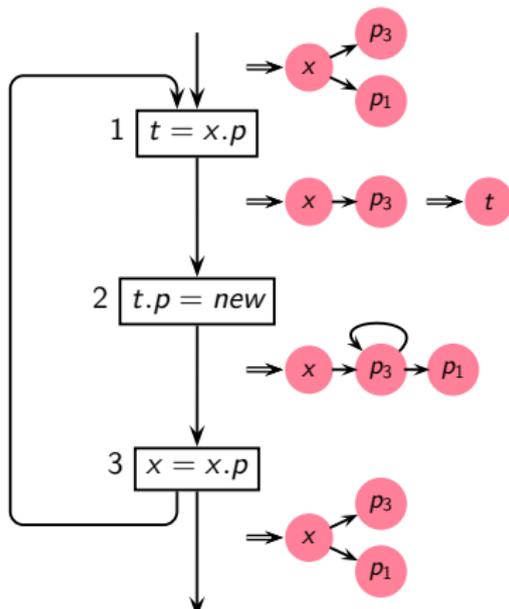
Overapproximation Caused by Our Summarization



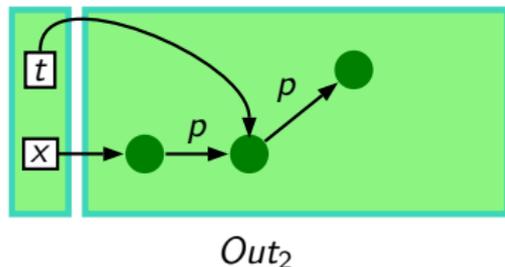
- The program allocates $x \rightarrow p$ in one iteration and uses it in the next
- *Only $x \rightarrow p \rightarrow p$ is live at Out_2*
- $x \rightarrow p \rightarrow p$ is live at Out_2
 $x \rightarrow p \rightarrow p \rightarrow p$ is dead at Out_2
- First p used in statement 3
Second p used in statement 4
- Third p is reallocated



Overapproximation Caused by Our Summarization



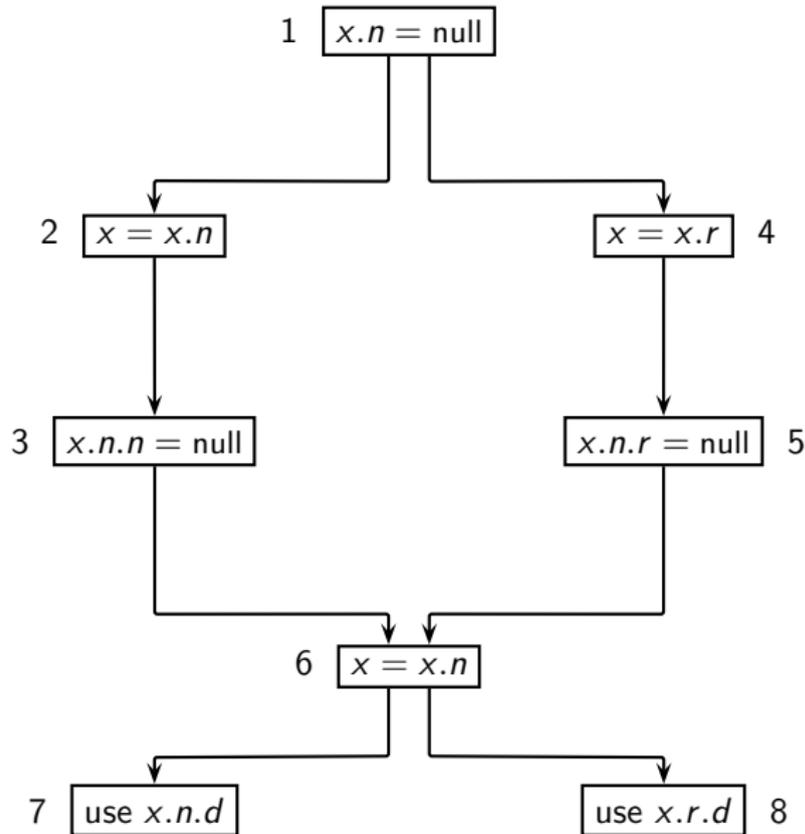
Second occurrence of a dereference does not necessarily mean an unbounded number of repetitions!



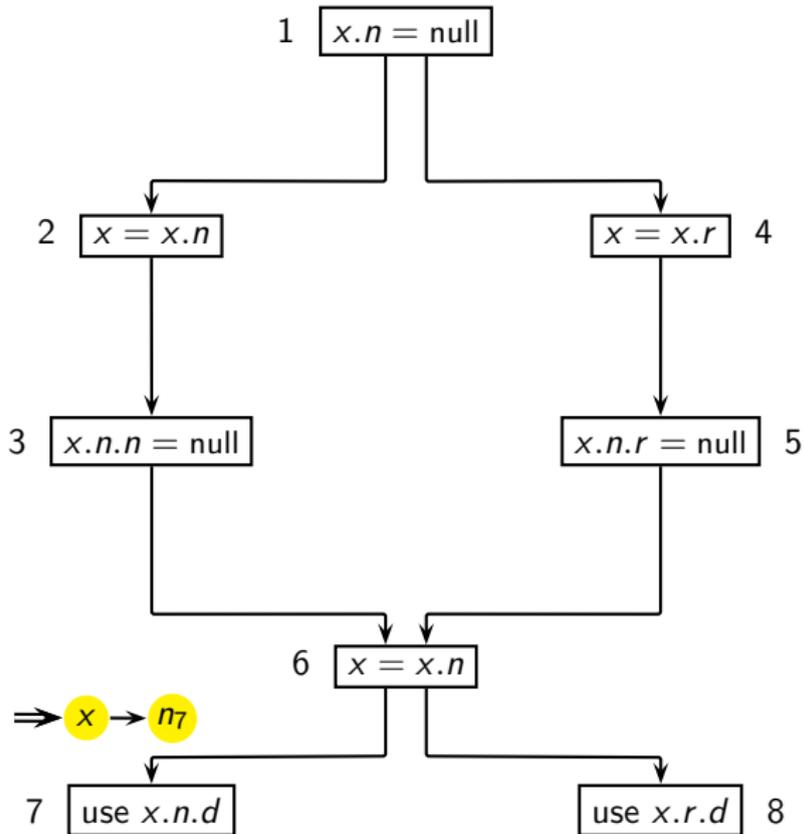
- The program allocates $x \rightarrow p$ in one iteration and uses it in the next
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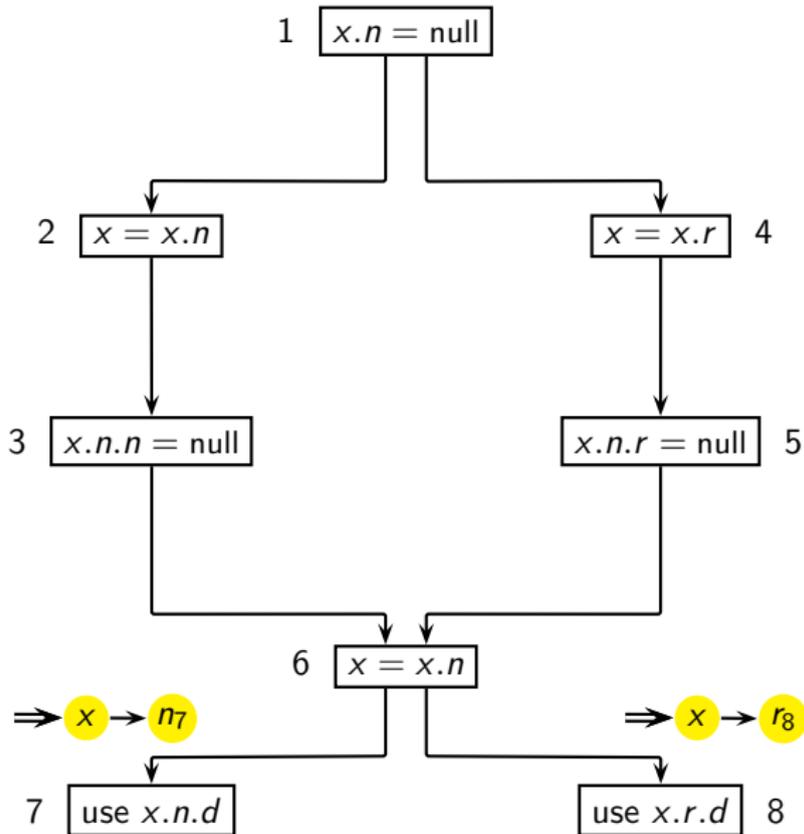
Non-Distributivity of Explicit Liveness Analysis



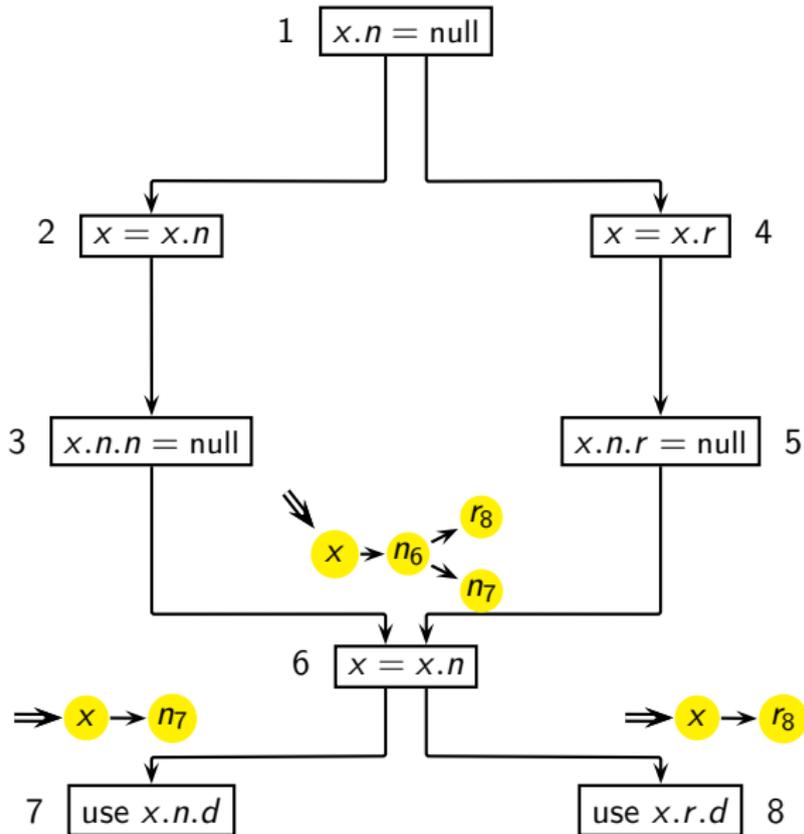
Non-Distributivity of Explicit Liveness Analysis



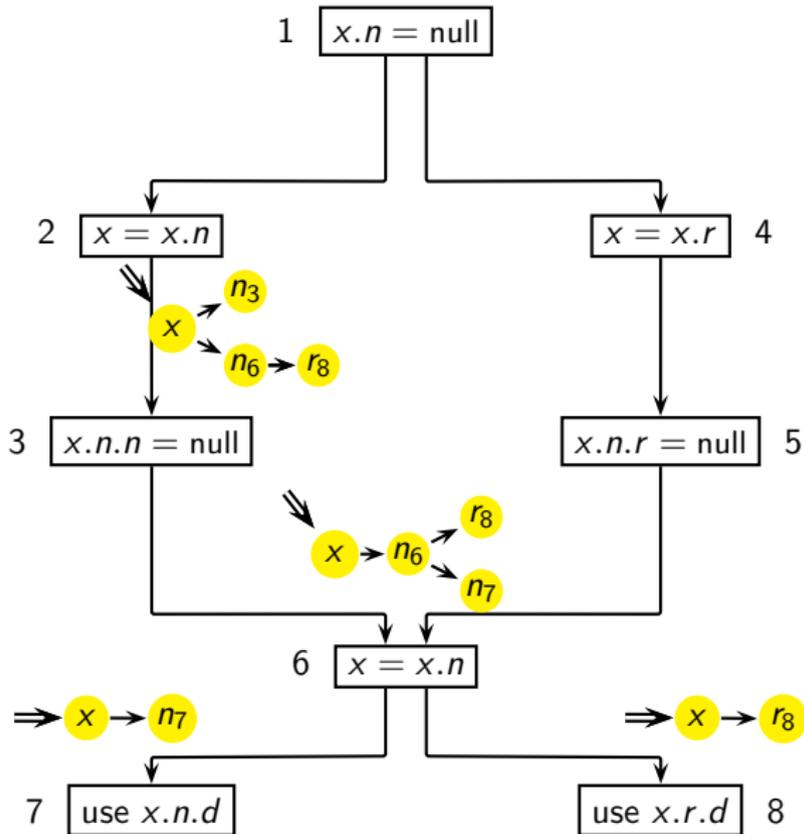
Non-Distributivity of Explicit Liveness Analysis



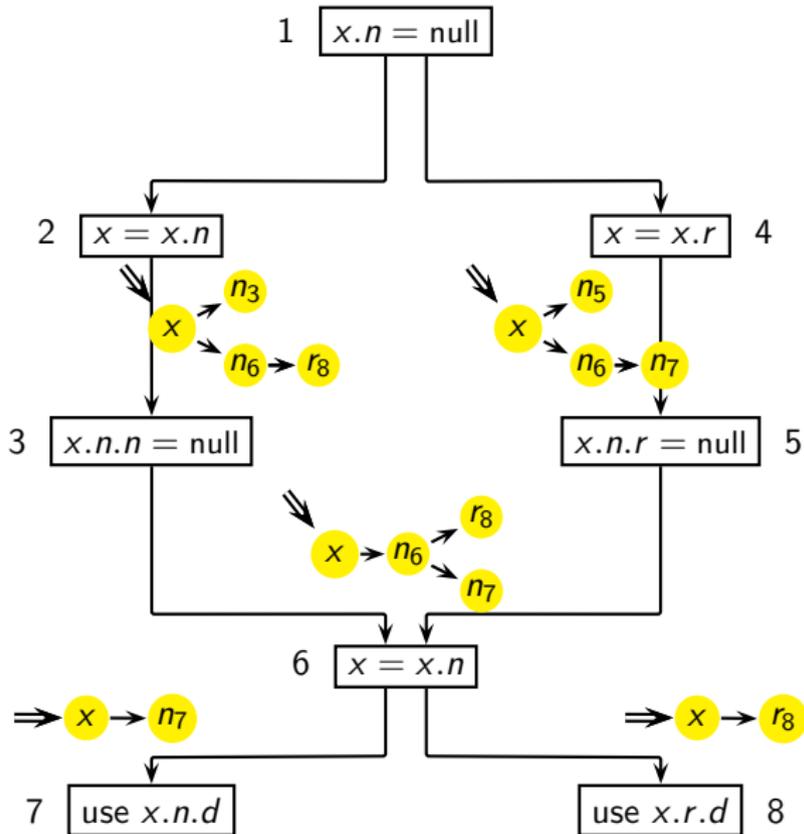
Non-Distributivity of Explicit Liveness Analysis



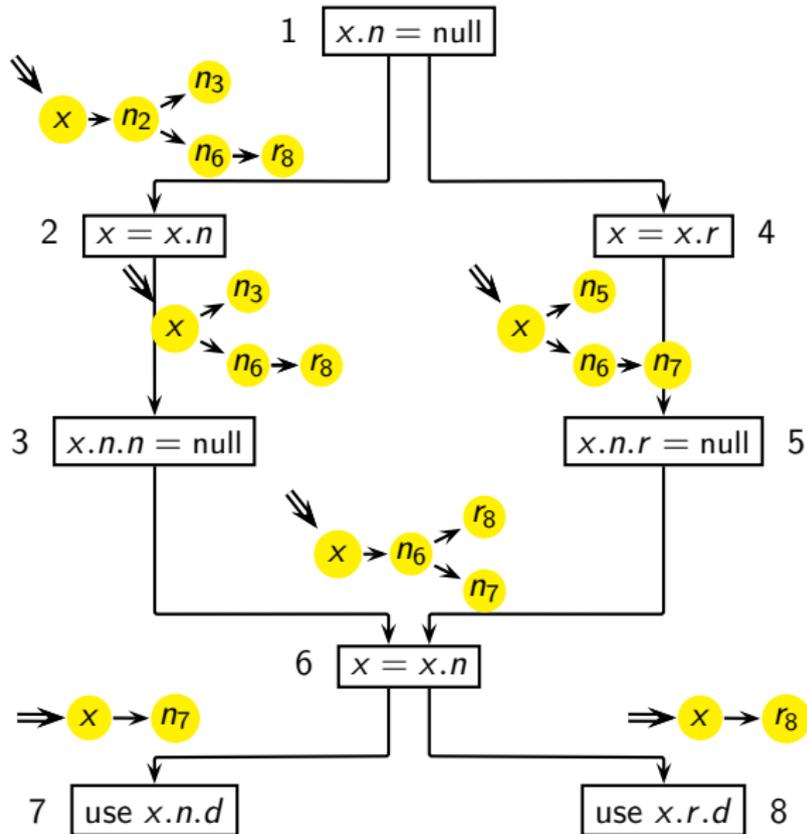
Non-Distributivity of Explicit Liveness Analysis



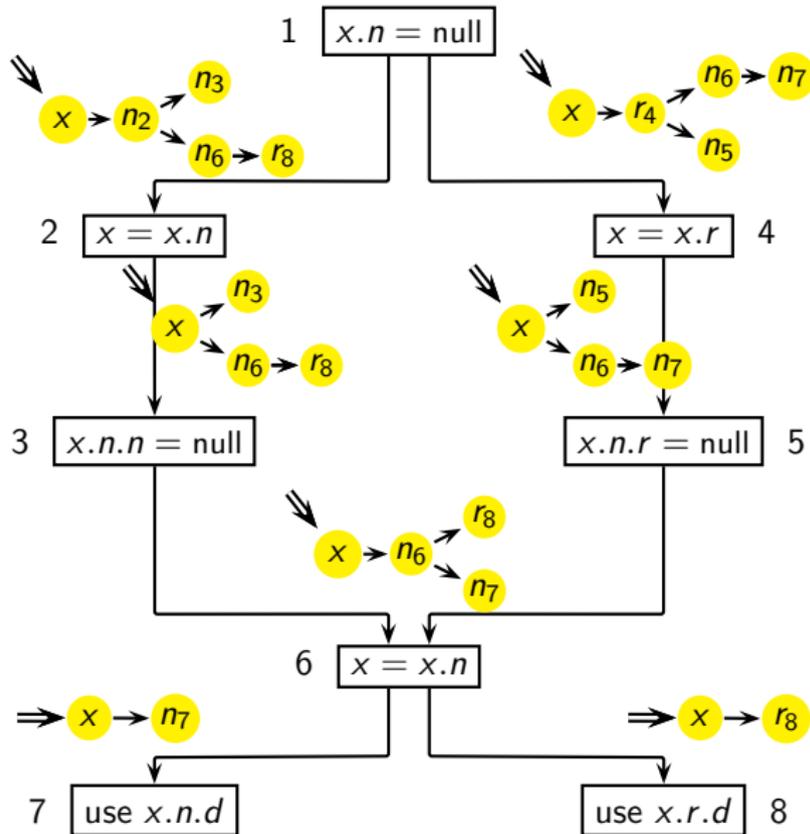
Non-Distributivity of Explicit Liveness Analysis



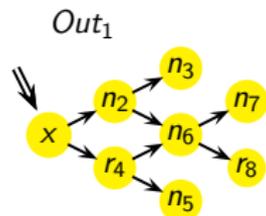
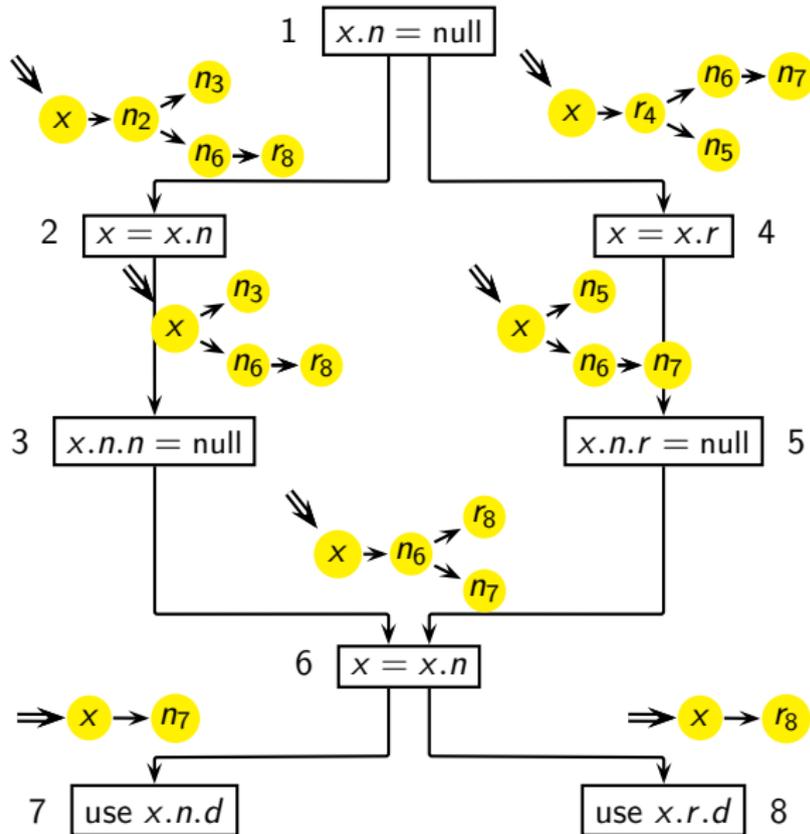
Non-Distributivity of Explicit Liveness Analysis



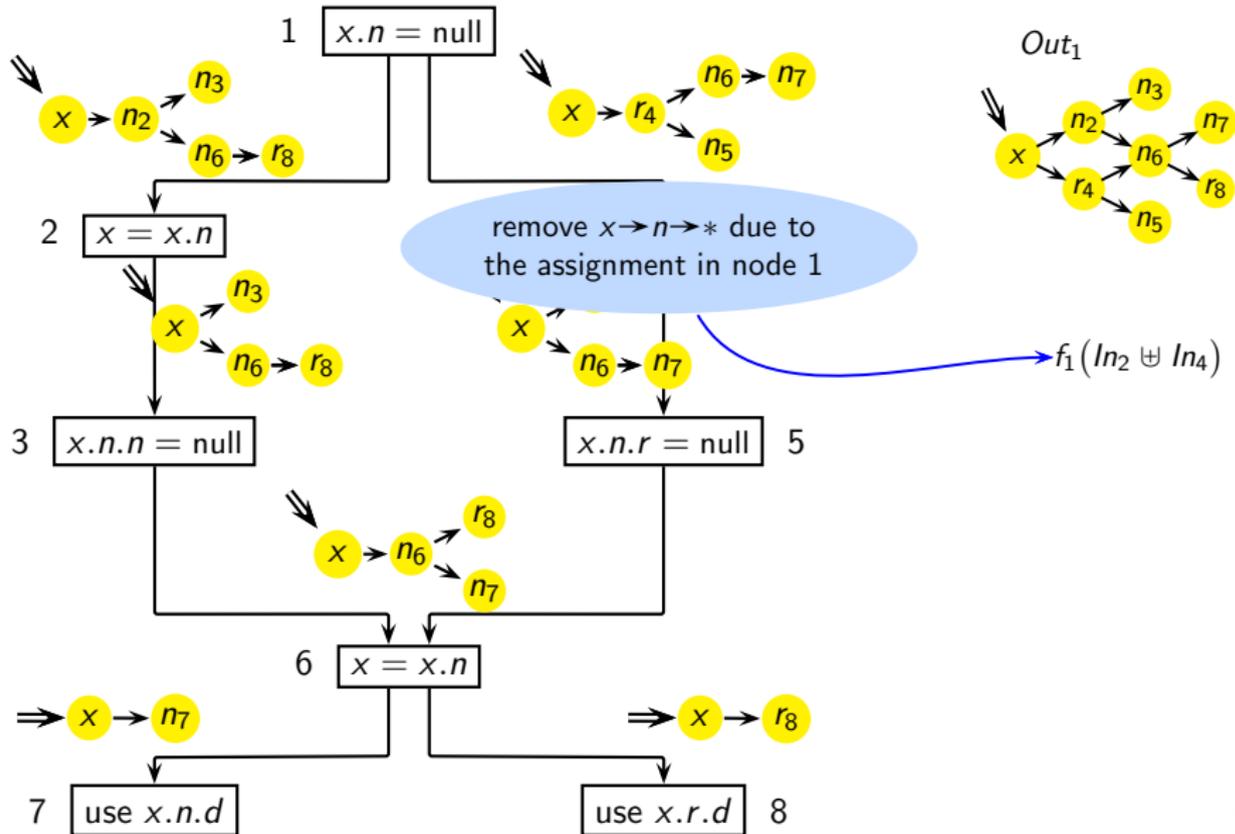
Non-Distributivity of Explicit Liveness Analysis



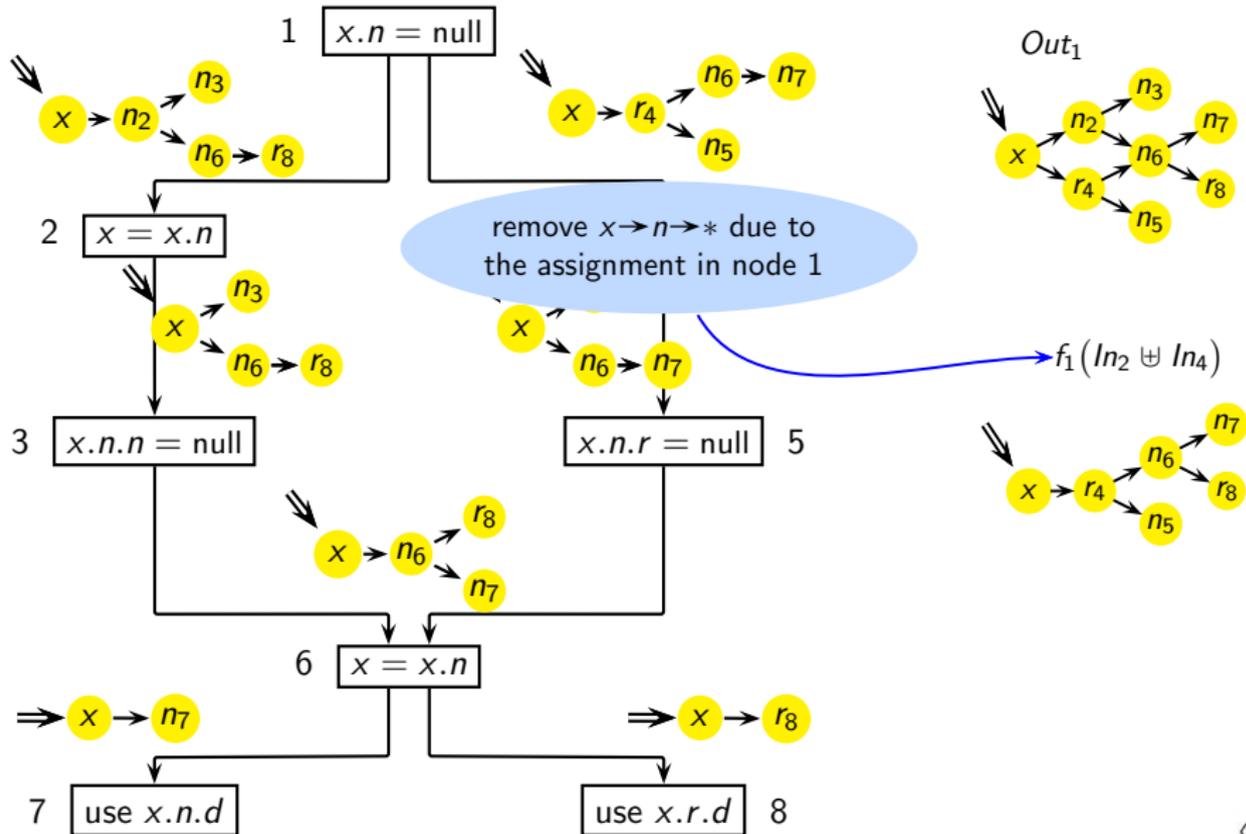
Non-Distributivity of Explicit Liveness Analysis



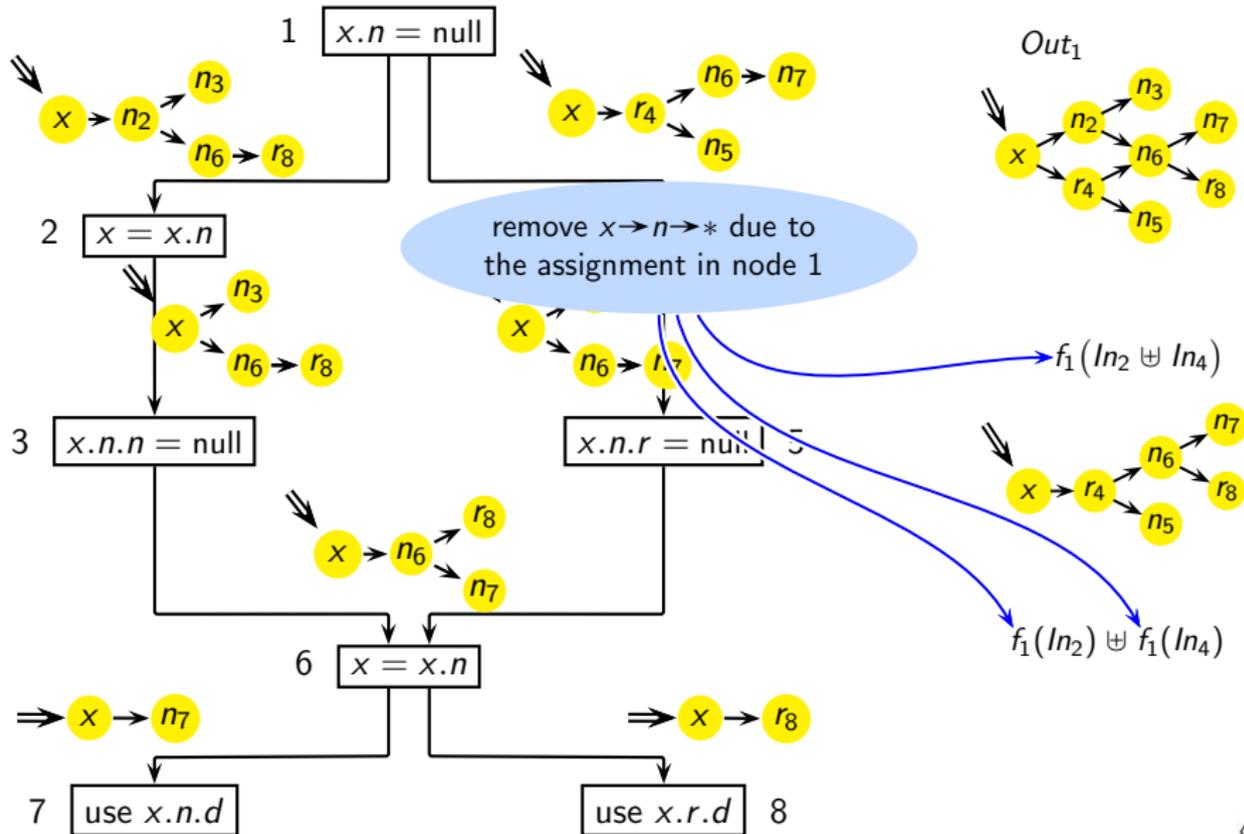
Non-Distributivity of Explicit Liveness Analysis



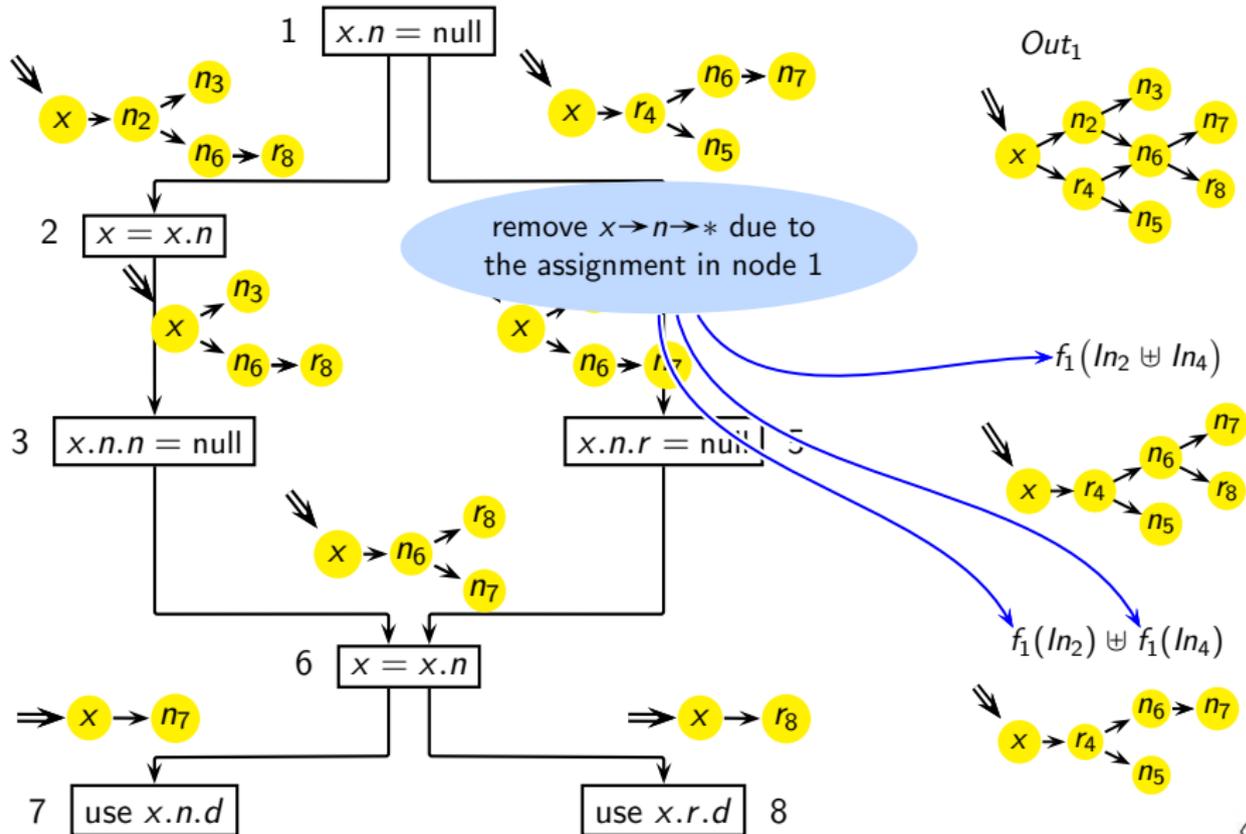
Non-Distributivity of Explicit Liveness Analysis



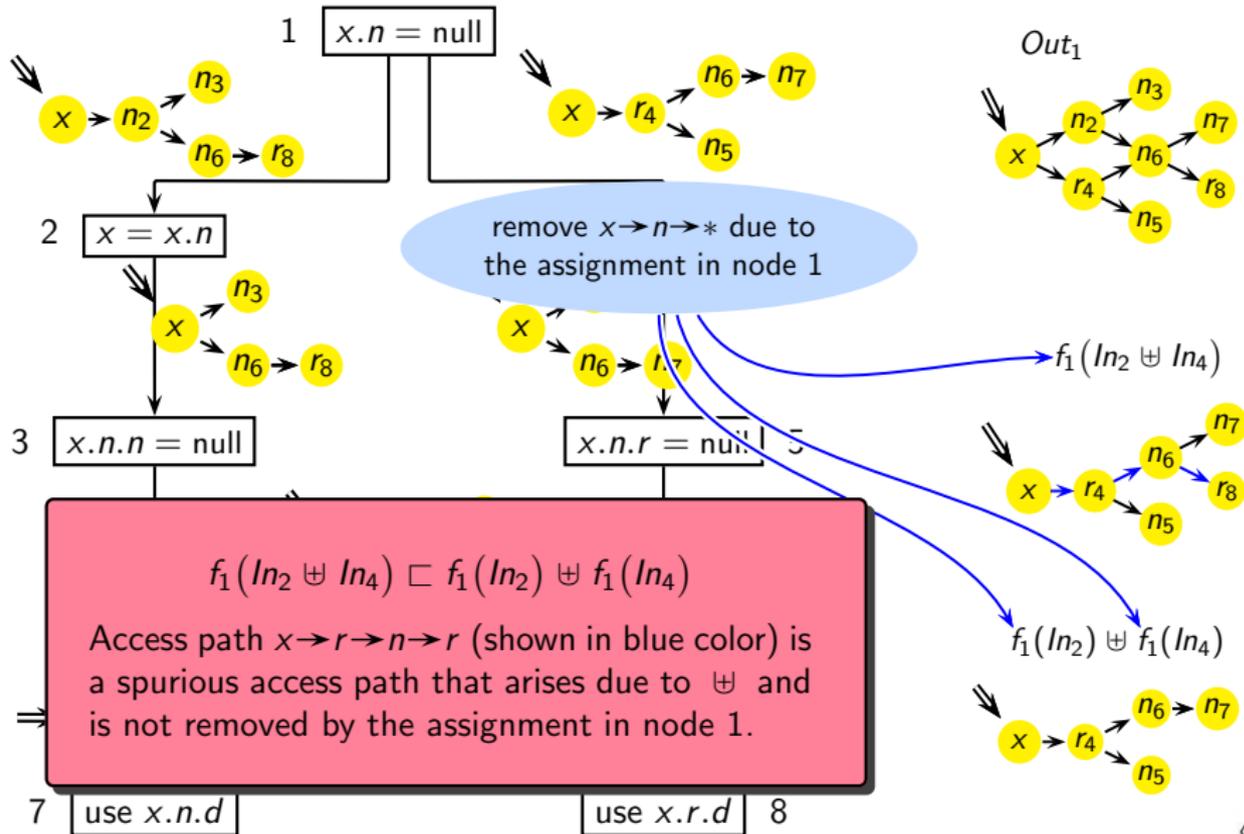
Non-Distributivity of Explicit Liveness Analysis



Non-Distributivity of Explicit Liveness Analysis



Non-Distributivity of Explicit Liveness Analysis



Issues Not Covered

- Precision of information
 - ▶ Cyclic Data Structures
 - ▶ Eliminating Redundant null Assignments
- Properties of Data Flow Analysis:
Monotonicity, Boundedness, Complexity
- Interprocedural Analysis
- Extensions for C/C++
- Formulation for functional languages
- Issues that need to be researched: Good alias analysis of heap



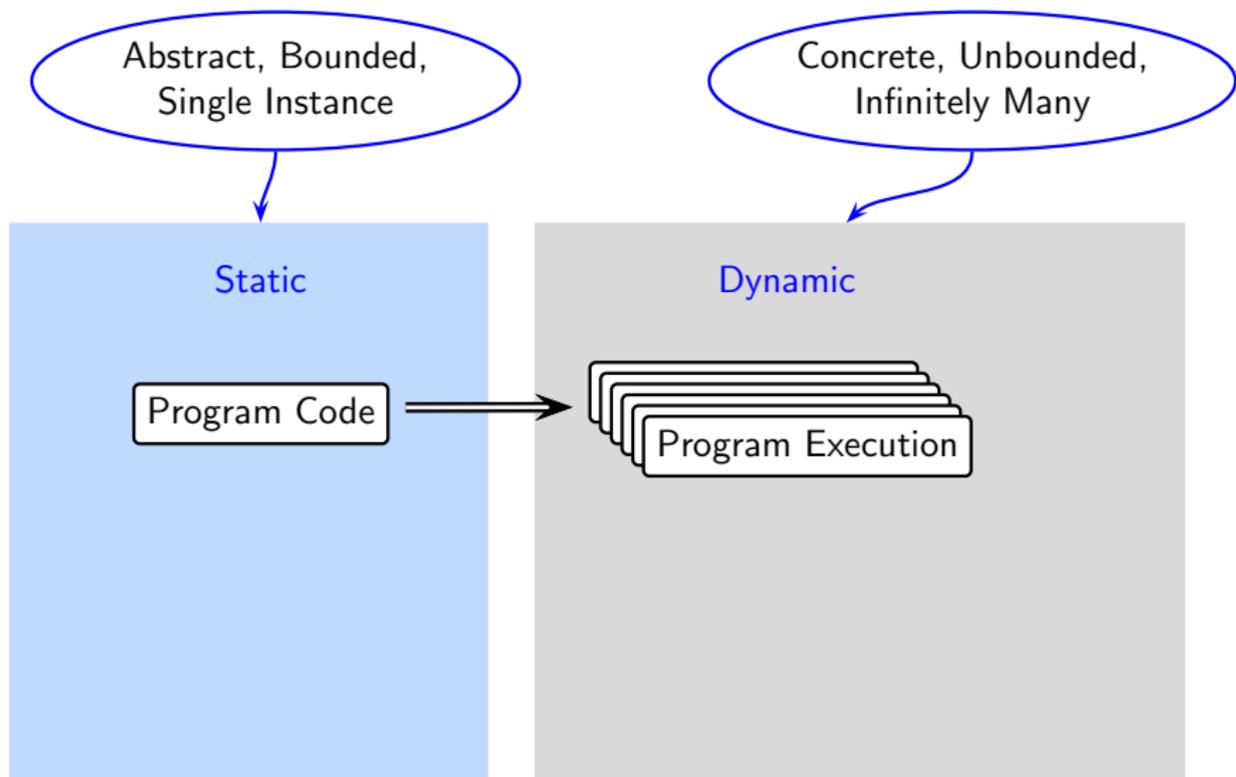
BTW, What is Static Analysis of Heap?

Static

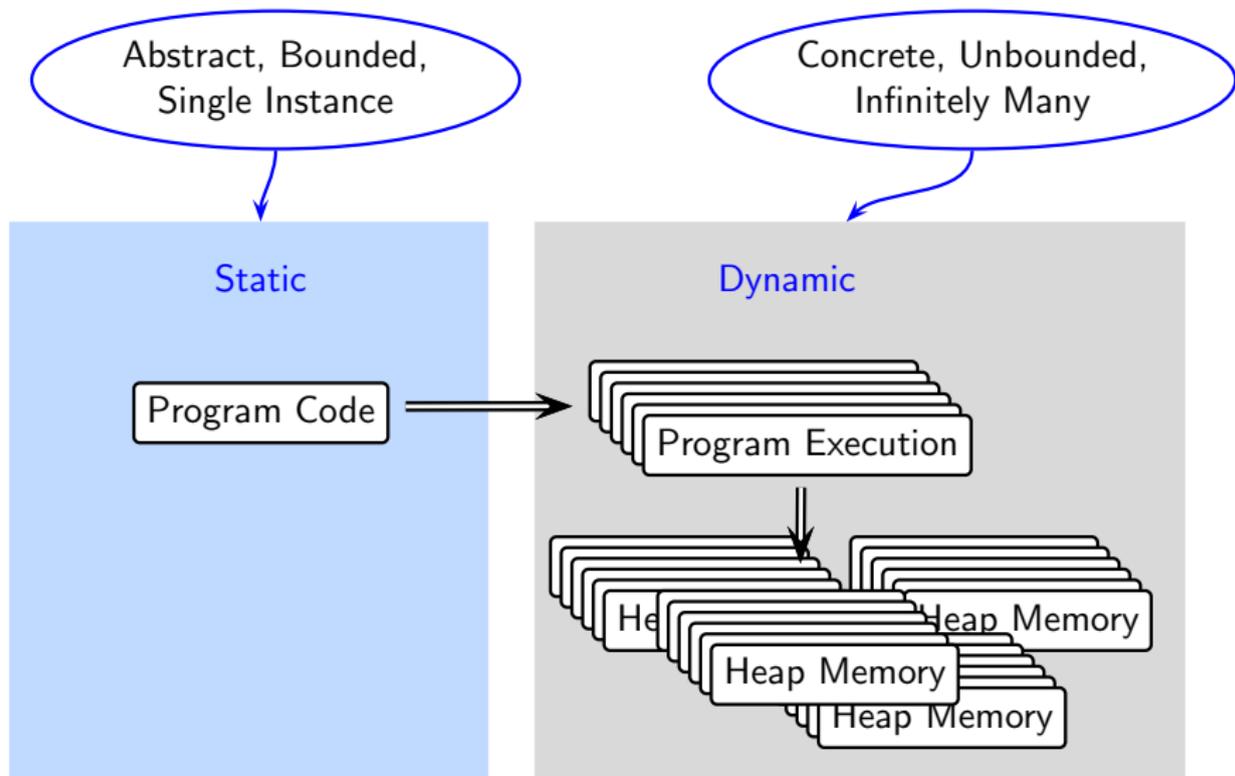
Dynamic



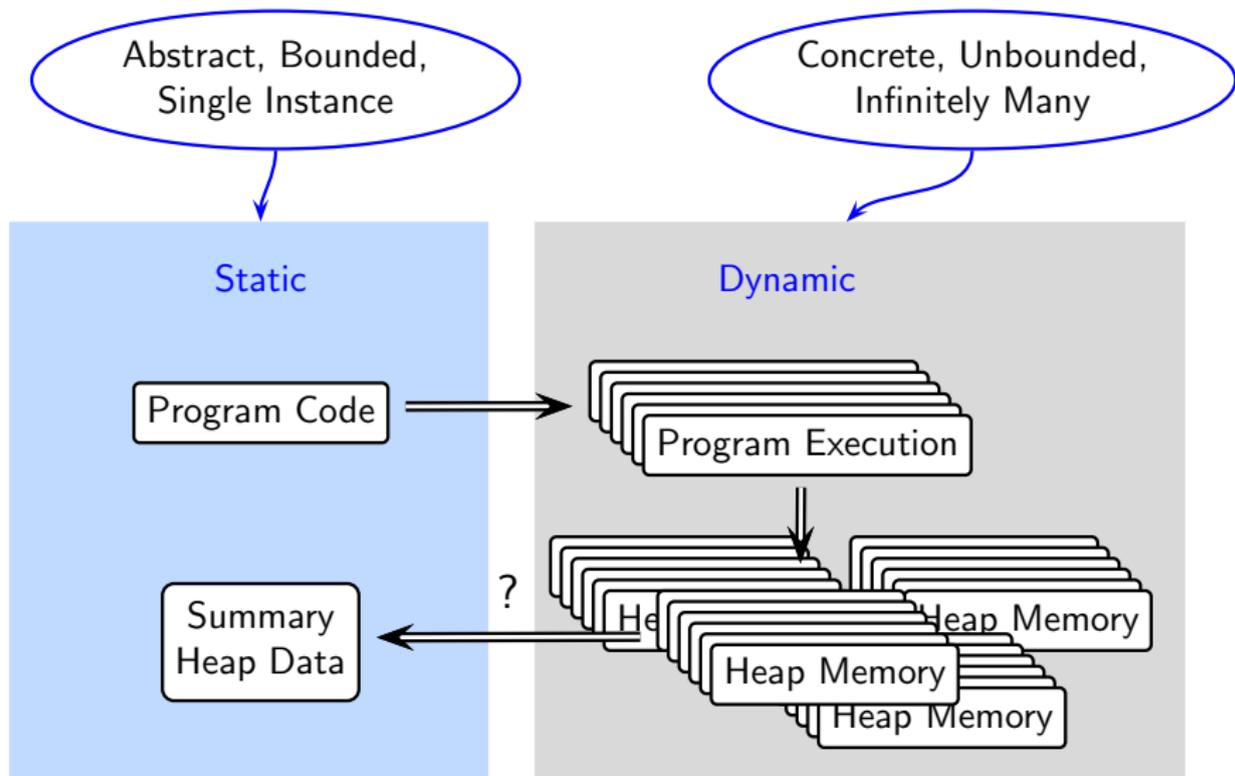
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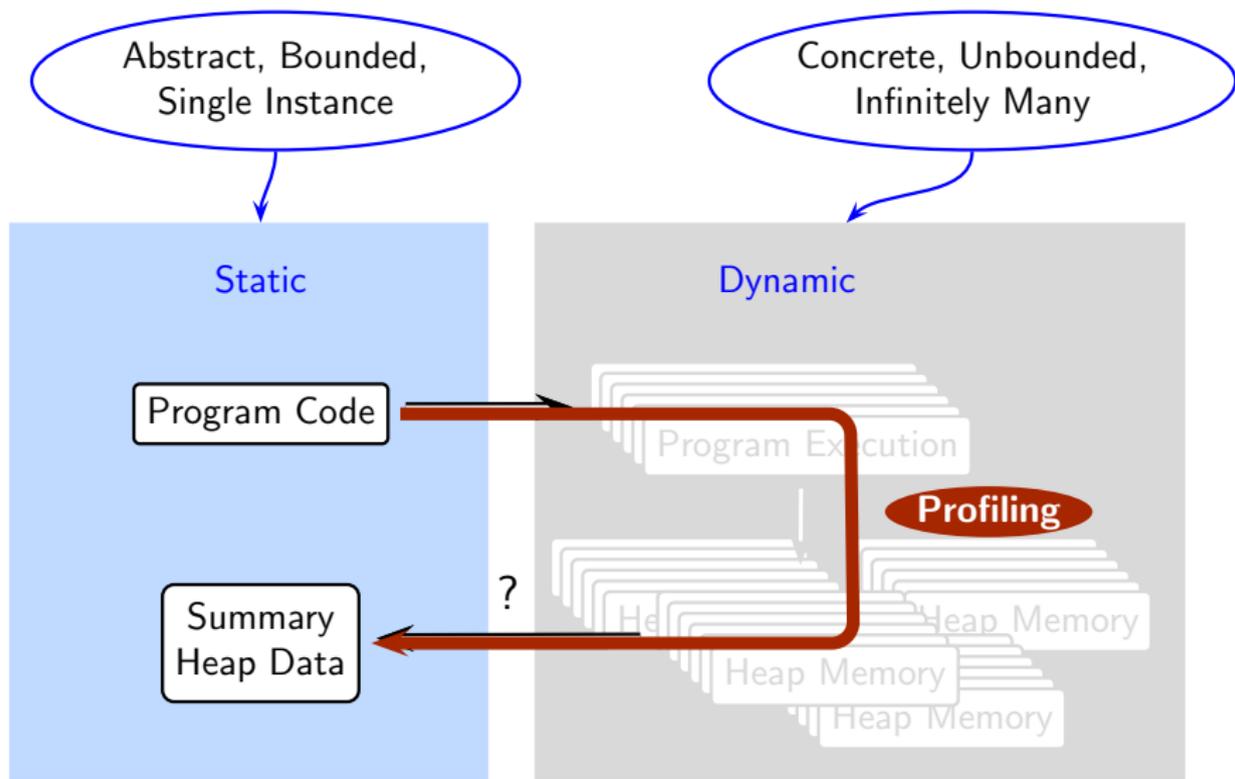
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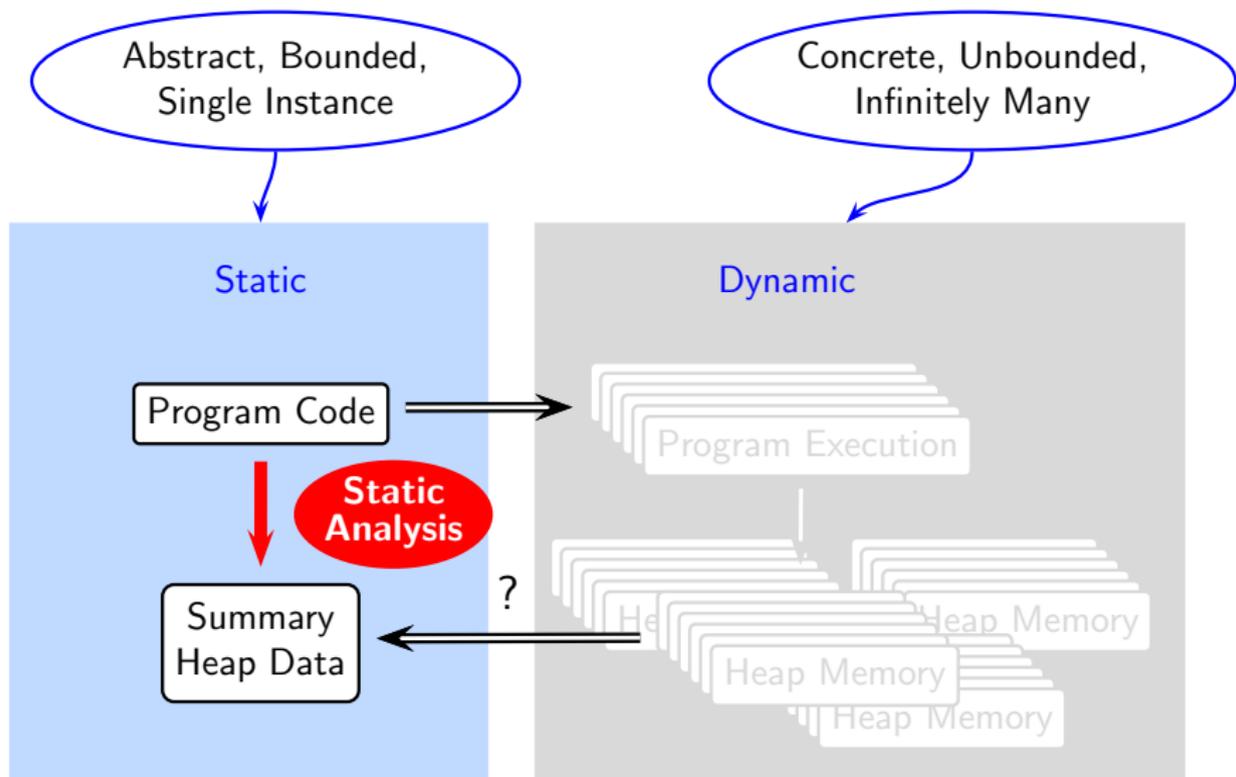
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Conclusions

- Unbounded information can be summarized using interesting insights
 - ▶ Contrary to popular perception, heap structure is not arbitrary
 - Heap manipulations consist of repeating patterns which bear a close resemblance to program structure*

Analysis of heap data is possible despite the fact that the mappings between access expressions and l-values keep changing

